MULTIPLE CHOICE QUESTIONS

The central processing unit has a sub-division of----- *

○ execution unit and memory unit

◉ execution unit and instruction unit

○ memory unit and ALU

○ memory unit and control unit

Size of segment in 8086 is ----------. *

64kb

The instruction that unconditionally transfers the control of execution to the specified address is *

○ IRET

○ RET

◉ JMP

○ CALL

If EDX:EAX is divided by EBX in which registers quotient and remainder will be strored. *

○ ax,dx

◉ eax,edx

○ ebx,ecx

○ rsi,rdi

The function of EU is ------------. *

○ Calculations

○ Encoding

○ Processing

◉ Decoding

In CMP CX,BX instruction if CX < BX then which flags are set. *

○ Parity flag

◉ Carry Flag and Sign Flag

○ Auxiliary Carry Flag

The register width used by the 32-bit addressing modes is-------------. *

○ 16 bits

○ 32 bits

○ 8 bits

◉ all of the mentioned

Which segment register is being used in the given instruction?MOV CX , SS: *

◉ None of the Above

○ Extra Segment Register (ES)

○ Code Segment Register (CS)

○ Stack Segment Register (SS)

The unit that increases the speed of all shift and rotate operations is------
--. *

O instruction unit

O execution unit

O memory management unit

● barrel shifter

pop cs is this instruction a legal instruction *

O Legal Instruction

● Illegal Instruction