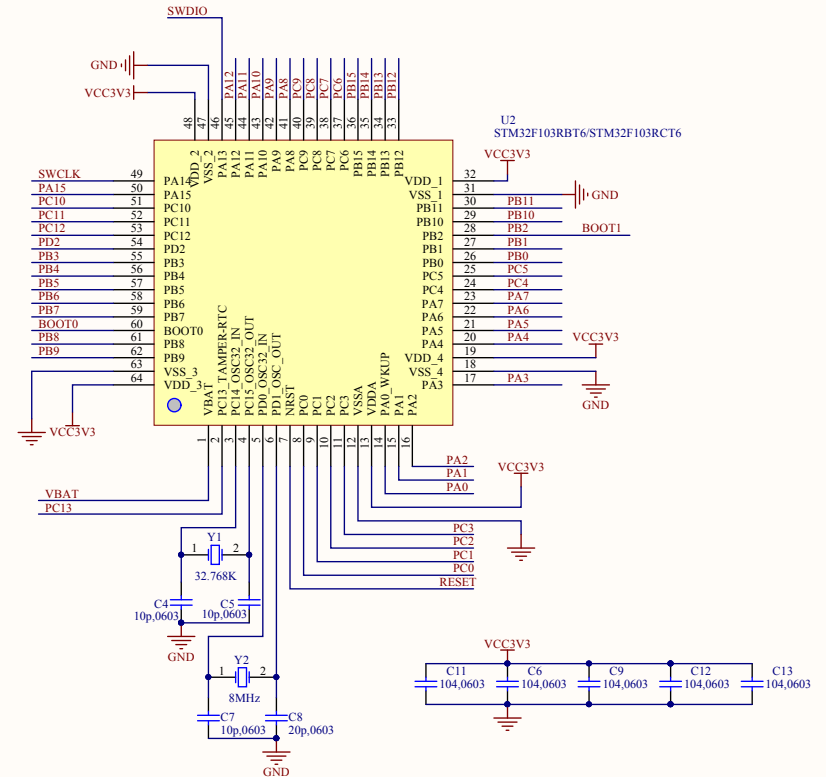
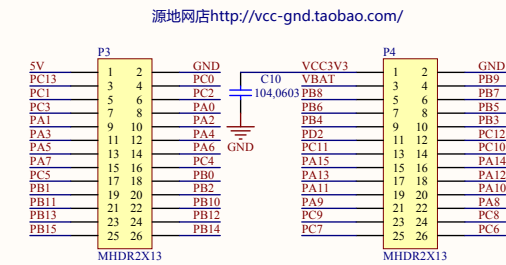
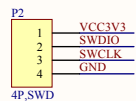
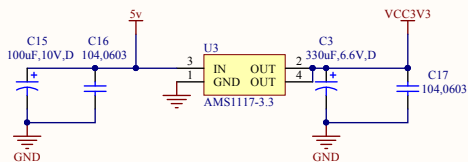
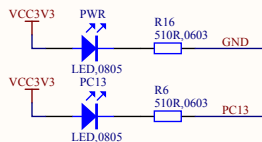


BOOT1	BOOT0	MODE
X	0	FLASH
1	1	SRAM
0	1	ISP



### STM32F103RCT6核心板原理图

Size A3	Number 源地工作室www.vcc-gnd.com	Revision
Date:	2015-07-19	Sheet of
File:	H:\源地资料整理\STM32F103RCT6	第 6 页 共 6 页 图 SCHDOC