Md Rubel Ahmed

CURRENT POSITION

Assistant Professor May. 2025 - Current

Computer Science, Louisiana Tech University

EDUCATION

University of South Florida, Tampa, FL

Aug. 2018 - Nov. 2023

Ph.D. in Computer Science and Engineering

Dissertation Title: "Machine Learning for Electronic Design Automation: Specification Mining and High-Level Synthesis."

Thesis Supervisor: Dr. Hao Zheng M.S. in Computer Engineering

Khulna University of Engineering and Technology, Bangladesh

Mar. 2013 - Apr. 2017

B.Sc. in Computer Science and Engineering

RESEARCH FOCUS

I have contributed to 20 publications in leading venues, including TCAD, ICCAD, MLCAD, and AAAI, spanning following areas:

- Electronic Design Automation
- Computer Architecture
- Computer Vision

PEER REVIEWED PUBLICATIONS

- 20. [MLCAD'25] Ahmed, M. R., Nur, S. N., & Ewetz, R. "Context-Enhanced Architectural Specification Generation for SoC Designs" 7th ACM/IEEE International Symposium on Machine Learning for CAD 2025.
- 19. [AISTATS'25] Walker, C., Ahmed, M. R., Kumar, S., & Ewetz, R. "Explaining ViTs Using Information Flow." The 28th International Conference on Artificial Intelligence and Statistics 2025.
- 18. [WACV'25] Rahat, F., Hossain, M. S., Ahmed, M. R., Jha, S. K., & Ewetz, R. "Data Augmentation for Image Classification using Generative AI." IEEE/CVF Winter Conference on Applications of Computer Vision 2025.
- 17. [ICAA'24] English, W., Simon, D., Ahmed, M. R., Jha, S., & Ewetz, R. "Neuro-Symbolic Program Synthesis for Multi-Hop Natural Language Navigation." 3rd International Conference on Assured Autonomy.
- 16. [ICMLA'24] Rahat, F., Shifat, M., Ahmed, M. R., & Ewetz, R. "CLE: Context-Aware Local Explanations for High Dimensional Tabular Data." 23rd IEEE International Conference on Machine Learning and Applications.
- 15. [ICCAD'24] Thijssen, S., Rashed, M. R. H., **Ahmed, M. R.**, Singireddy, S. S., Jha, S. K., & Ewetz, R. "Equivalence Checking for Flow-Based Computing using Iterative SAT Solving." International Conference on Computer Aided Design.
- 14. [TCAD'24] Md Rubel Ahmed, Bardia Nadimi, Hao Zheng, "AutoModel: Automatic Synthesis of Models from Communication Traces of SoC Designs", IEEE Transactions on Computer-Aided Design of Integrated Circuits & Systems.
- 13. [AAAI Workshop'23] M Shifat Hossain*, Md Rubel Ahmed*, Laura Pullum, Sumit Jha, Rickard Ewetz, "Neuro-Symbolic Representations of 3D Scenes using Universal Scene Description Language", Neuro-Symbolic Learning and Reasoning in the era of Large Language Models. (*equal contribution)
- 12. [MWSCAS'23] Md Rubel Ahmed, Toshiaki Koike-Akino, Kieran Parsons, Ye Wang, "Joint Software-Hardware Design for Green Al", 66^{th} International Midwest Symposium on Circuits and Systems.
- 11. [MWSCAS'23] Md Rubel Ahmed, Toshiaki Koike-Akino, Kieran Parsons, Ye Wang, "AutoHLS: Learning to Accelerate Design Space Exploration for HLS Designs", 66th International Midwest Symposium on Circuits and Systems.
- 10. [MWSCAS'23] Md Rubel Ahmed, Bardia Nadimi, Hao Zheng, "System-on-Chip Message Flow Mining with Masked-Language Models", 66th International Midwest Symposium on Circuits and Systems. (Student Best Paper Finalist)

- 9. $[\mathbf{SRF@ASP} \mathbf{DAC'23}]$ Md Rubel Ahmed, "Mining Message Flows from System-on-Chip Execution Traces", ACM SIGDA Student Research Forum at ASP-DAC.
- 8. [JBHI'22] Shuvo, Salman Sadiq; Symum, Hasan; Ahmed, Md Rubel; Yilmaz, Yasin; Zayas-Castro, Jose L, "Multi-Objective Reinforcement Learning Based Healthcare Expansion Planning Considering Pandemic Events", IEEE Journal of Biomedical and Health Informatics.
- 7. [ICCD'21] Hao Zheng, Md Rubel Ahmed, Parijat Mukherjee, Mahesh C. Ketkar, Jin Yang, "Model Synthesis for Communication Traces of System Designs", The 39th IEEE International Conference on Computer Design.
- 6. [ISVLSI'21] Md Rubel Ahmed, Hao Zheng, Parijat Mukherjee, Mahesh C. Ketkar, Jin Yang, "A Comparative Study of Specification Mining Methods for SoC Communication Traces."
- 5. [IJCNN'21] Salman Sadiq Shuvo, **Md Rubel Ahmed**, Hasan Symum, Yasin Yilmaz, "Deep Reinforcement Learning Based Cost-Benefit Analysis for Hospital Capacity Planning", International Joint Conference on Neural Networks.
- 4. [ISQED'21] Md Rubel Ahmed, Hao Zheng, Parijat Mukherjee, Mahesh C. Ketkar, Jin Yang, "Mining Message Flows from System-on-Chip Execution Traces", The 22^{nd} International Symposium on Quality Electronic Design.
- 3. [TCAD'21] Md Rubel Ahmed, Hao Zheng, Parijat Mukherjee, Mahesh C. Ketkar, Jin Yang, "Mining Patterns From Concurrent Execution Traces", IEEE Transactions on Computer-Aided Design of Integrated Circuits & Systems.
- 2. [NSysS'20] Salman Sadiq Shuvo, Md Rubel Ahmed, Sadia Binta Kabir, Shaila Akter Shetu, "Application of Machine Learning Based Hospital Up-gradation Policy for Bangladesh", 7th Int'l Conf. on Networking, Systems and Security.
- 1. $[\mathbf{EICT'17}]$ Amit Sutradhar, Md. Samiul Haque Sunny, Manash Mandal, **Rubel Ahmed**, "Design and construction of an automatic electric wheelchair: An economic approach for Bangladesh", 2017 3^{rd} International Conference on Electrical Information and Communication Technology.

INTELLECTUAL PROPERTY

Patent Application: Agile Hardware Implementation System with Learning-Based Synthesis Assistance.

Publication No.: US 2025/0045492 A1, **Inventors:** T. K. Akino, R. Ahmed.

Filed: Nov. 13, 2023, **Published:** Feb. 6, 2025.

Assignee: Mitsubishi Electric Research Laboratories (MERL).

EXPERIENCE

Postdoctoral Researcher Jan. 2024 - May. 2025

Al and Emerging Computing Lab (moved to the UF in Fall 2024), U. of Central Florida, Orlando, FL

- Developed hybrid neuro-symbolic algorithms for the ANSR program, ensuring robust perception for autonomous drones
- Contributed to machine learning projects and co-authored high-quality manuscripts for publication in leading AI conferences
- Facilitated and mentored undergrad and grad students in their research plan development and execution

Research Intern Sept. 2022 - May 2023

Mitsubishi Electric Research Laboratories, Cambridge, MA

- Developed an ML model for novel applications with innovative techniques
- Improved the efficiency of ML model on FPGA implementation
- Analyzed performance and power trade-offs and achieved low power consumption and high throughput
- Developed a multi-objective optimization framework utilizing the capabilities of both Open AI and Optuna APIs

Research Assistant Aug. 2019 - Aug. 2022

The SEES Lab, U. of South Florida, Tampa, FL

- Successfully instrumented and generated traces from SoCs modeled in VHDL, gem5, and Rocket Chip Generators
- Developed an innovative algorithm for automatic specification mining using advanced Data Mining and NLP models
- Mentored and trained Research Experience for Undergrad (REU) students, providing guidance and support for their research and helping to foster the next generation of experts

Instructor May 2019 - July 2022

University of South Florida, Tampa, FL

- Computer Architecture (CDA 4205) in the summer of 2022 and 2019 covers modern computing architecture and RISC-V ISA
- Developed labs, and class activities and implemented a backward course design approach to help students understand microarchitecture using gem5
- Designed and administered both summative and formative assessments, held weekly discussion sessions and office hours
- Successfully managed and facilitated the smooth running of three lab sections for the course. Held weekly meetings with lab TAs and provided timely feedback.

Teaching Assistant

Aug. 2018 - July 2022

University of South Florida, Tampa, FL

- FPGA Design (Spring 2020)
- System Integration and Architecture (Spring 2020)
- Computer Architecture (Fall 2019)
- Cloud Computing for IT (Fall 2018)
- Software Engineering (Fall 2018)

Software Engineer

Nov. 2017 - July 2018

Synchronous ICT, Dhaka, Bangladesh

- Leveraged FFMPEG technology to process multimedia data in mobile applications effectively
- Innovatively designed and implemented a cutting-edge data-driven cross-platform mobile application using React JS

MAJOR PROJECT HIGHLIGHTS

Assured Neuro Symbolic Learning and Reasoning (ANSR)

Jan. 2024 - Jan. 2025

The ANSR project, part of DARPA's initiative, focuses on developing hybrid AI algorithms that combine neuro-symbolic reasoning with data-driven learning to create trustworthy autonomous systems. My work centers on enhancing the perception capabilities of autonomous drones in tightly resource-constrained situations. It also includes integration with the maneuver component, using ROS2 and Docker multi-container setups. As a postdoctoral researcher, I developed neuro-symbolic perception algorithms, mentored students, and co-authored papers for top AI conferences. [C17, C16, C13]

Specification Mining 2018 - 2022

This project aims to study and explore various approaches to mine sequential patterns from SoC traces. It overcomes the challenges associated with traditional pattern mining for SoC traces. This work incorporates an assertion mining approach to find highly correlated patterns. In addition, a trace-to-Finite State Models (FSMs) conversion approach is also proposed. The FSM models can be used for analysis and improvement of flow specifications. [J1, J2, C4, C7, C6, C9, C10]

HLS Accelerator Design 2022 - 2023

Design parameter space in HLS creates a multi-objective optimization problem. A Quantum Neural Network (QNN) based early failure prediction method is proposed that solves three issues with the existing methods. The QNN model has a relatively small set of weights and is less computationally expensive, making it a greener pathway for HLS acceleration. [C11, C12]

Multi-Objective Optimization in Health-Care Planning

2020 - 2021

This project proposes a healthcare facility expansion plan using Reinforcement Learning (RL). The objective is to use an RL model to simulate the demand-supply scenario of various natural and man-made situations in order to suggest an optimal expansion plan for the hospital facility. This is a multi-objective optimization problem. [J3, C5, C2]

REVIEW ACTIVITY

- TPC reviewer: International Conference on Computer Design
- Transactions on Computer-Aided Design of Integrated Circuits & Systems
- Joint Conference on Neural Networks

PROFESSIONAL ACTIVITIES

• Treasurer for IEEE-CS USF Student Chapter 2020 - 2022 • Judge: USF Undergraduate Research Conference 2021

Registered volunteer for Meals on Wheels of Tampa

2019 - 2022 2021

Volunteer of IFIP IoT Annual Conference

• Volunteer of ISVLSI Conference

2019

FUNDING PROPOSAL DEVELOPMENT

Assisted development of funding proposals in the following areas.

- 1. Collaborative Research: DESC: Type I: Multi-Use Systems for Sustainable In-Memory Computing, NSF DESC Program, \$600k, University of Florida, (under review).
- 2. Accelerating the Rate of Scientific Discovery within Autonomous Laboratories using Virtual and Augmented Reality, DOE EXPRESS, \$500k, University of Central Florida, (under review).
- 3. Specification Mining for heterogeneous architecture (Under development)

AWARDS

| • Dissertation Completion Fellowship (worths \$9K) from the Office of Graduate Studies at USF | 2023 |
|--|-------------|
| • Student Research Forum at ASP-DAC (SRF@ASP-DAC) | 2023 |
| USF Engineering Alumni Society Scholarship | 2022 |
| NSF travel grant for ISVLSI | 2021 |
| USF Student Govt. travel grant for ISQED | 2021 |
| Young Fellow DAC | 2021 |
| • A. Richard Newton Young Student Fellowship award: $56^{th}(2019)$ and $57^{th}(2020)$ Design Automation Conference | nce |
| • Technical Education Scholarship, Bangladesh Govt. | 2015 - 2016 |
| • Secondary School Scholarship, Bangladesh Govt. | 2010 |
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PROFESSIONAL TRAINING

| • Information Security Awareness Training, (UCF) 2024 | |
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| • Responsible Conduct of Research for Engineers- Stage 2, (CITI) | 2024 |
| • Authorship, Credit and Collaborative Scholarship, (UCF) | 2024 |
| • Doing the Right Thing: Know About Research Misconduct, (UCF) | 2024 |
| Preparing for College Teaching, (USF) | 2021 |
| • At-Risk Friends in College, (USF) | 2021 |
| • Graduate Writing Workshop, (USF) | 2020 |
| • USF Grant Writing Workshop, (USF) | 2020 |
| • ATLE Teaching Assistant Training, (USF) | 2018 |

TALKS

| Al and Emerging Computing Lab Research talk | |
|--|-------------|
| • Student Research Forum at ASP-DAC | 2023 |
| • "Interrupt and pseudo-multithreading in Arduino", Arduino Day at FabLab, IUB | 2017 |
| SEES Lab quarterly research summary presentation | 2020 - 2023 |