Low-Cost M Series Multifunction DAQ 16-Bit, 250 kS/s, up to 32 Analog Inputs

M Series - Low Cost

- 16 or 32 analog inputs at 16-bit, 250 kS/s
- Up to 4 analog outputs at 16-bit, 833 kS/s (6 μs full-scale settling time)
- Programmable input range (±10, ±5, ±1, ±0.2 V) per channel
- Up to 48 TTL/CMOS digital I/O lines (up to 32 hardware-timed at 1 MHz)
- Two 32-bit, 80 MHz counter/timers
- Digital triggering
- NI-MCal calibration technology for improved measurement accuracy
- 6 DMA channels for fast data throughput
- NI-DAQmx Measurement Services for simplified configuration and measurements
- 3-year warranty

Operating Systems

• Windows 2000/NT/XP

Recommended Software

- LabVIEW
- LabWindows/CVI
- Measurement Studio

Other Compatible Software

- Visual Studio.NET
- C/C++/C#
- NI Signal Express

Measurement Services Software (included)

• NI-DAQmx



			Analog Input		Output	Max Output	Output		Correlated
Family	Bus	Analog Inputs	Resolution (bits)	Analog Outputs	Resolution (bits)	Rate (kS/s)	Range (V)	Digital I/O	(Clocked) DIO
NI 6220	PCI, PXI	16	16	-	-	-	-	24	8, up to 1 MHz
NI 6221	PCI, PXI	16	16	2	16	833	±10	24	8, up to 1 MHz
NI 6224	PCI, PXI	32	16	-		-		48	32, up to 1 MHz
NI 6229	PCI PXI	32	16	4	16	833	+10	48	32 up to 1 MHz

Table 1. NI Low-Cost M Series Selection Guide

Overview and Applications

National Instruments low-cost M Series devices provide optimized functionality for cost-sensitive applications. They are ideal for applications including data logging and control, and measure sensors and high voltages when used in conjunction with NI signal conditioning. Synchronize the operations of multiple devices using the RTSI bus or PXI trigger bus.

Recommended Accessories

Signal conditioning is required for sensor measurements or voltage inputs greater than 10 V. National Instruments SCXI is a versatile, high-performance signal conditioning platform, optimized for high-channel-count applications. NI SCC provides portable, flexible signal conditioning options on a per-channel basis. For applications not requiring signal conditioning, refer to Table 2 for recommended cabling and accessories.

System Description	Terminal Block Ω	Cable		
High Performance	SCB-68, BNC-2110, TBX-68	SHC68-68-EP		
Basic Shielding	SCB-68, BNC-2110, TBX-68	SHC68-68-S		

Table 2. Recommended Accessories

Ordering Information

PCI	
NI PCI-6220	779065-01
NI PCI-6221	779066-01
NI PCI-6224	779067-01
NI PCI-6229	779068-01
PXI	
NI PXI-6220	779112-01
NI PXI-6221	779113-01
NI PXI-6224	779114-01
NI PXI-6229	779115-01
Includes NI-DAOmy software and calibration certificate	



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Specifications

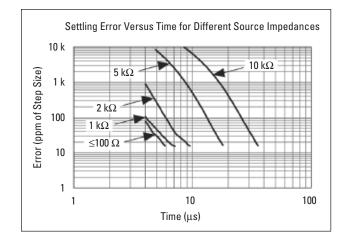
Typical at 25 °C unless otherwise noted.

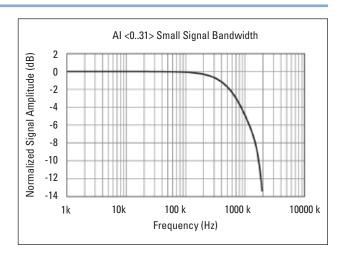
Analog Input Number of channels 8 differential or 16 single ended NI 6220/NI 6221 8 differential or 32 single ended NI 6224/NI 6229 16 differential or 32 single ended ADC resolution 16 bits DNL No missing codes guaranteed INL Refer to the AI Absolute Accuracy Table Sampling rate Maximum Maximum 250 KS/s Minimum 0 S/s Timing accuracy 50 ppm of sample rate Timing resolution 50 ns Input coupling DC Input range ±10 V, ±5 V, ±1 V, ±0 Z V
NI 6224/NI 6229
ADC resolution 16 bits DNL No missing codes guaranteed INL Refer to the AI Absolute Accuracy Table Sampling rate 250 KS/s Maximum 250 KS/s Timing accuracy 50 ppm of sample rate Timing resolution 50 ns Input coupling DC
DNL No missing codes guaranteed INL Refer to the AI Absolute Accuracy Table Sampling rate Maximum 250 KS/s Minimum 0 S/s Timing accuracy 50 ppm of sample rate Timing resolution 50 ns Input coupling DC
INL Refer to the AI Absolute Accuracy Table Sampling rate
Sampling rate 250 KS/s Maximum
Maximum 250 KS/s Minimum 0 S/s Timing accuracy 50 ppm of sample rate Timing resolution 50 ns Input coupling DC
Minimum
Timing accuracy 50 ppm of sample rate Timing resolution 50 ns Input coupling DC
Timing resolution 50 ns Input coupling DC
Input coupling DC
Input range +10 V +5 V +1 V +0 2 V
IIIput range ±10 v, ±3 v, ±1 v, ±0.2 v
Maximum working voltage for analog
inputs (signal + common mode) ±11 V of Al GND
CMRR (DC to 60 Hz)
Input impedance
Al+ to Al GND>10 G Ω in parallel with 100 pF
Al– to Al GND>10 G Ω in parallel with 100 pF
Input bias current ±100 pA
Crosstalk (at 100 kHz)
Adjacent channels
Non-adjacent channels –90 dB
Small signal bandwidth (–3 dB)
Input FIFO size
Scan list memory
Data transfers
Overvoltage protection
(AI <031>, AI SENSE, AI SENSE 2)
Device on ±25 V for up to two Al pins
Device off ±15 V for up to two Al pins
Input current during overvoltage condition ±20 mA max/Al pin

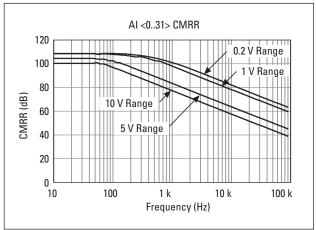
Settling Time for Multichannel Measurements

Accuracy, full scale step, all ranges 4 us convert interval ±90 ppm of step (±6 LSB). ±30 ppm of step (±2 LSB). 5 µs convert interval ± 15 ppm of step (± 1 LSB). 7 μs convert interval

Typical Performance Graphs







Analog Output

Number of channels	
NI 6220	0
NI 6221	2
NI 6224	0
NI 6229	4
DAC resolution	16 bits
DNL	±1 LSB
Monotonicity	16 bit guaranteed
Maximum update rate	
1 channel	833 kS/s
2 channels	740 kS/s per channel
3 channels	666 kS/s per channel
4 channels	625 kS/s per channel
Timing accuracy	50 ppm of sample rate
Timing resolution	50 ns
Output range	±10 V
Output coupling	DC
Output impedance	0.2Ω
Output current drive	±5 mA

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Overdrive protection	±25 V
Overdrive current	10 mA
Power-on state	±20 mV
Power-on glitch	8.5 V peak for 14.5 ms
Output FIFO size	8,191 samples shared among channels used
Data transfers	DMA (Scatter-gather), interrupts, programmed I/O
AO waveform modes	Aperiodic waveform
	Periodic waveform regeneration mode from onboard FIFO
	Periodic waveform regeneration from host buffer including dynamic update

Settling time, full scale step 15 ppm (1 LSB)	6 µs
Slew rate	15 V/μs
Glitch energy	
Magnitude	100 mV
Duration	2.6 µs

Calibration (Al and AO)

Recommended warm-up time	15 minute
Calibration interval	1 year

Al Absolute Accuracy Table

Nomina	l Range	Residual			Residual	Offset			Absolute	
Positive	Negative	Gain Error	Gain Tempco	Reference	Offset Error	Tempco (ppm	INLError	Random Noise,	Accuracy at	Sensitivity ²
Full Scale	Full Scale	(ppm of Reading)	(ppm/°C)	Tempco	(ppm of Range)	of Range/°C	(ppm of Range)	σ (μV _{rms})	Full Scale ¹ (μV)	(μV)
10	-10	75	25	5	20	57	76	244	3100	97.6
5	-5	85	25	5	20	60	76	122	1620	48.8
1	-1	95	25	5	25	79	76	30	360	12.0
0.2	-0.2	135	25	5	80	175	76	13	112	5.2

AbsoluteAccuracy = Reading · (GainError) + Range · (OffsetError) + NoiseUncertainty

GainError = ResidualAlGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)

 $OffsetError = Residual Al OffsetError + OffsetTempco \cdot (TempChangeFromLastInternalCal) + INL_Error + OffsetTempco \cdot (TempChangeFromLastInternalCal) + OffsetTempco$

NoiseUncertainty = RandomNoise · 3

For a coverage factor of 3 σ and averaging 100 points

¹Absolute accuracy at full scale on the analog input channels is determined using the following assumptions:

TempChangeFromLastExternalCal = 10 °C TempChangeFromLastInternalCal = 1 °C

number_of_readings = 100

CoverageFactor = 3 σ

For example, on the 10 V range, the absolute accuracy at full scale is as follows: $GainError = 75 \text{ ppm} + 25 \text{ ppm} \cdot 1 + 5 \text{ ppm} \cdot 10 \qquad \qquad GainError = 150 \text{ ppm}$

OffsetError = 20 ppm + 57 ppm · 1 + 76 ppm OffsetError = 153 ppm

NoiseUncertainty = $\frac{244 \mu V \cdot 3}{r}$,

AbsoluteAccuracy = 10 V · (GainError) + 10 V · (OffsetError) + NoiseUncertainty

AbsoluteAccuracy = 3100 μV

AO Absolute Accuracy Table

Nomina	I Range	Residual			Residual	Offset		Absolute
Positive	Negative	Gain Error	Gain Tempco	Reference	Offset Error	Tempco (ppm	INLError	Accuracy at
Full Scale	Full Scale	(ppm of Reading)	(ppm/°C)	Tempco	(ppm of Range)	of Range/°C	(ppm of Range)	Full Scale ¹ (μV)
10	-10	90	10	5	40	5	128	3230

¹Absolute Accuracy at full scale numbers is valid immediately following internal calibration and assumes the device is operating within 10 °C of the last external calibration.

 $AbsoluteAccuracy = OutputValue \cdot (GainError) + Range \cdot (OffsetError)$

 $GainError = ResidualGainError + GainTempco \cdot (TempChangeFromLastInternalCal) + ReferenceTempco \cdot (TempChangeFromLastExternalCal) + ReferenceTempco \cdot (TempChangeFromLastExte$

OffsetError = ResidualOffsetError + AOOffsetTempco · (TempChangeFromLastInternalCal) + INL Error

Digital I/O/PFI

Static Characteristics

Number of channels	
NI 6220/NI 6221	24 total
	8 (P0.<07>)
	16 (PFI <015>/P1/P2)
NI 6224/NI 6229	48 total
	32 (P0.<031>)
	16 (PFI <015>/P1/P2)
Ground reference	D GND
Direction control	Each terminal individually
	programmable as input or output
Pull-down resistor	50 k Ω to 75 k Ω
Input voltage protection ¹	±20 V on up to two pins

¹ Stresses beyond those listed under Input voltage protection may cause permanent damage to the device.

Waveform Characteristics (Port 0 0	nly)
NI 6220/NI 6221 NI 6224/NI 6229	
NI 6220/NI 6221	Up to 8 bits Up to 32 bits 2,047 samples 2,047 samples 0 to 1 MHz Any PFI, RTSI, Al Sample or Convert Clock, AO Sample Clock, DI Change Event, Ctr n Internal Output, and many other signals
PFI/Port 1/Port 2 Functionality Functionality	Static digital input, static digital

Static digital input, static digital
output, timing input, timing output
Many AI, AO, counter, DI, DO timing signals
125 ns, 6.425 μs, 2.54 ms, disable; high
and low transitions; selectable per input

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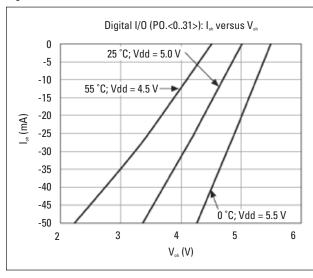
Recommended Operation Conditions

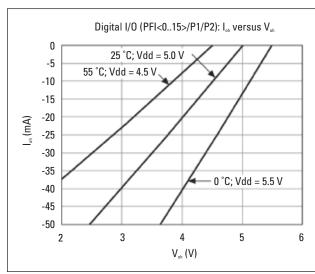
Level	Minimum	Maximum
Input high voltage (V _{IH})	2.2 V	5.25 V
Input low voltage (V _{IL})	0 V	0.8 V
Output high current (I _{OH})		
P0.<031>	_	−24 mA
PFI <015>/ P1/P2	-	−16 mA
Output low current (I _{OL})		
P0.<031>	-	24 mA
PFI <015>/P1/P2	_	16 mA

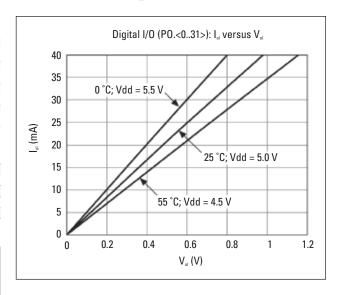
Electrical Characteristics

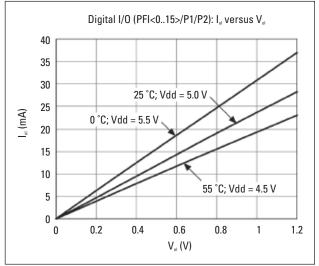
Level	Minimum	Maximum
Positive-going threshold (VT+)	-	2.2 V
Negative-going threshold (VT-)	0.8 V	_
Delta VT hysteresis (VT+ - VT-)	0.2 V	-
I _{IL} input low current (V _{in} = 0 V)	-	–10 μA
I _{IH} input high current (V _{in} = 5 V)	_	250 μΑ

Digital I/O Characteristics









General-Purpose

General-Purpose	
Counter/Timers	
Number of counter/timers	2
Resolution	32 bits
Counter measurements	Edge counting, pulse, semi period,
	period, two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with
	Channel Z reloading; two-pulse encoding
Output applications	Pulse, pulse train with dynamic updates,
	frequency division, equivalent time sampling
Internal base clocks	80 MHz, 20 MHz, 0.1 MHz
External base clock frequency	0 MHz to 20 MHz
Base clock accuracy	50 ppm
Inputs	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Routing options for inputs	Any PFI, RTSI, PXI_TRIG, PXI_STAR,
	analog trigger, many internal signals
FIFO	2 samples
Data transfers	Dedicated scatter-gather DMA controller for
	each counter/timer; interrupts; programmed I/O

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Frequency Generator			1 A max, each connector, with self-resetting fuse
Number of channels		Utner power limit for PXI devices	Current drawn from +5 V terminals and all PO/PFI/P1/P2 terminals should not exceed 2 A
Base clocks			FO/FFI/F I/FZ tellillidis siloulu liot exceeu Z A
Divisors		Physical	
Base clock accuracy	**	Dimensions	
Output can be available on any PFI or RTSI to	erminal.	PCI	9.7 by 15.5 cm (3.8 by 6.1 in.)
Phase-Locked Loop (PLL	1	PXI	
Number of PLLs		I/O connector	
Reference signal		NI 6220/NI 6221	1 68-pin VHDCI
	. 80 MHz timebase; other signals derived from 80 MHz	NI 6224/NI 6229	2 68-pin VHDCI
	timebase including 20 MHz and 100 kHz timebases	Maximum Warking Val	togo1
	C	Maximum Working Vol	
External Digital Triggers	;	Channel-to-earth	
Source		Channel-to-channel	
Polarity	9	¹ Maximum working voltage refers to the signal voltage	oltage plus the common-mode voltage.
Analog input function	. Start Trigger, Reference Trigger, Pause Trigger,	Environmental	
	Sample Clock, Convert Clock, Sample Clock Timebase	Operating temperature	Ω to 55 °C
Analog output function		Storage temperature	
0	Sample Clock Timebase	Relative Humidity	
	. Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down	Maximum altitude	2,000 m
Digital waveform generation (D0) function	•	Pollution Degree (indoor use only)	2
Digital wavelolli acquisition (DI) function	. Sample Cluck		
Device-To-Device Trigger Bus	. Заприе споск	Safety	
Device-To-Device Trigger Bus PCI devices	. RTSI <07> ¹	Safety This product is designed to meet the require	ements of the following standards of safety for electrical
Device-To-Device Trigger Bus PCI devices	. RTSI <07> ¹	Safety This product is designed to meet the requirequipment for measurement, control, and la	,
Device-To-Device Trigger Bus PCI devices	. RTSI <07>1 PXI_TRIG <07>, PXI_STAR 10 MHz Reference Clock; frequency	Safety This product is designed to meet the requirequipment for measurement, control, and late IEC 61010-1, EN 61010-1	,
Device-To-Device Trigger Bus PCI devices	RTSI <07> PXI_TRIG <07>, PXI_STAR 10 MHz Reference Clock; frequency generator output; many internal signals	Safety This product is designed to meet the requirequipment for measurement, control, and let IEC 61010-1, EN 61010-1 • UL 61010-1	,
Device-To-Device Trigger Bus PCI devices	. RTSI <07>1 PXI_TRIG <07>, PXI_STAR 10 MHz Reference Clock; frequency generator output; many internal signals 125 ns, 6.425 µs, 2.54 ms, disabled; high	Safety This product is designed to meet the requirequipment for measurement, control, and late IEC 61010-1, EN 61010-1 • UL 61010-1 • CAN/CSA C22.2 No. 61010-1	aboratory use:
Device-To-Device Trigger Bus PCI devices	RTSI <07> PXI_TRIG <07>, PXI_STAR 10 MHz Reference Clock; frequency generator output; many internal signals	Safety This product is designed to meet the requirequipment for measurement, control, and let IEC 61010-1, EN 61010-1 • UL 61010-1 • CAN/CSA C22.2 No. 61010-1 For UL and other safety certifications, refer	aboratory use: to the product label, or visit <i>ni.com/certification</i> , search by
Device-To-Device Trigger Bus PCI devices PXI devices Output selections Debounce filter settings	. RTSI <07>1 PXI_TRIG <07>, PXI_STAR 10 MHz Reference Clock; frequency generator output; many internal signals 125 ns, 6.425 µs, 2.54 ms, disabled; high	Safety This product is designed to meet the requirequipment for measurement, control, and let IEC 61010-1, EN 61010-1 • UL 61010-1 • CAN/CSA C22.2 No. 61010-1 For UL and other safety certifications, refer	aboratory use:
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PCI devices	. RTSI <07>1 . PXI_TRIG <07>, PXI_STAR . 10 MHz Reference Clock; frequency generator output; many internal signals . 125 ns, 6.425 µs, 2.54 ms, disabled; high and low transitions; selectable per input 8TSI <07> for PXI devices or PXI_TRIG <07> for PXI devices.	Safety This product is designed to meet the requirequipment for measurement, control, and late IEC 61010-1, EN 61010-1 UL 61010-1 CAN/CSA C22.2 No. 61010-1 For UL and other safety certifications, refermedel number or product line, and click the Electromagnetic Compatibility Emissions.	to the product label, or visit <i>ni.com/certification</i> , search by appropriate link in the Certification column. EN 55011 Class A at 10 m; FCC Part 15A above 1 GHz
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