

I Parallelization of phonon calculations

The PHonon package enables calculations of phonon frequencies and eigenmodes. This chapter examines the best ways to run PHonon calculations. The benchmarks are averaged over 10 runs.

I.1 Optimal parallelization parameters for phonon calculations

As discussed in sec. ??, the PHonon package offers the same three parallelization levels as the PWscf package, namely plane-wave, k -point and linear-algebra parallelization. Furthermore, parallelization on q -points (so called image parallelization) can be used.

I.1.1 k point parallelization

In a first step, the same k -point parallelization benchmark as in sec. ?? is run. This is depicted in fig. I.1.

Interestingly, the result from the PWscf calculation on silicon from sec. ?? is not reproduced here: the smallest pool size of 2 is not the one parallelizing best, but instead it is pool size 8. Furthermore, for more than 50 processors, even the biggest pool size 18 shows better scaling than the pool size 2. The general picture is similar to PWscf benchmark with k -point parallelization on the TaS₂ benchmarking system in sec. ??, as there is no optimal pool size over the whole range of processors, instead some pool sizes seem to work best for some ranges.

One possible conclusion to draw from the benchmarks in ?? is, that in general longer runtime results in the calculation profiting more from parallelization and as such also from bigger pool sizes. The phonon benchmark has a similar runtime to the PWscf benchmark on TaS₂ shown in sec. ??, so following this argumentation a similar scaling behavior is expected. Comparison in wait time reveals the differences in the quality of parallelization between the two systems, which results in the observed different scaling. Whereas the PWscf benchmark on TaS₂ had wait time not exceeding about 8% of the wall time, the wait time shown in fig. I.2 between 10% and 50%.

A possible explanation for these differences between the two kinds of calculation can be found in how the time is actually spent during the calculation (which can be found in the QUANTUM ESPRESSO output files): In the case of the phonon calculation on silicon, the time of one iteration is on the scale of seconds, whereas one iteration for the PWscf calculation on TaS₂ is about 1 min. This means that the proportion of time spent on the distribution of data is bigger for the phonon calculation on silicon compared to the PWscf calculation on TaS₂, which introduces wait times.

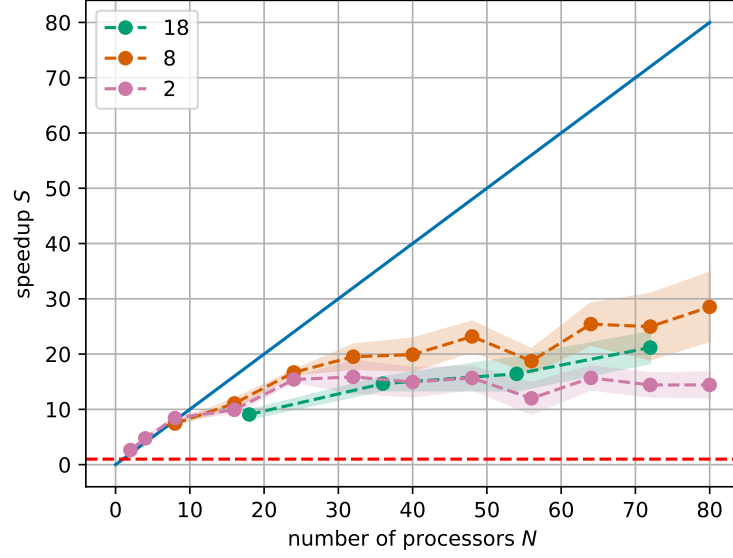


Figure I.1: Scalability utilizing k -point parallelization for the Si benchmarking system with three sizes of processor pools. Best scaling behavior is seen for pool size 8. QUANTUM ESPRESSO compiled with [Intel oneAPI 2021.4](#), `nd 1`

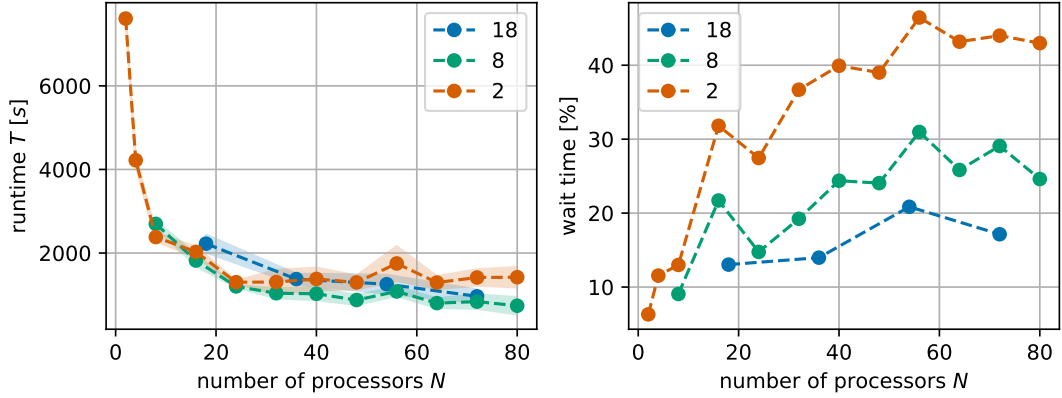


Figure I.2: Absolute runtime and wait time for the scalability test utilizing k -point parallelization for the Si benchmarking system with three sizes of processor pools. Wait time is highest for the pool size which parallelizes worst, with the other two having lower wait times. QUANTUM ESPRESSO compiled with [Intel oneAPI 2021.4](#), `nd 1`

From the result of this benchmark, the parameters for the image parallelization in sec. [I.1.3](#) can be set: the pool size will be fixed at 8.

I.1.2 Linear-algebra parallelization

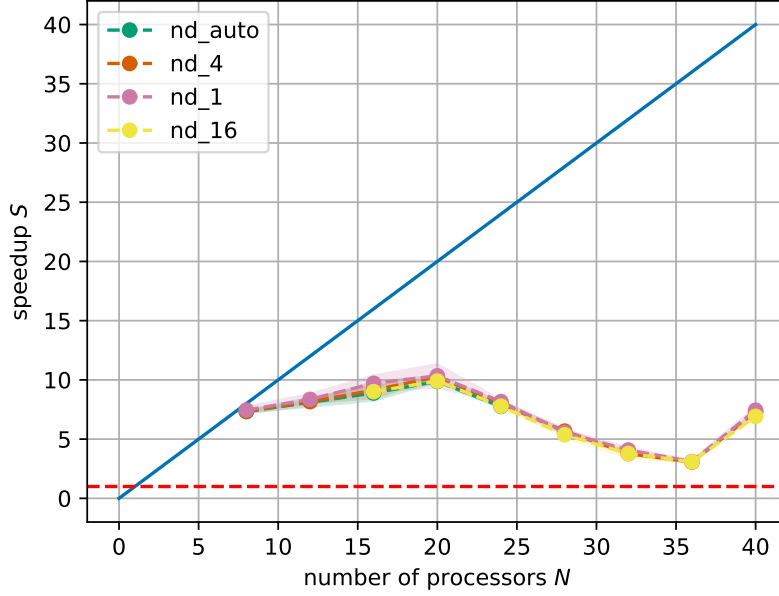


Figure I.3: Scalability utilizing linear-algebra parallelization for the Si benchmarking system. The scaling is the same across the whole range of processors for every linear-algebra group size. QUANTUM ESPRESSO compiled with [Intel oneAPI 2021.4](#), `nk 1`

Fig. I.3 shows that using linear-algebra parallelization has no significant impact on the speedup. This is again in contrast to the PWscf results from sec. ??, where using linear-algebra slowed down the calculation and more similar to the PWscf calculation on TaS₂.

As such, linear-algebra parallelization will not be used in the benchmarks for image parallelization.

I.1.3 Image parallelization

When using image parallelization, QUANTUM ESPRESSO outputs a separate time report for every image, so one additional step is needed in the analysis: While the total runtime of a calculation is determined by the longest running image, the variation of times between images is important to judge how well the work between images is distributed. This is depicted in fig. I.4.

As the standard deviation between images is just 20-60 s, which amounts to about 10 % of the average runtime, good load balancing between images can be assumed for the silicon benchmarking system.

With the maximum time across images, speedup is then calculated, shown in fig. I.5.

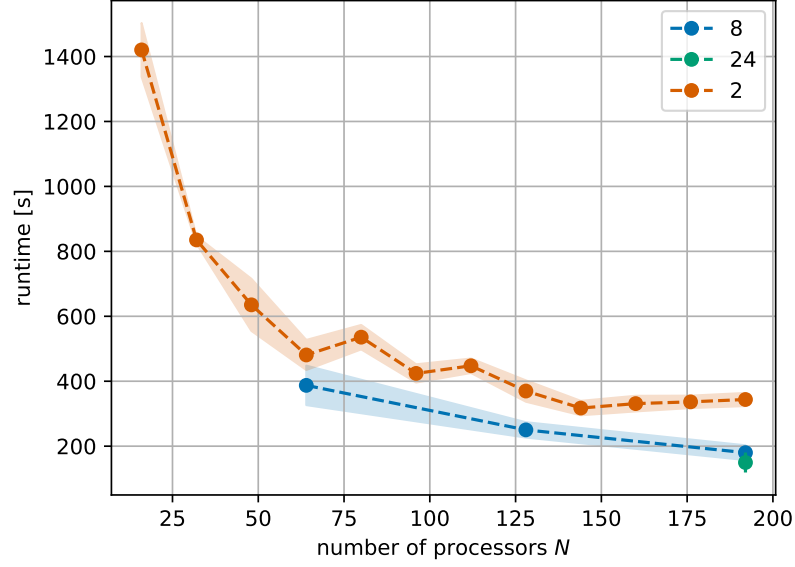


Figure I.4: Average runtime across images for the scalability test utilizing image and k -point parallelization on the Si benchmarking system with three values of \mathbf{ni} . The transparent line shows standard deviation across images, which shows that all images need around the same time for their respective calculations. QUANTUM ESPRESSO compiled with Intel oneAPI 2021.4, \mathbf{nk} , \mathbf{ni} chosen such that $\mathbf{poolsize} = 8$, $\mathbf{nd} = 1$

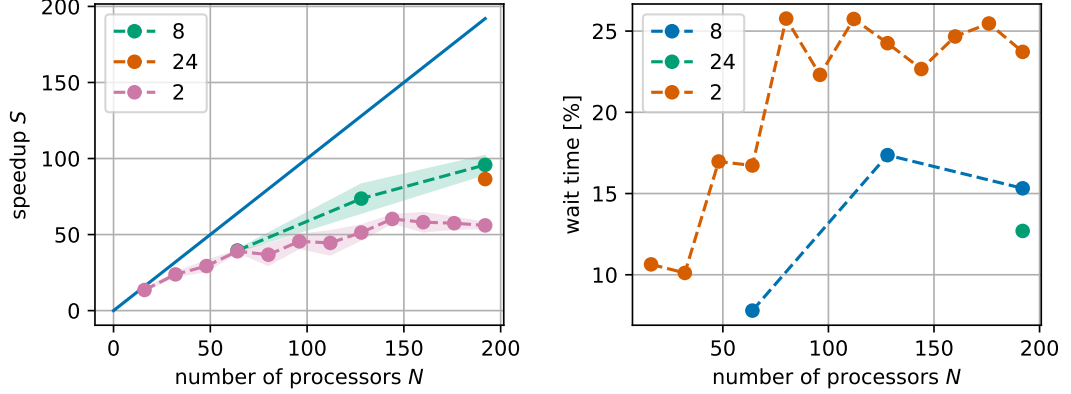


Figure I.5: Speedup and wait time calculated from the longest running image for the scalability test utilizing image and k -point parallelization on the Si benchmarking system with three values of \mathbf{ni} . Using more images leads to the linear slope of the speedup continuing over more processors. The wait time is lowest for every first data point and lower than without image parallelization in general. QUANTUM ESPRESSO compiled with Intel oneAPI 2021.4, \mathbf{nk} , \mathbf{ni} chosen such that $\mathbf{poolsize} = 8$, $\mathbf{nd} = 1$

Table I.1: Benchmark values for phonon calculations of TaS₂ in the charge-density-wave phase, run on 180 processors

	runtime	wait time
pool size 18	3044 min	16 %
pool size 36	2020 min	7.4 %

The speedup shows that using image parallelization helps the phonon calculations scale over more processors than just using k -point parallelization. Even just using 2 images almost doubles the maximal achieved speedup in fig. I.1. This behavior doesn't extend across the whole range of processors, but using more images can partially lift this problem.

The wait time (also calculated from the longest running image) shown in fig. I.2 is particularly good for every first data point, so in cases where only image parallelization without additional k -point parallelization is applied. As an example, for 64 processors, 8 images is $N_P/N_i = 8$, so the pool size is already 8 as required and the parameter `nk` is chosen to be 1. So at least for the silicon system it seems to be advisable to choose the parameter `ni` as large as possible, so that `nk` can be chosen as small as possible while keeping a pool size of 8.

I.2 Phonon calculations on TaS₂

The results from the last section can be used to estimate good parallelization parameters for a phonon calculation at the Γ point for TaS₂ in the charge-density-wave phase. The calculations were run on 180 processors, once with the previous established optimal pool size of 36 and once with a pool size of 18 for comparison. The relevant benchmark values for this calculation are listed in tab. I.1.

In this calculation the need for a good choice of parallelization parameters becomes especially clear: while the runs share an identical number of processors, the different choice of the parameter `nk` results in a runtime saving of 17h.

I.3 Conclusion: Parameters for optimal scaling

The benchmarks on QUANTUM ESPRESSO's PHonon module showed that calculation on the silicon system profited more from bigger k -point pools than PWscf calculations. Linear-algebra parallelization was shown to have no impact on calculations.

Image parallelization in combination with k -point parallelization led to very good scaling across a wide range of processors. Best results were found when the processors were only split up by image parallelization.

Phonon calculations on TaS₂ in the charge-density-wave phase were then carried out, with the best time reached for a pool size of 36.