

2020-04-10

时间: 16:00-16:30

与会人员: 陈耿阳 田晨江 赵文祺 孙逸伦

记录人: 孙逸伦

议题

- 使用issue对遇到的问题进行管理
 - 可以在commit message进行引用，方便查询
 - 记录工程行为
- 搜索的divider溢出 (issue #19)

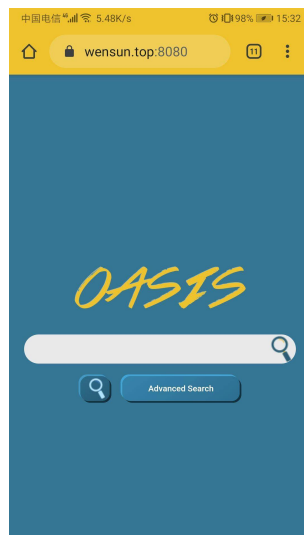
Software prefetching for **software** pipelined loops

F.J. Sanchez A. Gonzalez HICSS 1998

The paper investigates the interaction between **software** pipelining and different **software** prefetching techniques for VLIW machines. It is shown that processor stalls due to memory dependencies have a great impact into execution time. A novel heuristic is proposed and it is shown to outperform previous proposals.

keywords: Prefetching Delay Processor scheduling Pipeline processing Registers VLIW Proposals Computer architecture Degradation Argon instruction sets software pipelined loops software prefetching VLIW... [show more](#)

- PWA: demo
 - 是否有必要？



- 测试的粒度
 - 不是越多越好
 - 适当注意白盒的分支覆盖，黑盒测试运用等价类划分，而不是在某一个等价类内部进行多余的测试
- 集群、大数据，Java+ Scala的可行性
- 也许可以保存搜索记录

总结执行

下次会议时间：2020-04-12（周日）下午15:00