Instruction	OPCODE	Jump	Shift	RegDst	RegWrite	MemRead	MemWrite	MtoR	Beq	Bneq	AluSrc	ALU Control
subi	"0000"	0	0	0	1	0	0	0	0	0	1	"001"
sw	"0001"	0	0	0	0	0	1	0	0	0	1	"000"
bneq	"0010"	0	0	0	0	0	0	0	0	1	0	"001"
andi	"0011"	0	0	0	1	0	0	0	0	0	1	"011"
nor	"0100"	0	0	1	1	0	0	0	0	0	0	"100"
addi	"0101"	0	0	0	1	0	0	0	0	0	1	"000"
or	"0110"	0	0	1	1	0	0	0	0	0	0	"010"
ori	"0111"	0	0	0	1	0	0	0	0	0	1	"010"
srl	"1000"	0	1	1	1	0	0	0	0	0	0	"110"
and	"1001"	0	0	1	1	0	0	0	0	0	0	"011"
j	"1010"	1	0	0	0	0	0	0	0	0	0	XXX
sll	"1011"	0	1	1	1	0	0	0	0	0	0	"101"
lw	"1100"	0	0	0	1	1	0	1	0	0	1	"000"
add	"1101"	0	0	1	1	0	0	0	0	0	0	"000"
beq	"1110"	0	0	0	0	0	0	0	1	0	0	"001"
sub	"1111"	0	0	1	1	0	0	0	0	0	0	"001"