#### SYNERGY INSTITUTE OF ENGINEERING AND TECHNOLOGY, DHENKANAL



### Near NH-55, Banamaliprasad – 759001

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING NBA ACCREDITED

#### Assignment-I

Full Marks-40 Duration- Within 1 Week from Notification Name- Registration No-

**Subject with Code:** Computer Organization and Architecture (CSPC2005)

Year & Semester: 2nd & IV

Course & Branch: B. Tech. & CSE
Academic Session: 2024-2025

#### Section-A Answer All Questions

1.	Define Program Counter ?	[2 marks] [CO1] [L2]
2.	What are the primary registers present in CPU?	[2 marks] [CO1] [L2]
3.	What does an instruction contain?	[2 marks] [CO1] [L2]
4.	Why is MAR connection with memory unidirectional?	[2 marks] [CO1] [L2]

#### Section-B Answer All Questions

1. Describe with diagram Von Neumann Architecture.	[6 marks] [CO1] [L2]
2. Differentiate Big Endian System and Little Endian System.	[6 marks] [CO1] [L2]

# **Section-C Answer All Questions**

1.Discuss different types instruction formats with example. [10 marks] [CO1] [L2]

2. List the instructions using one-address instruction for the below mentioned example.

X = (A\*B)+(C\*D), where A,B,C,D & X are memory locations [10 marks] [CO1] [L2]

#### **Additional Questions**

- 1. Describe the different functional units of computer?
- 2. Draw the connection diagram between memory and processor.
- 3. Write the Register Transfer Notation for the following instruction

Add R1,R2,R3