

# EE 599 Spring 2020 Homework 1

## Report

Github repo link: [https://github.com/RudrenduMahindar/EE599\\_mahindar\\_6343999513](https://github.com/RudrenduMahindar/EE599_mahindar_6343999513)

1. Images for Odd-even transposition sort have been attached below:

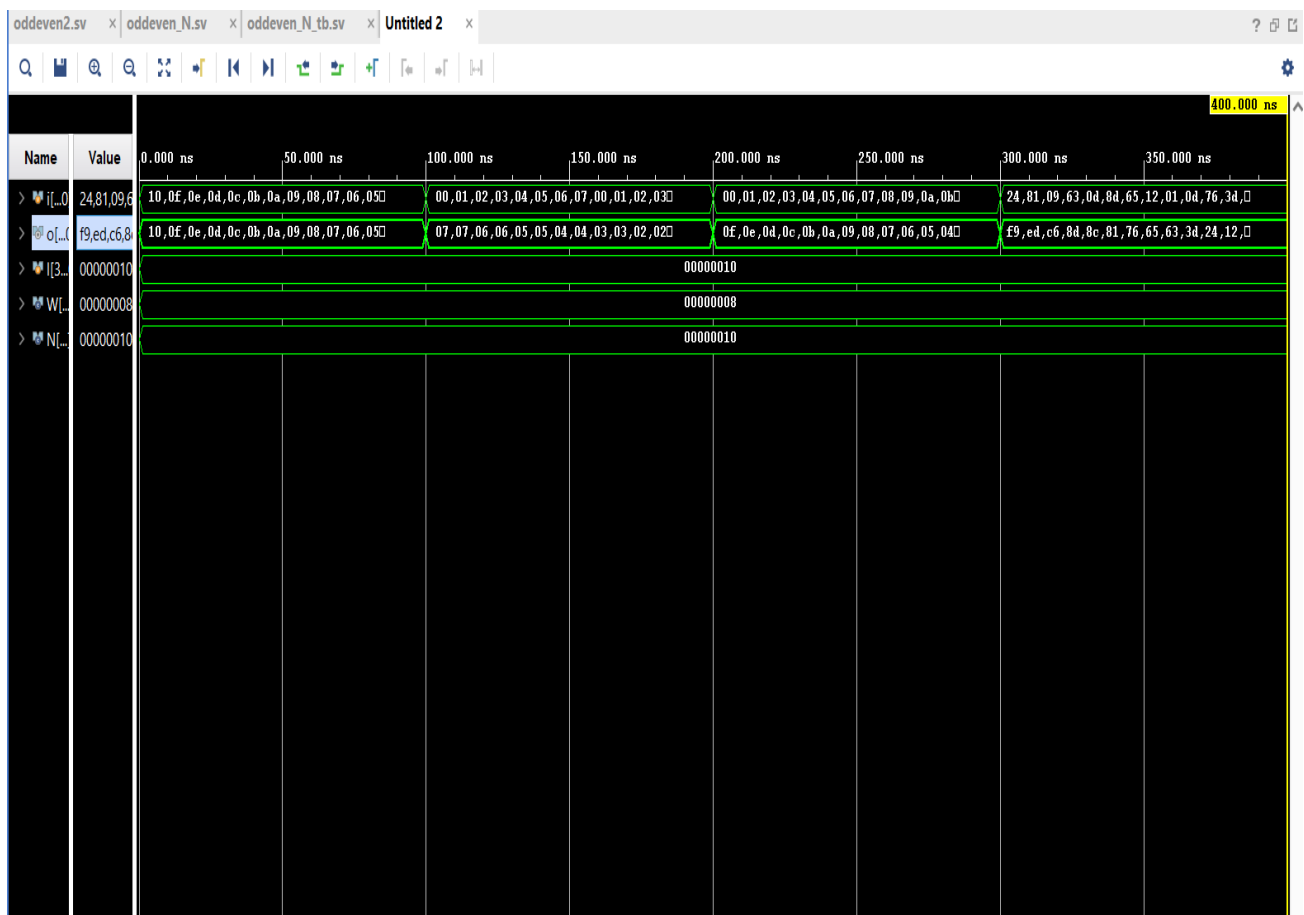


Fig1: Waveform for N=16

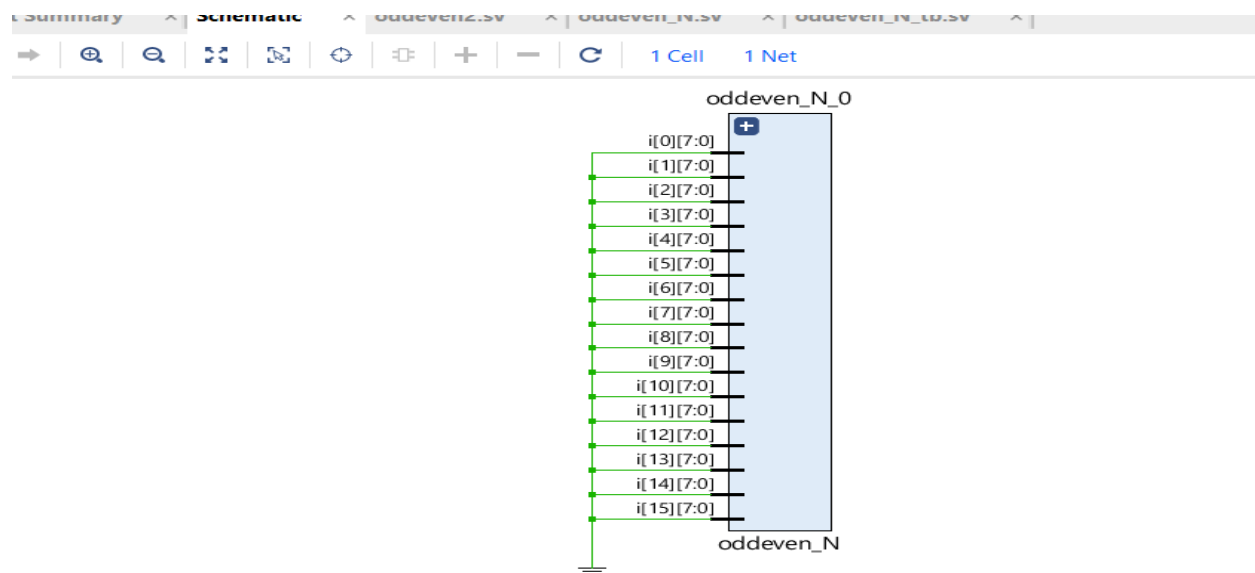


Fig2: Schematic screenshot of module for N=16

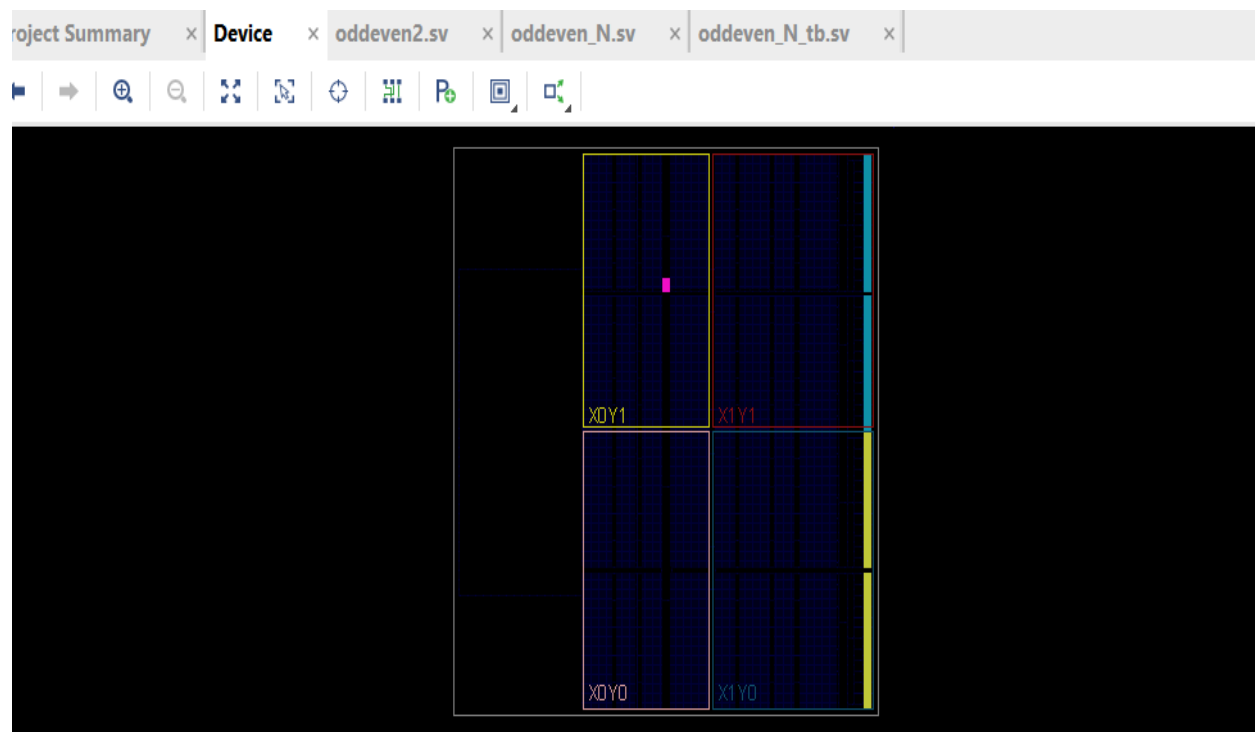


Fig3: Post synthesis schematic of device model for N=16

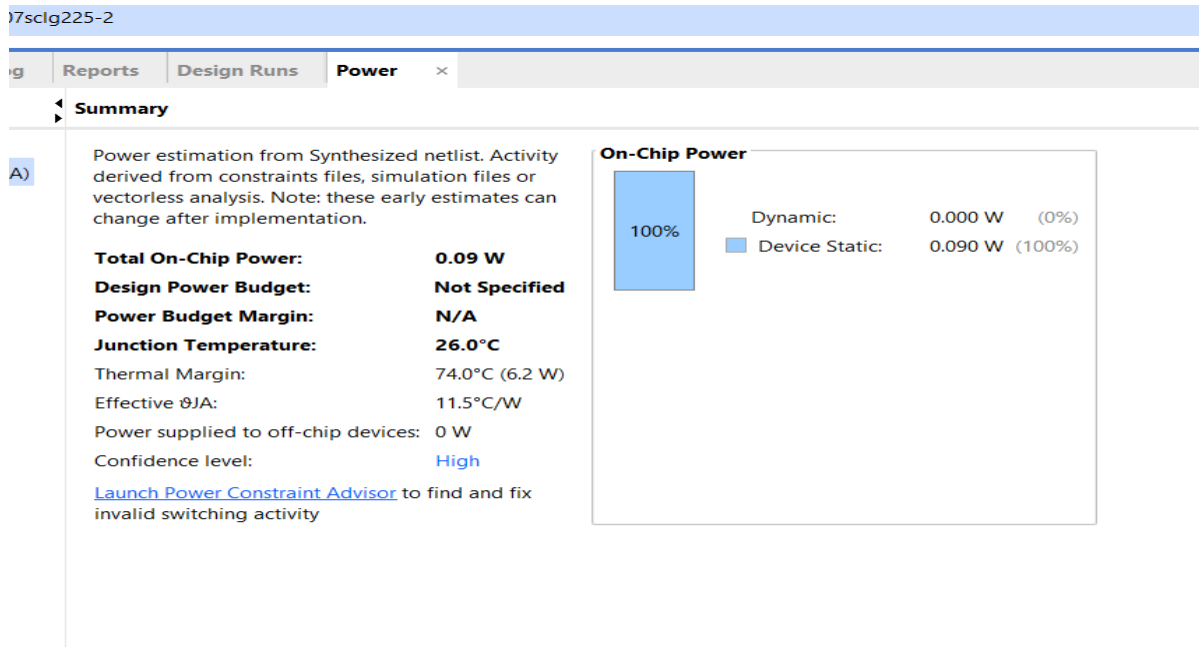


Fig4: Power estimation for N=16

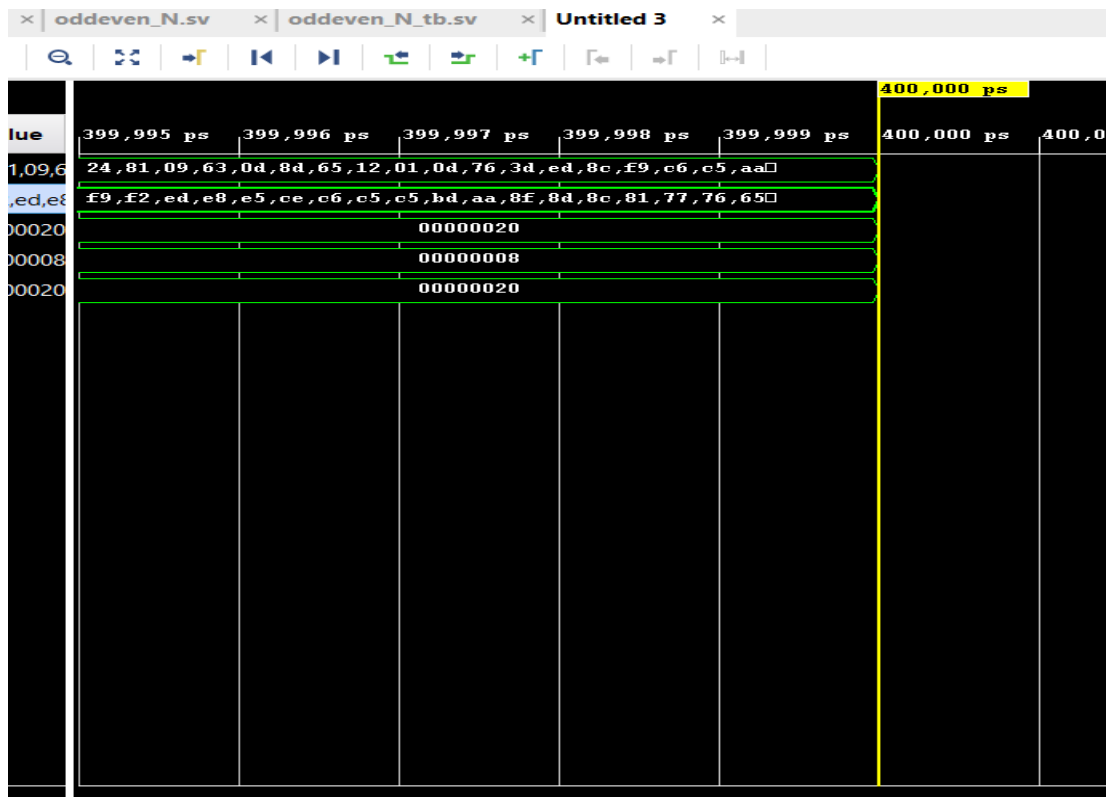


Fig5: Waveform for N=32

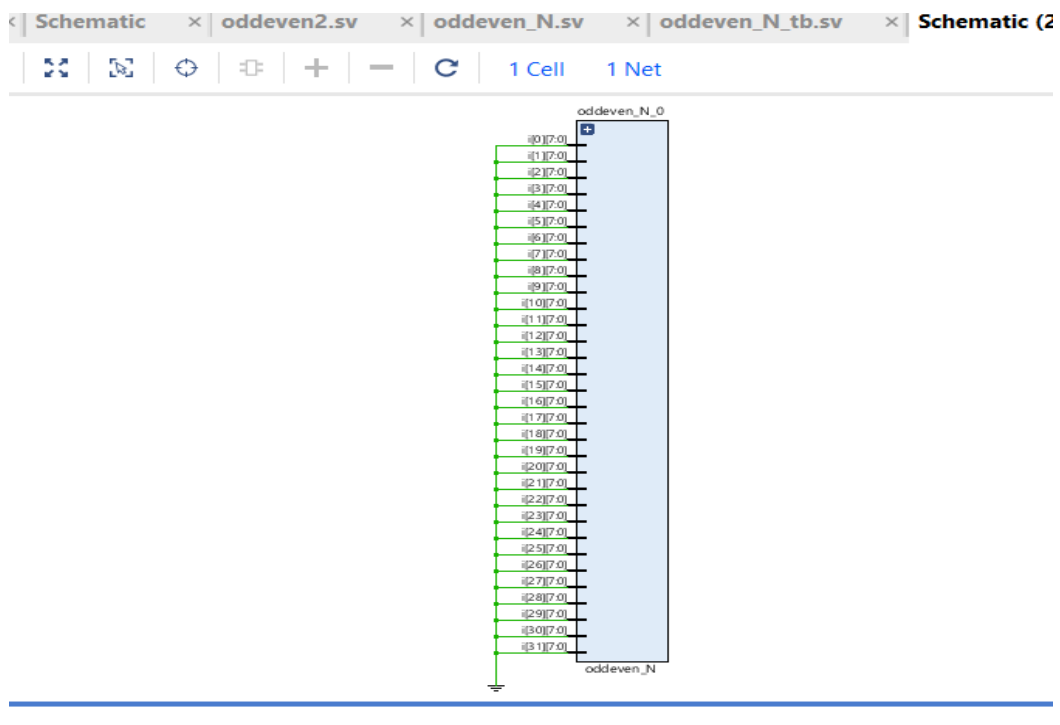


Fig6: Schematic screenshot of module for N=32

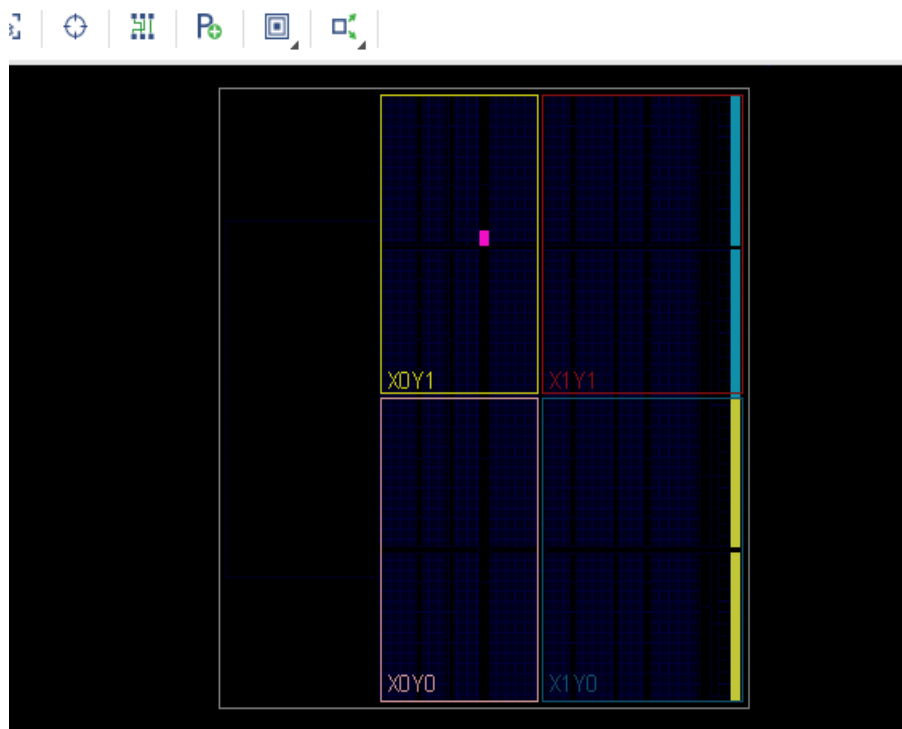


Fig7: Post synthesis schematic of device model for N=32

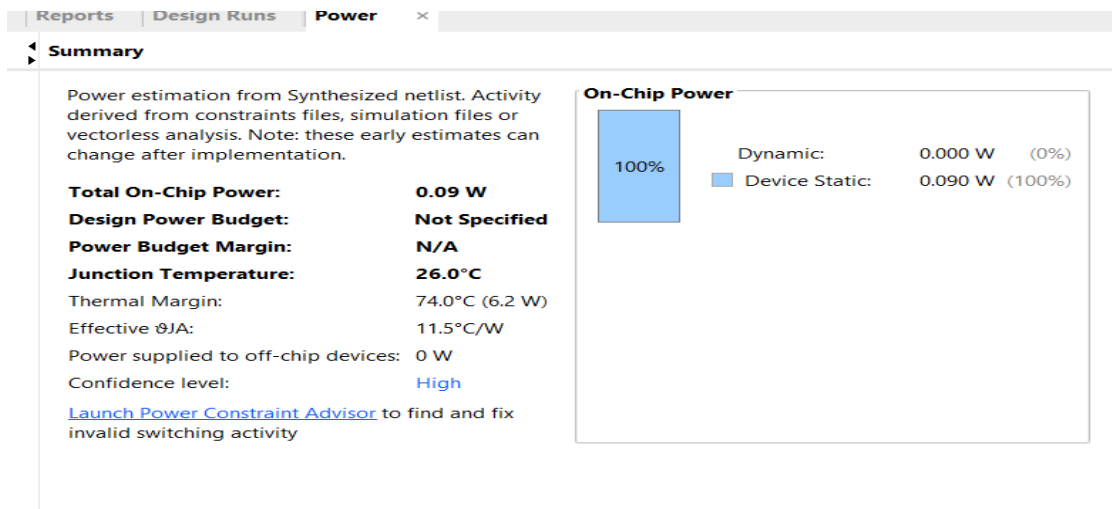


Fig8: Power estimation for N=32

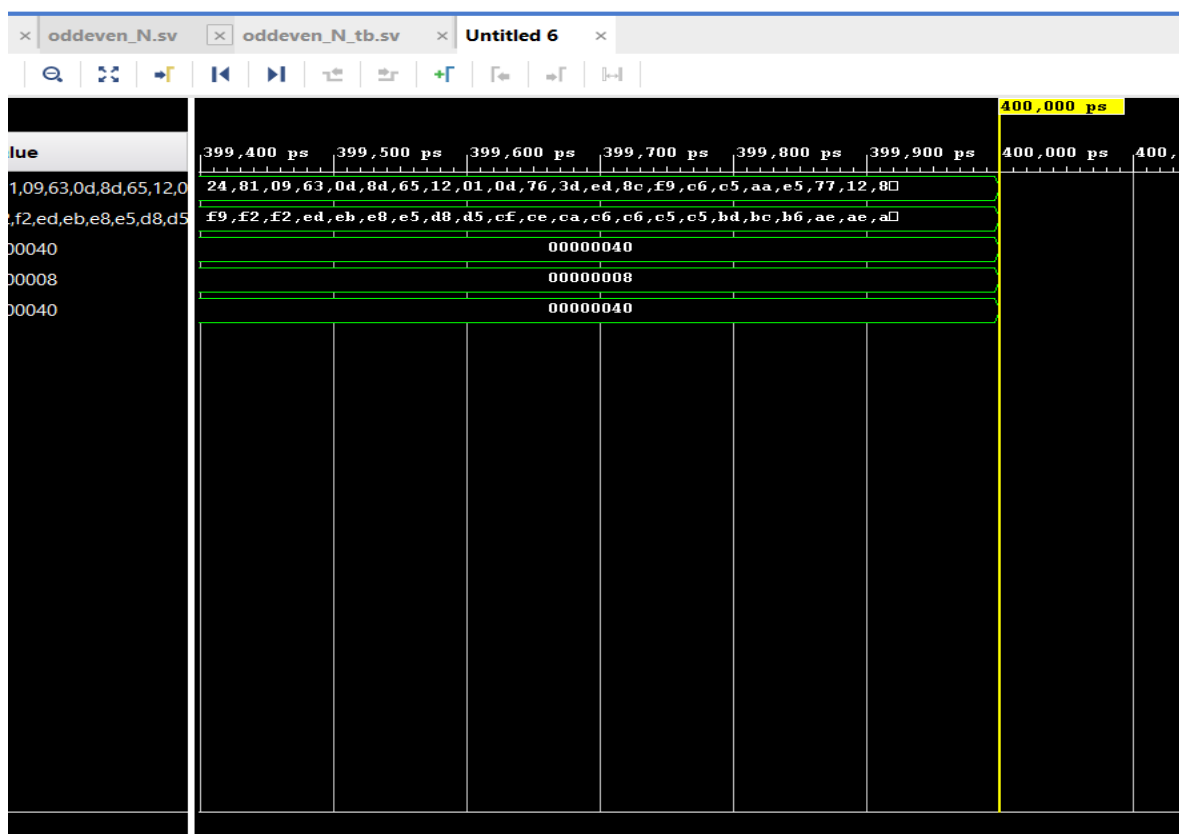


Fig9: Waveform for N=64

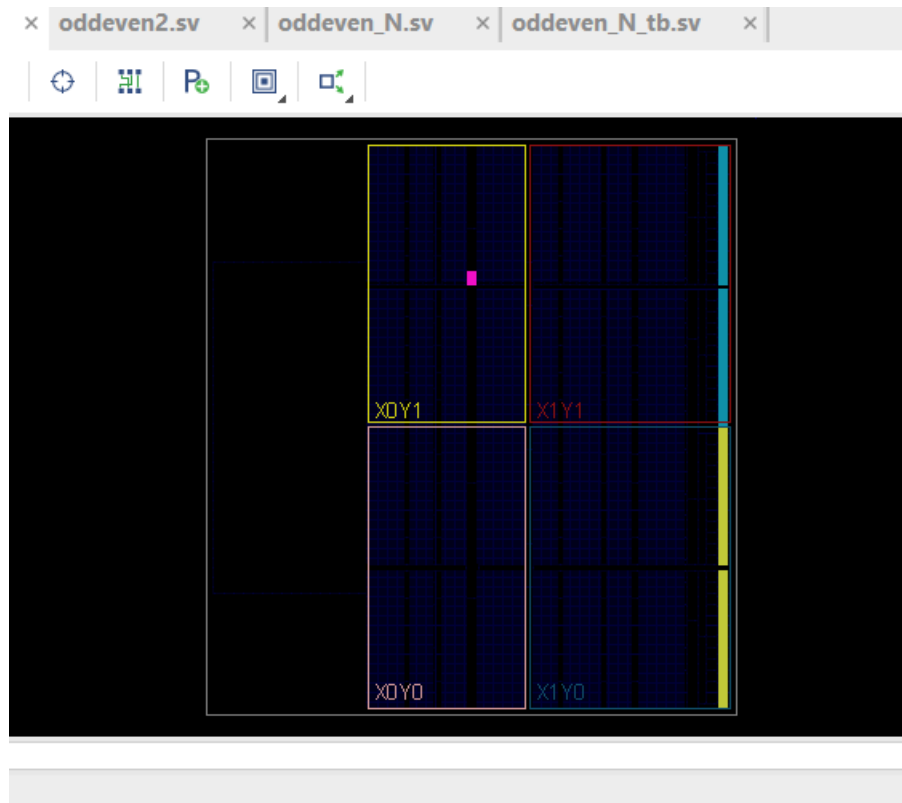


Fig10: Post synthesis schematic of device model for N=64

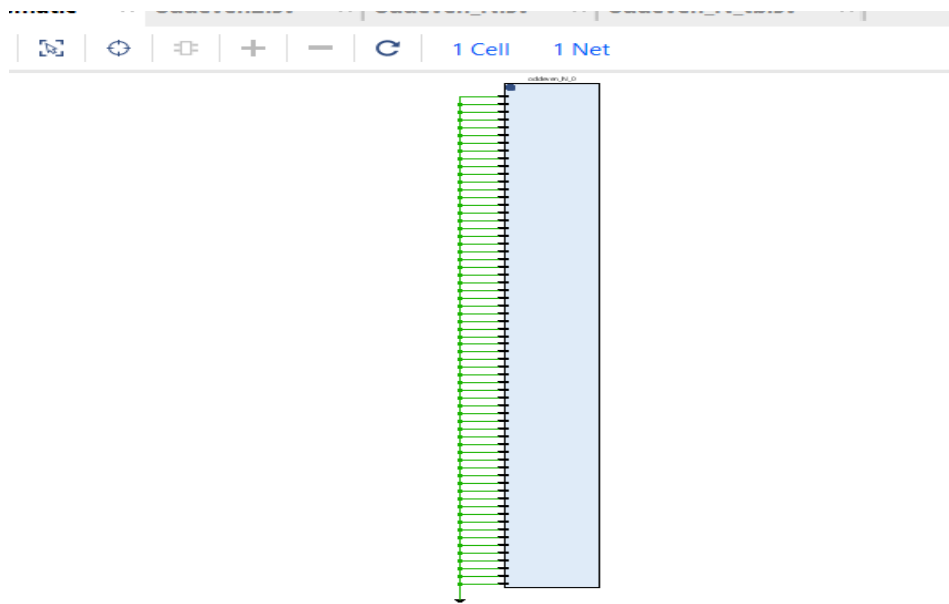


Fig11: Schematic screenshot of module for N=64

Design is out-of-date. Newer Synthesis results are available. [Reload](#) [Close Design](#)

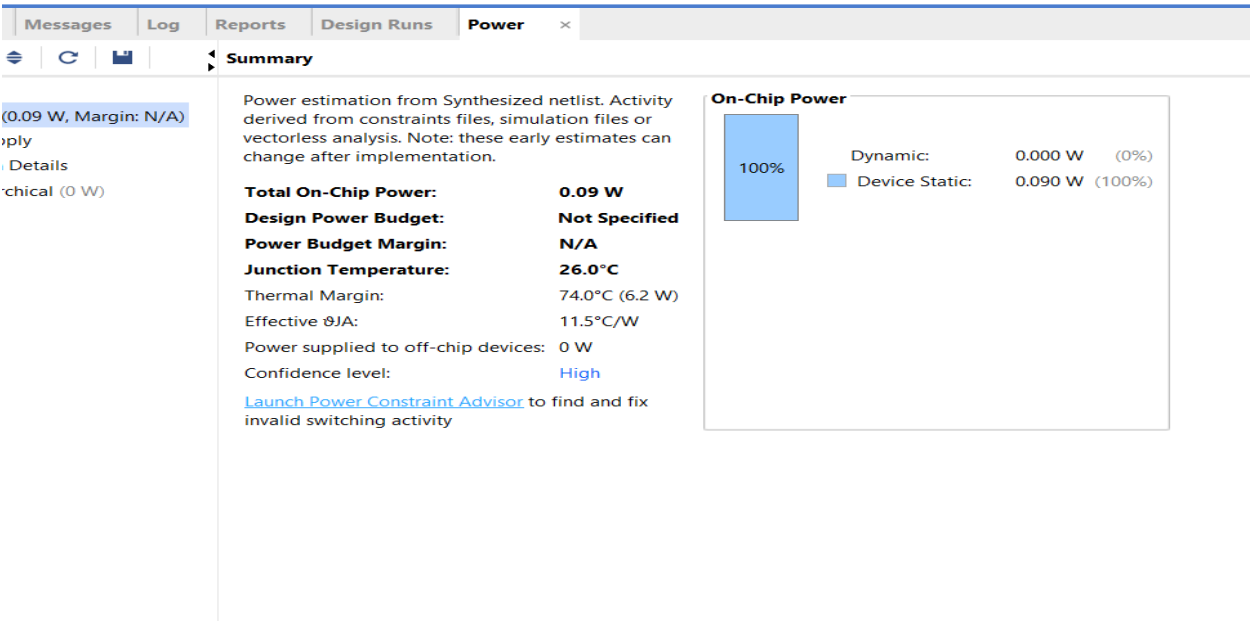


Fig12: Power estimation for N=64

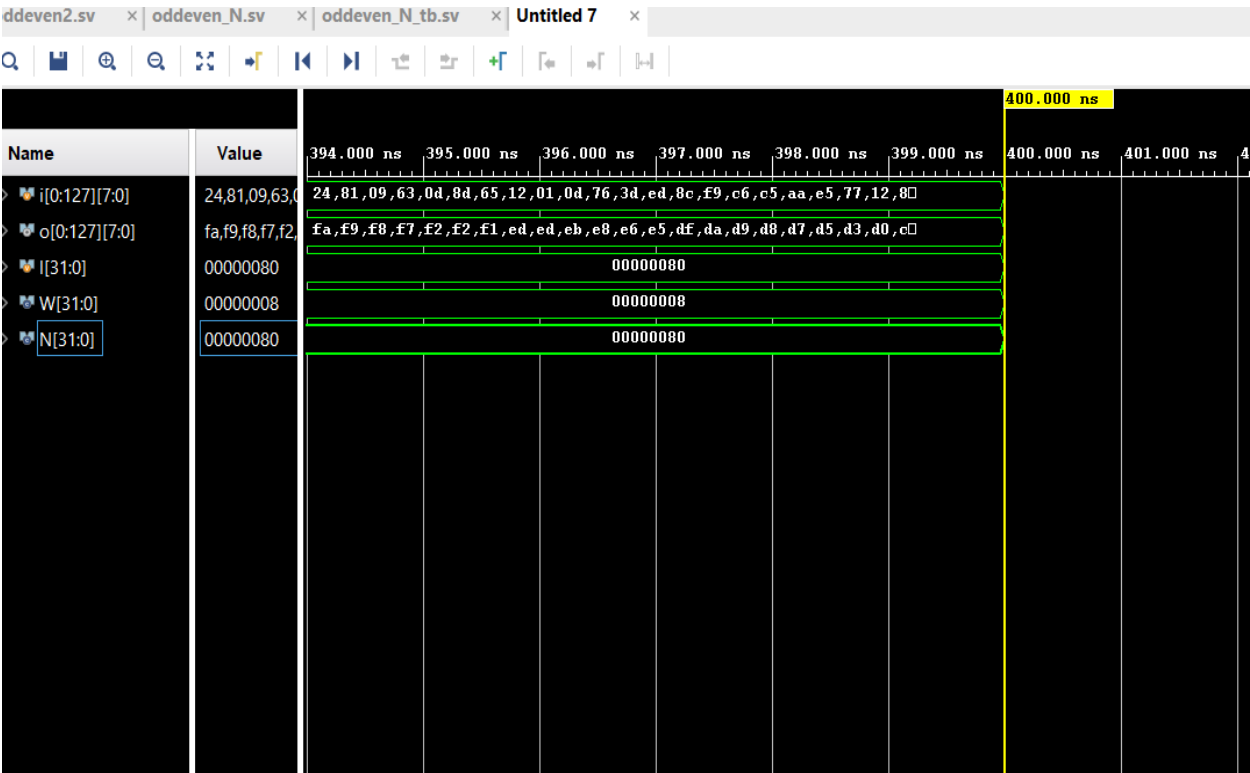


Fig13: Waveform for N=128

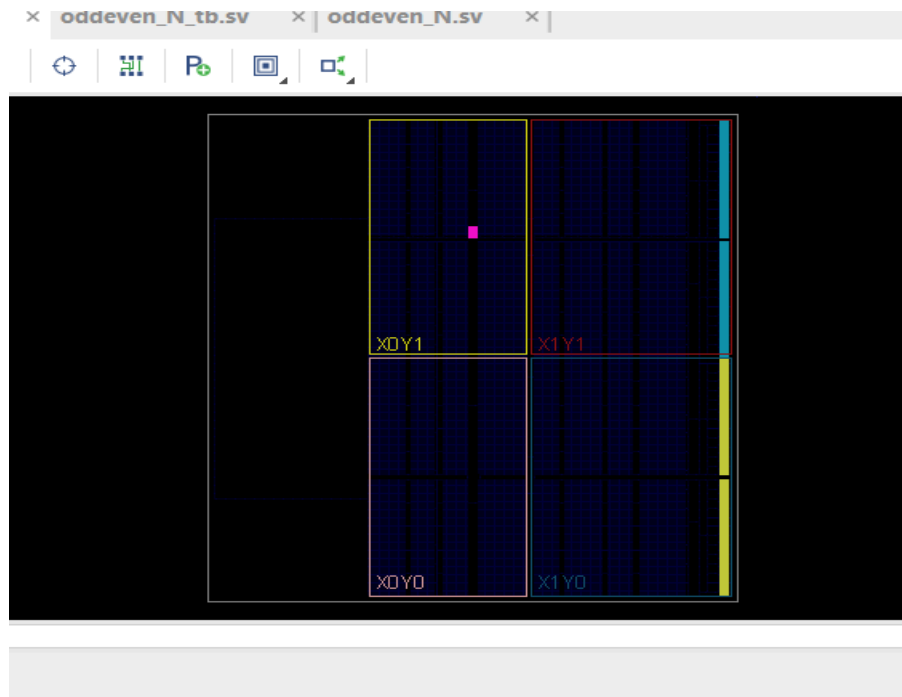


Fig14: Post synthesis schematic of device model for N=128

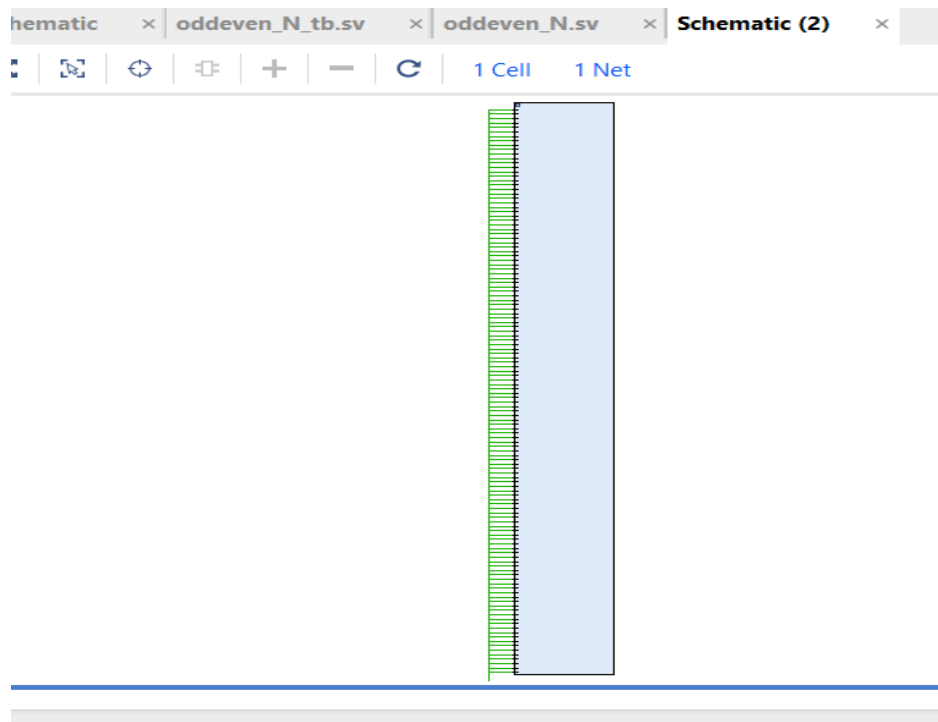


Fig15: Schematic screenshot of module for N=128



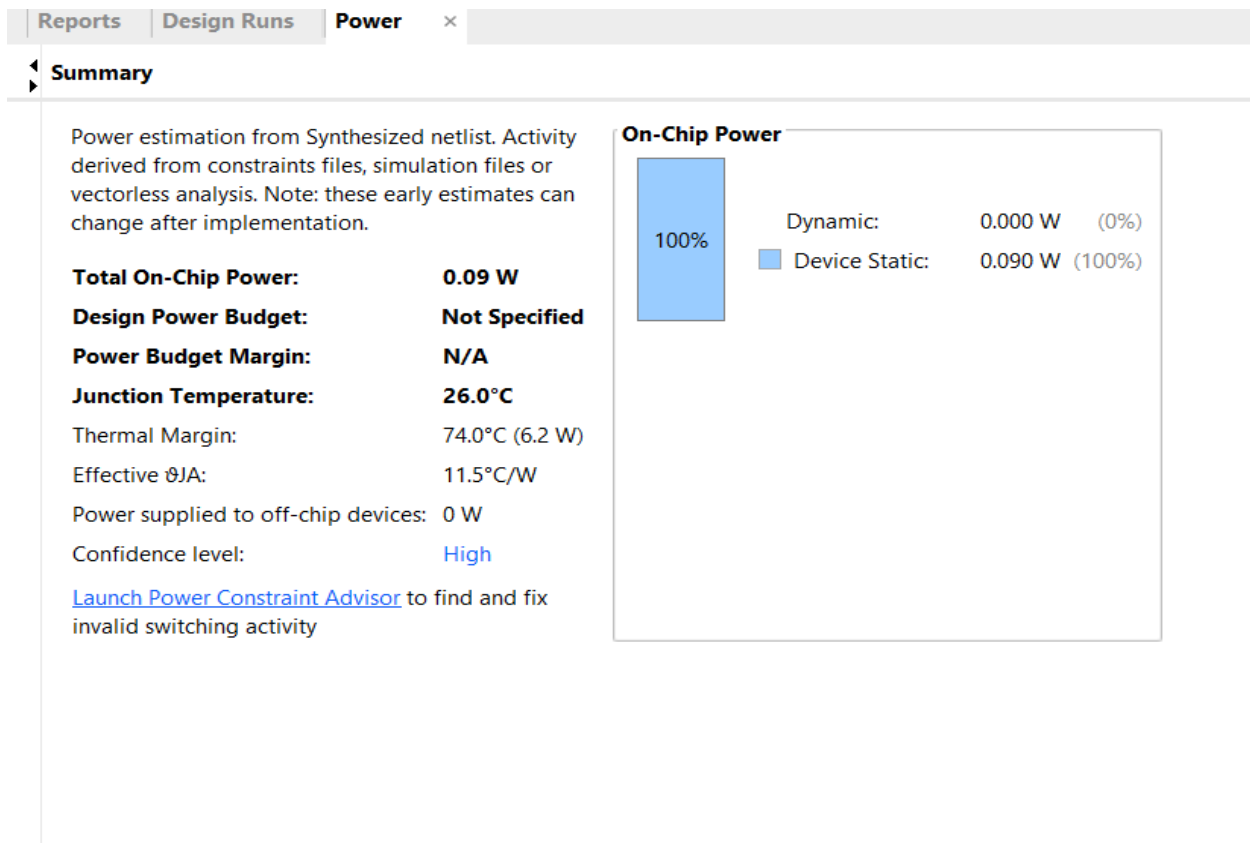


Fig16: Power estimation for N=128