Assignment 2 Report

Github link: https://github.com/RudrenduMahindar/EE599_mahindar_6343999513

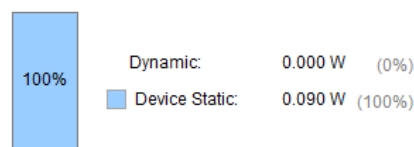Problem1
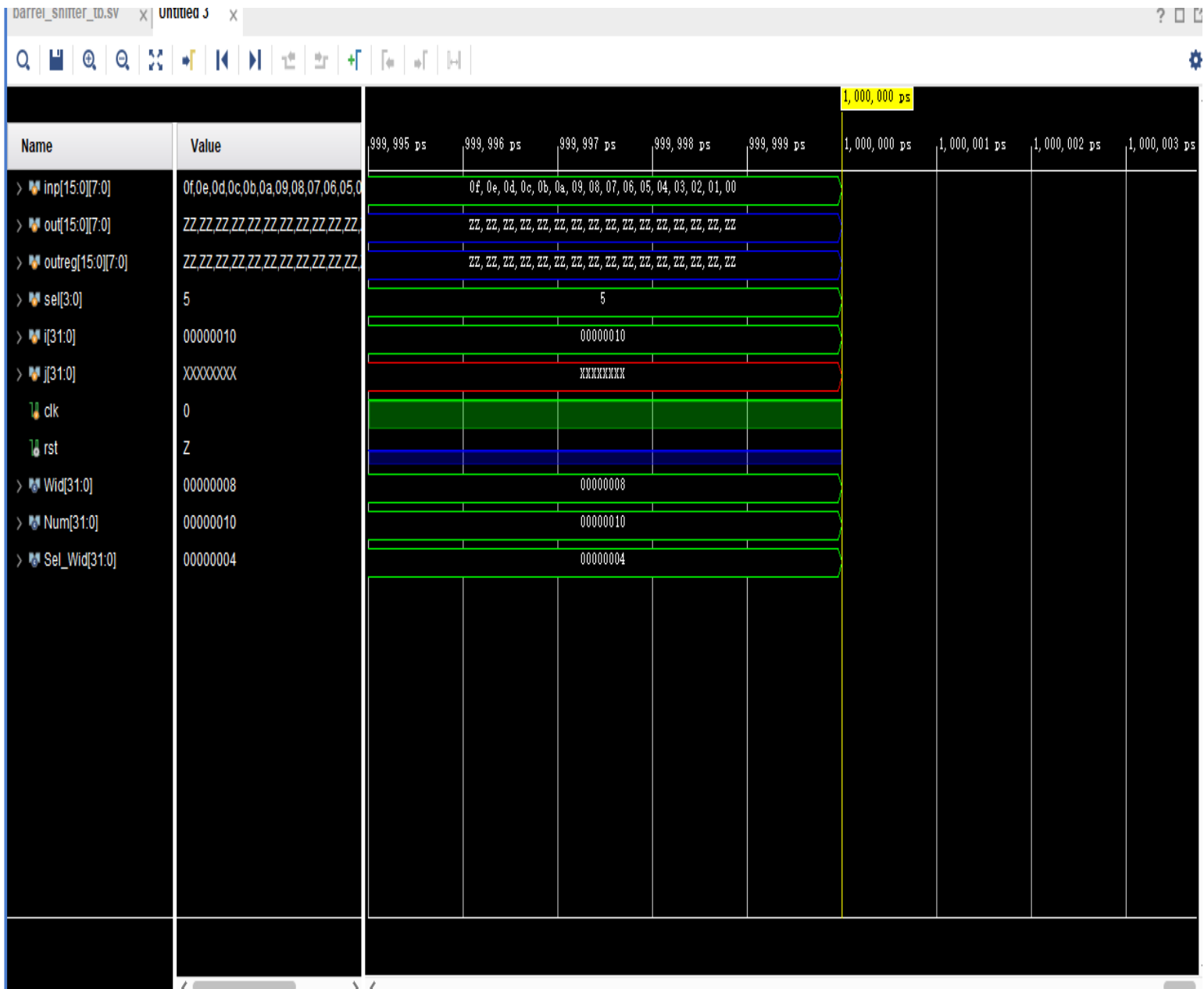
Waveform for N=16
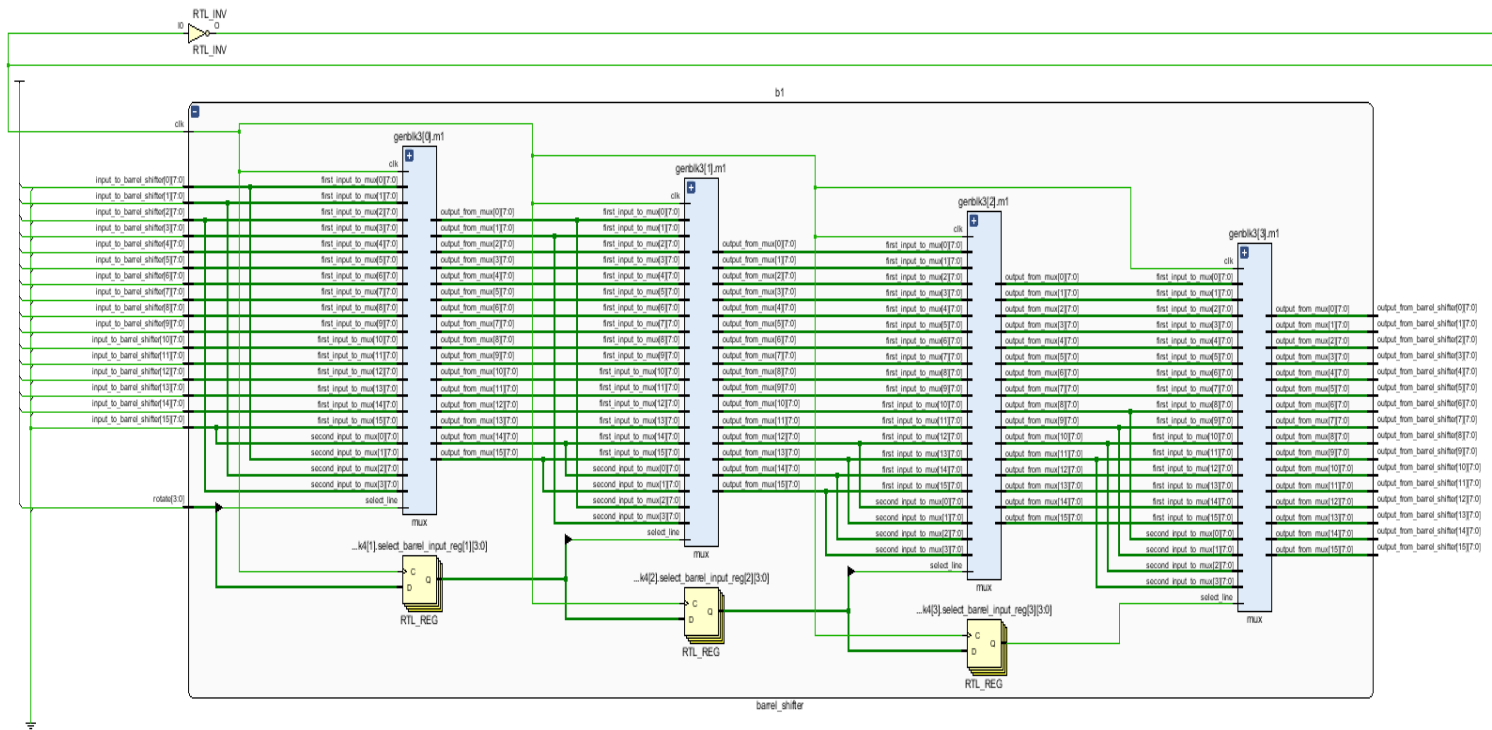
Circuit Schematic for N=16

Summary

Power estimation from Synthesized netlist. Activity derived from constraints files, simulation files or vectorless analysis. Note: these early estimates can change after implementation.

On-Chip Power

	Dynamic:	0.000 W	(0%)
100%	☐ Device Static:	0.090 W	(100%)

Total On-Chip Power:	**0.09 W**
Design Power Budget:	**Not Specified**
Power Budget Margin:	**N/A**
Junction Temperature:	**26.0°C**
Thermal Margin:	74.0°C (6.2 W)
Effective ϑJA:	11.5°C/W
Power supplied to off-chip devices:	0 W
Confidence level:	High

Launch Power Constraint Advisor to find and fix invalid switching activity

Power estimation for N=16

Circuit Schematic for N=64

Summary

Power estimation from Synthesized netlist. Activity derived from constraints files, simulation files or vectorless analysis. Note: these early estimates can change after implementation.
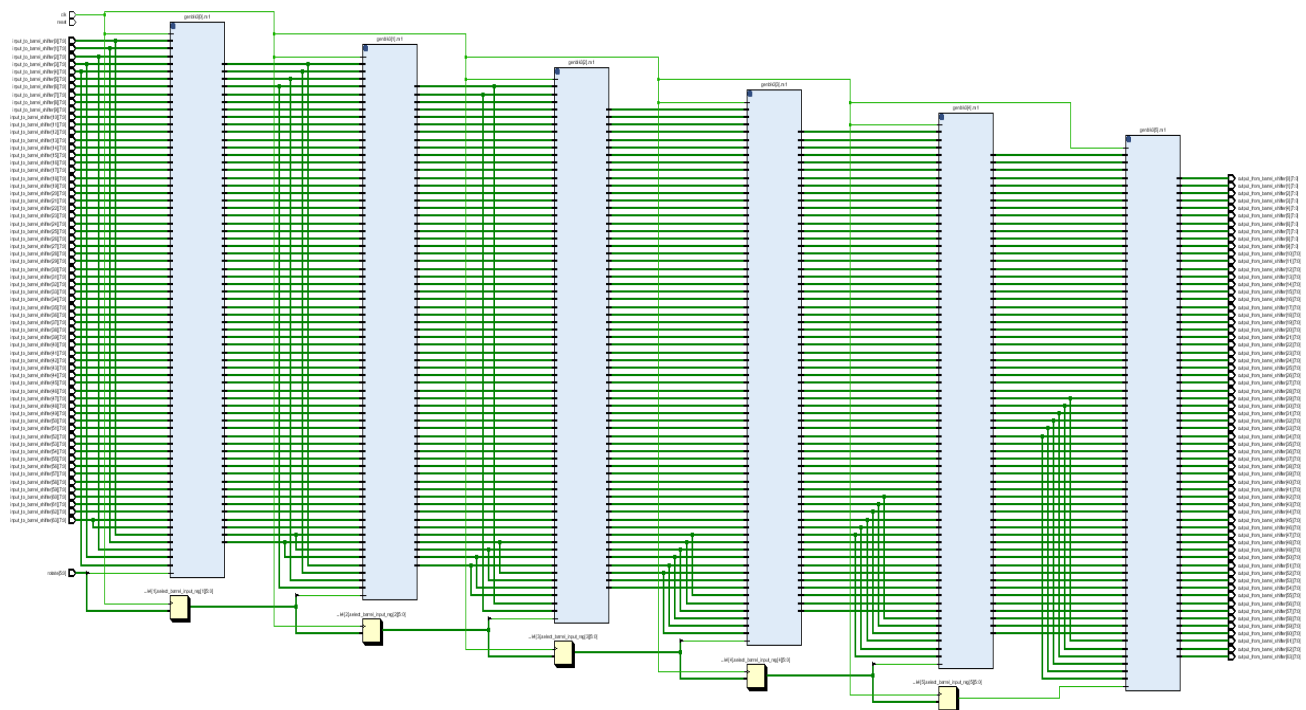
On-Chip Power

Total On-Chip Power:	**0.09 W**	
Design Power Budget:	**Not Specified**	
Power Budget Margin:	**N/A**	
Junction Temperature:	**26.0°C**	
Thermal Margin:	74.0°C (6.2 W)	
Effective ϑJA:	11.5°C/W	
Power supplied to off-chip devices:	0 W	
Confidence level:	High	

100%

Dynamic:	0.000 W	(0%)
■ Device Static:	0.090 W	(100%)

Launch Power Constraint Advisor to find and fix invalid switching activity

Power estimation for N=64