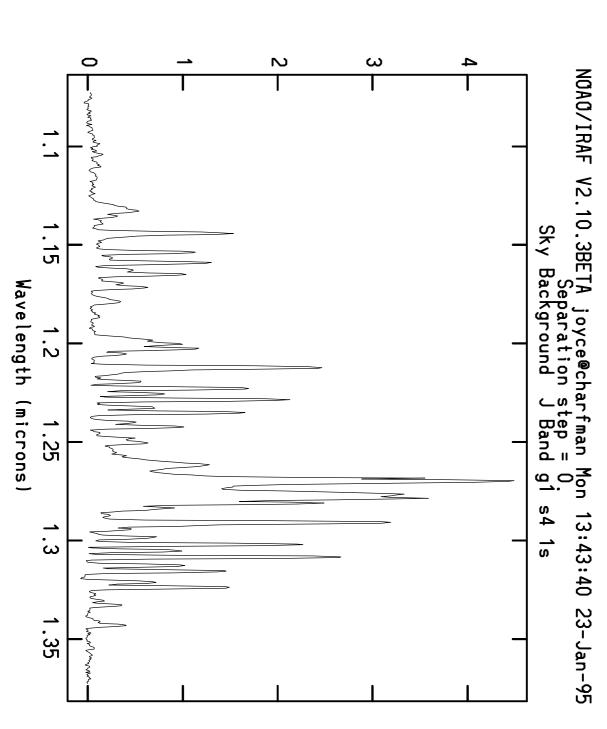
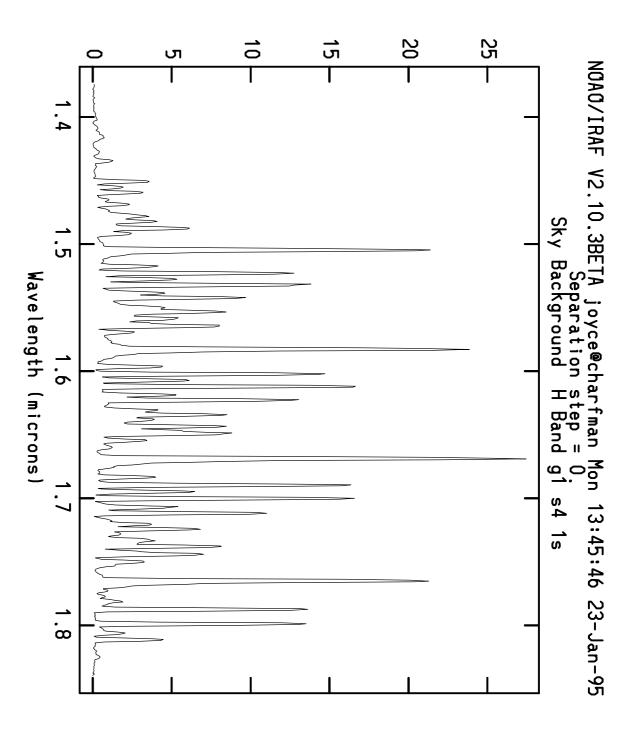
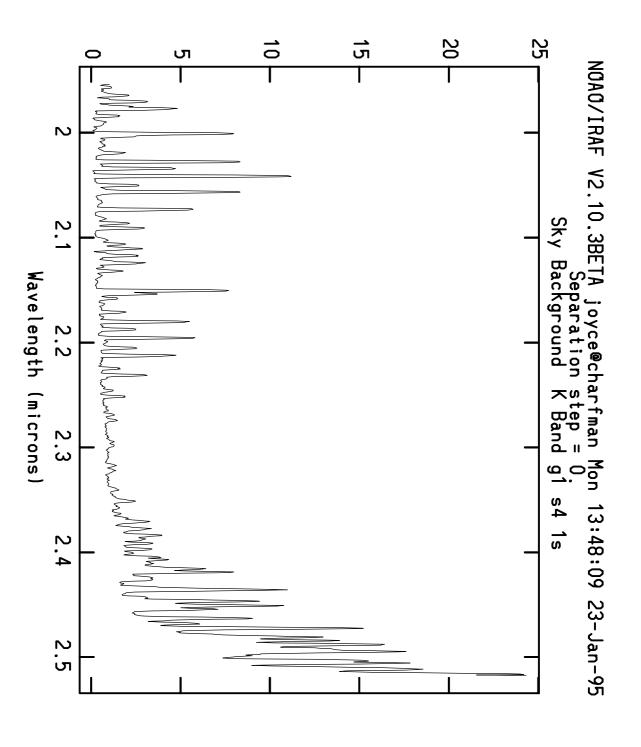
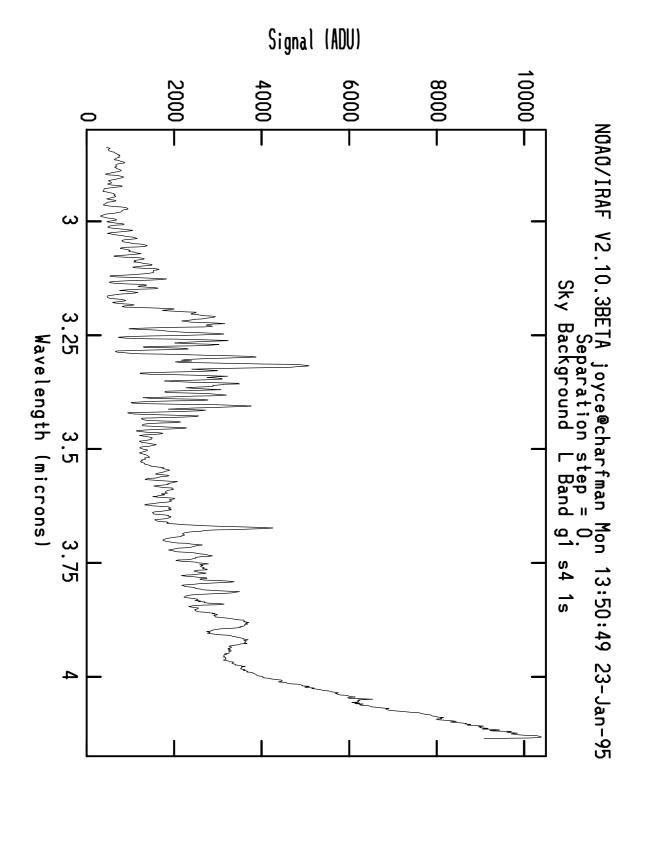
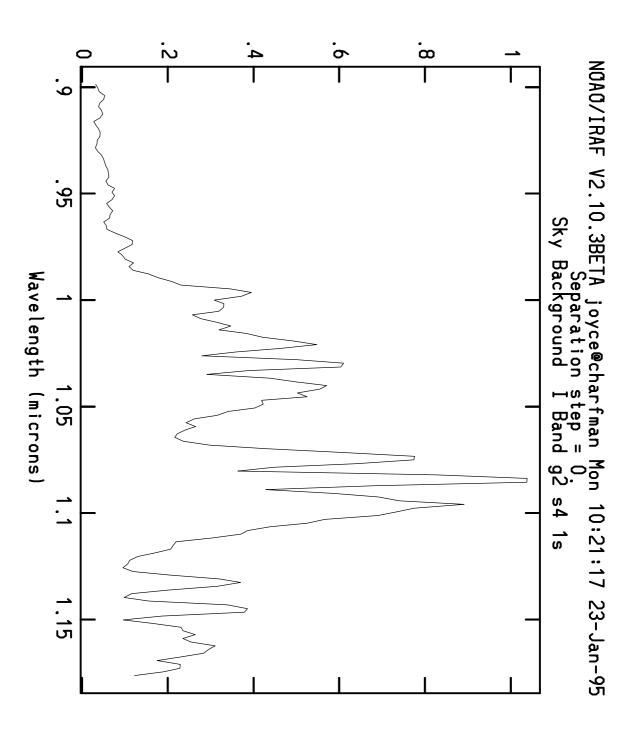
Signal (ADU)



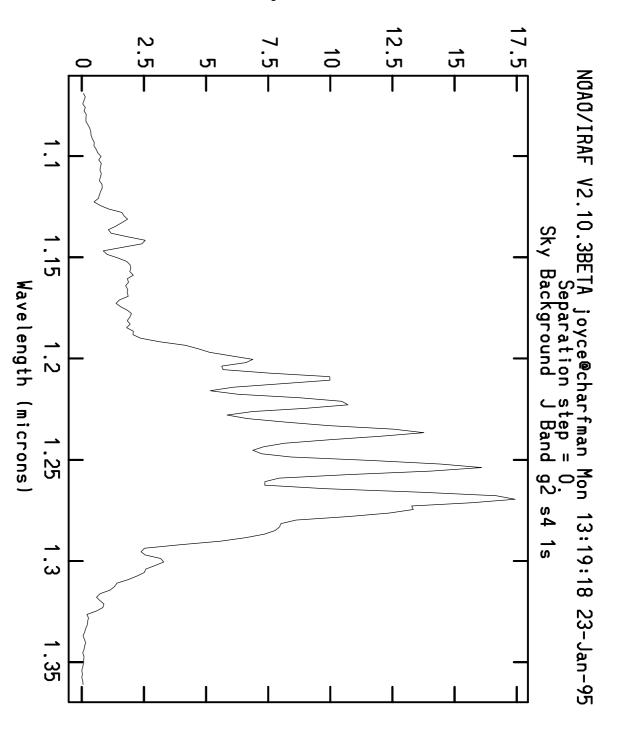


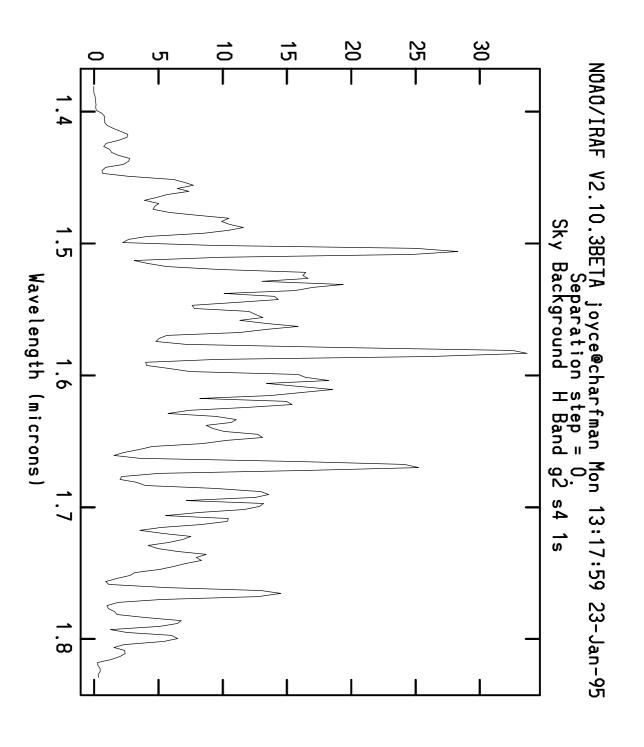




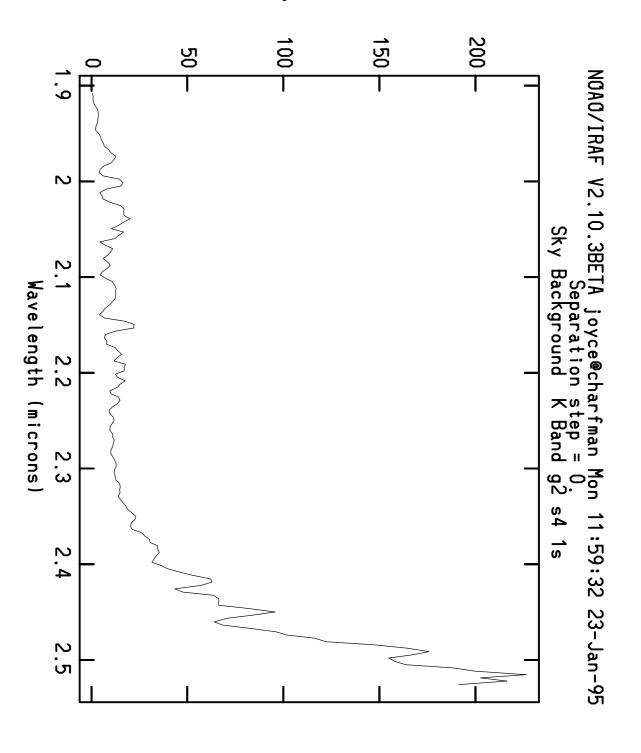


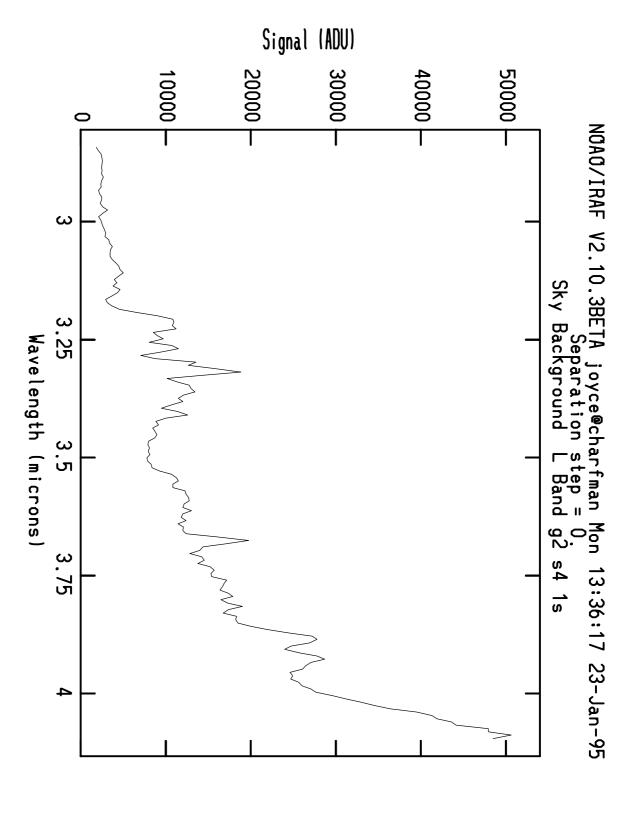
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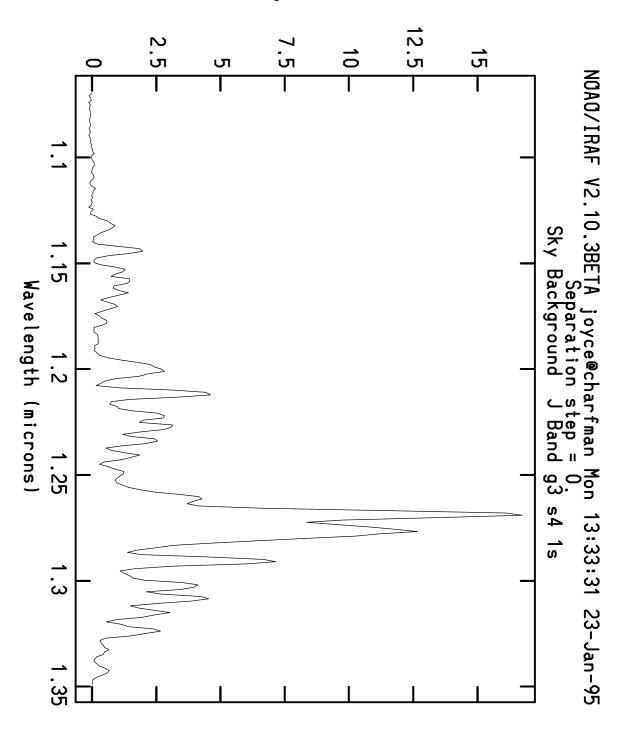


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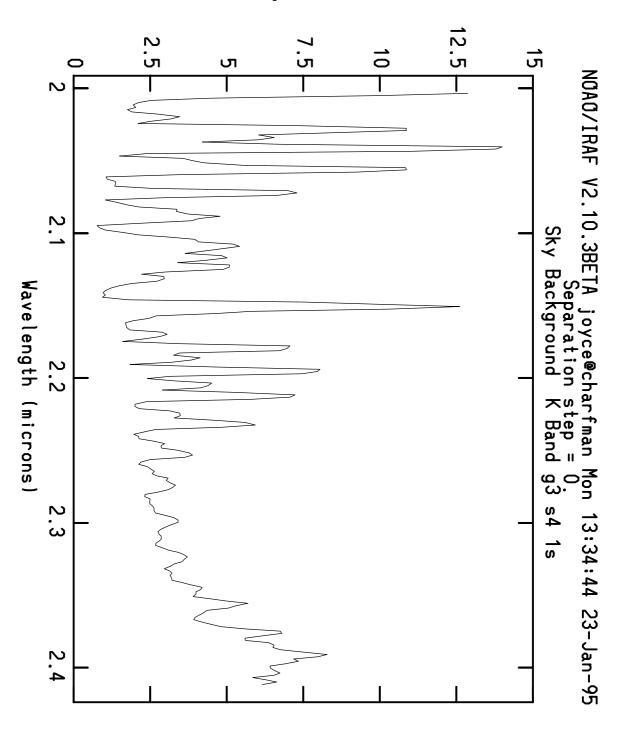


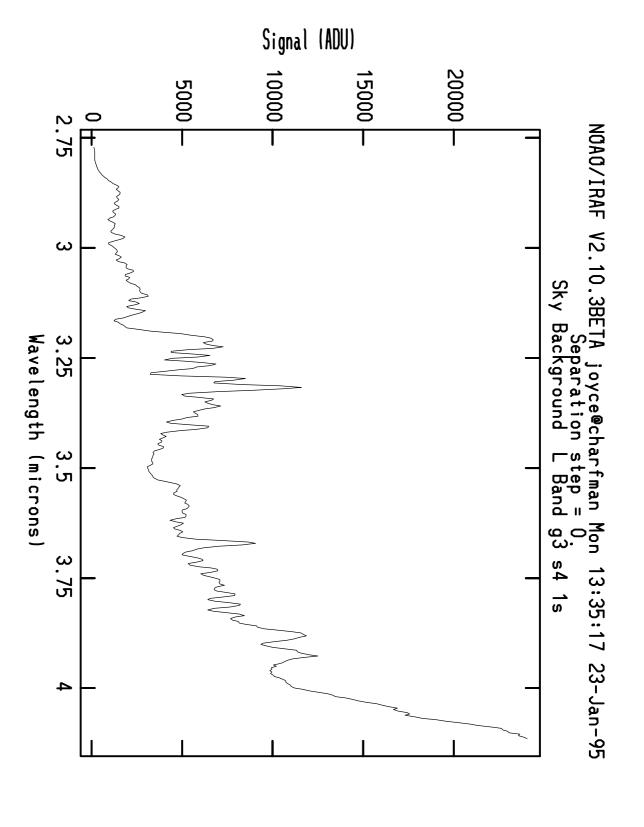


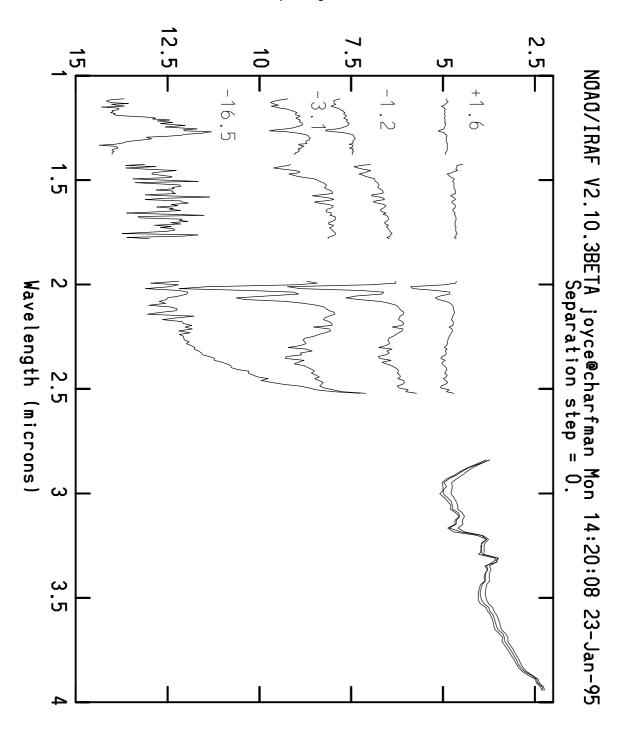
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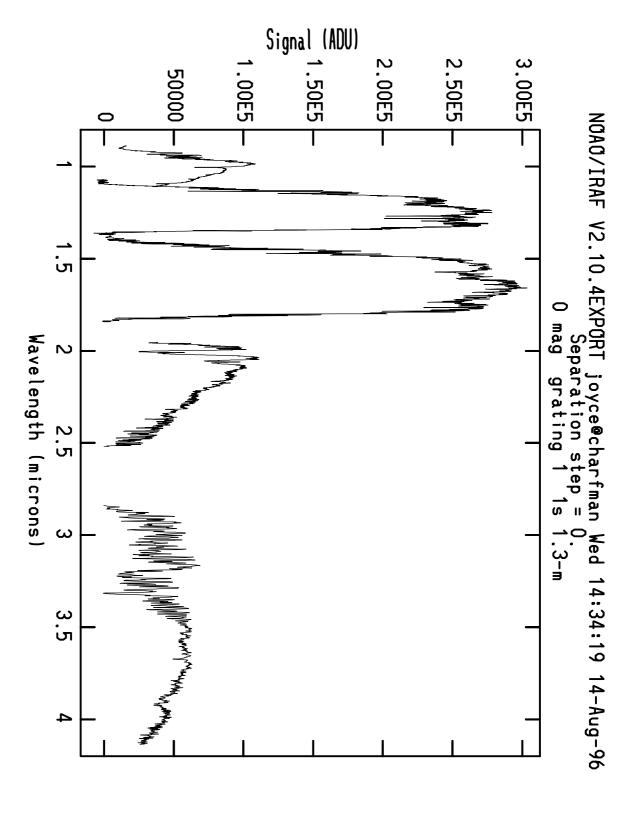


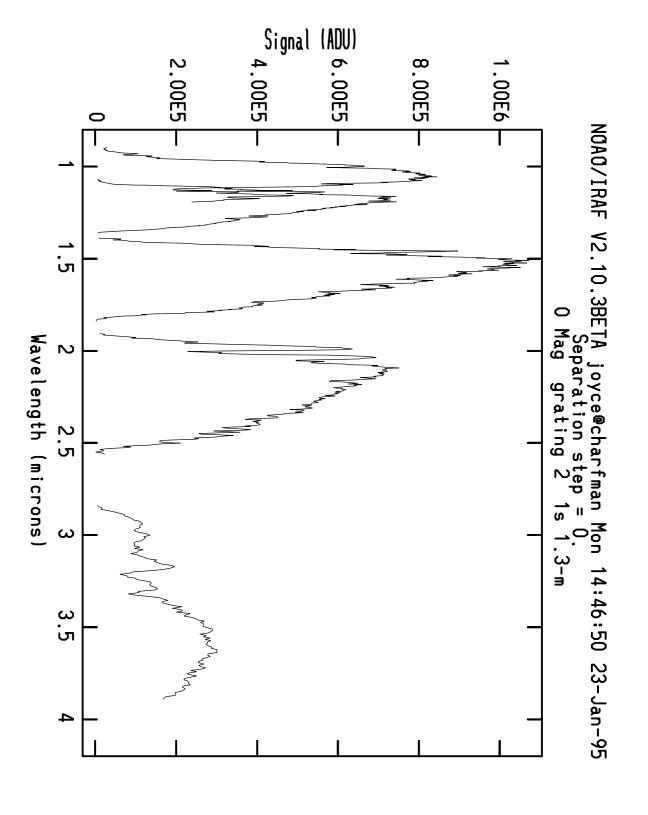
Signal (ADU)

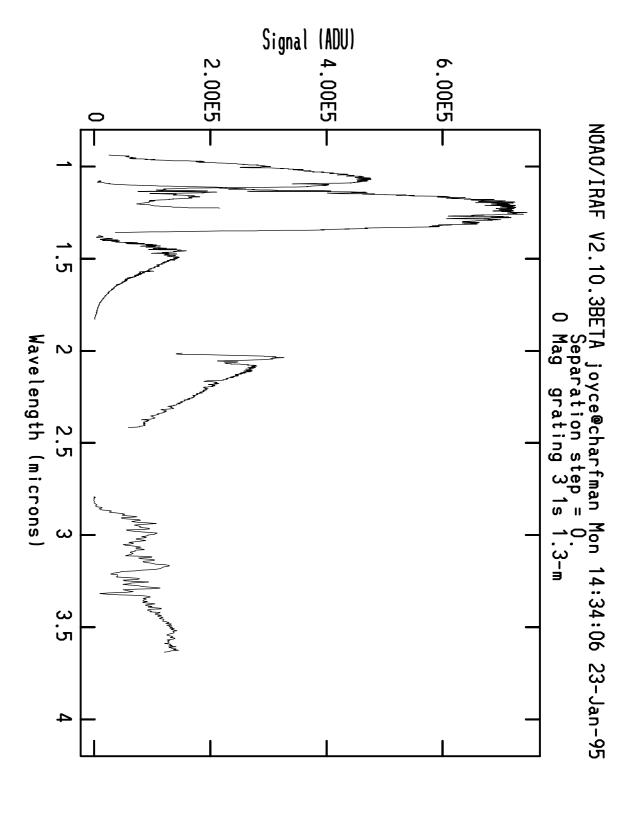


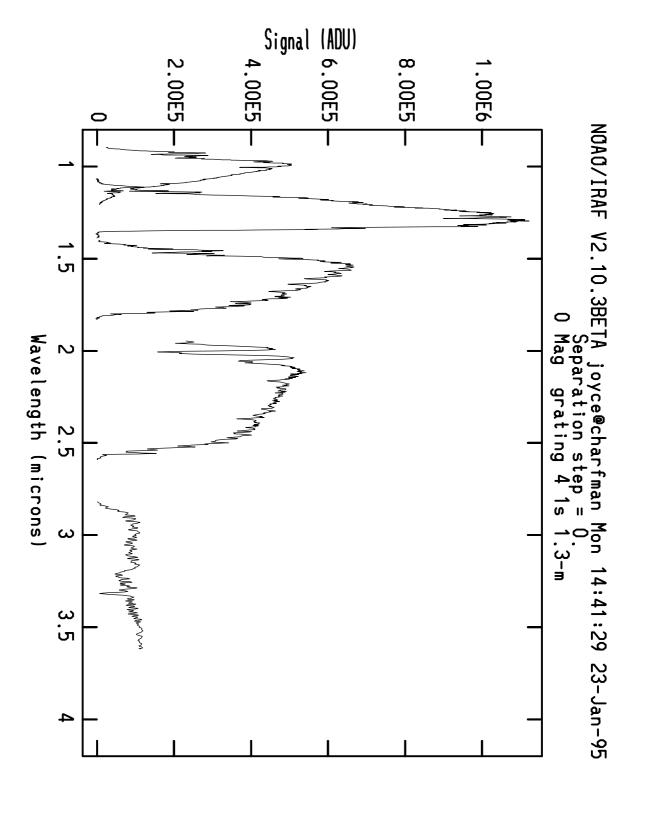


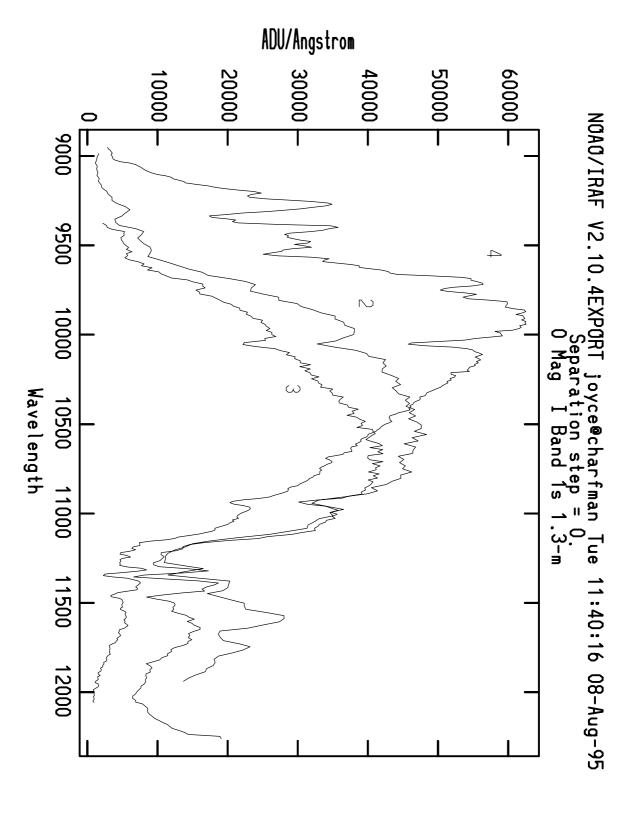


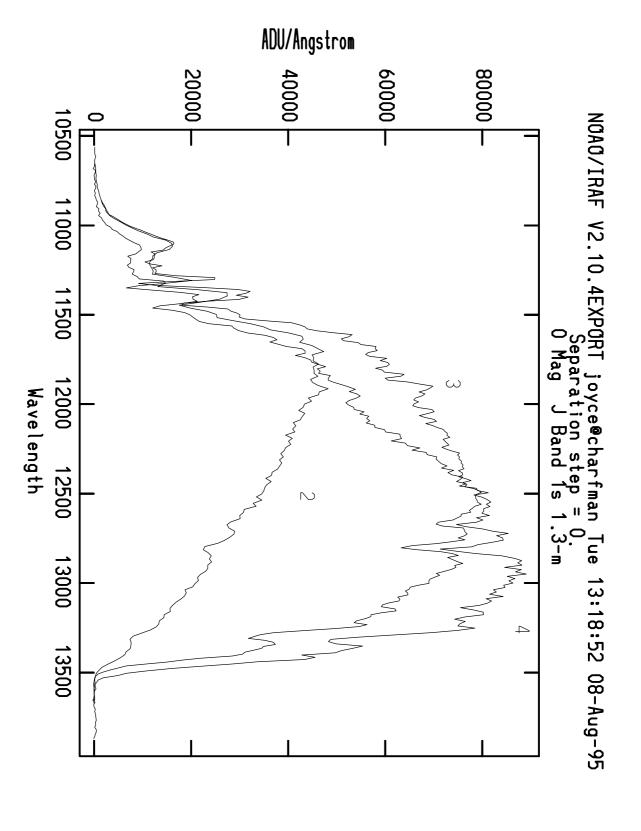


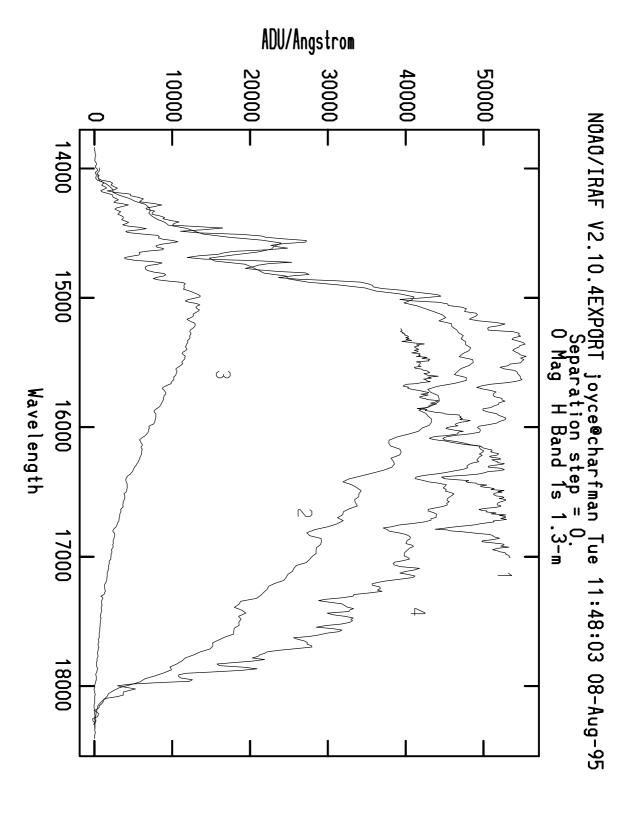


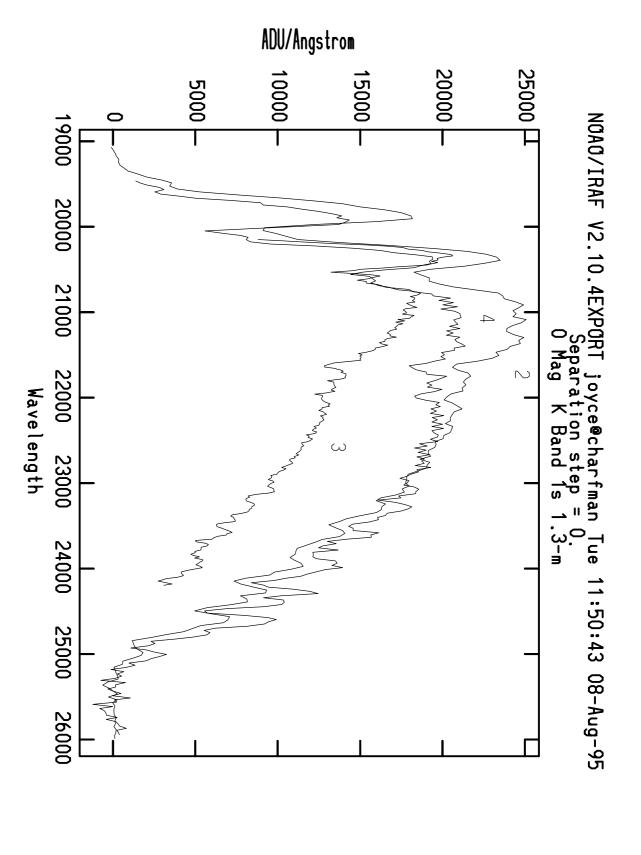


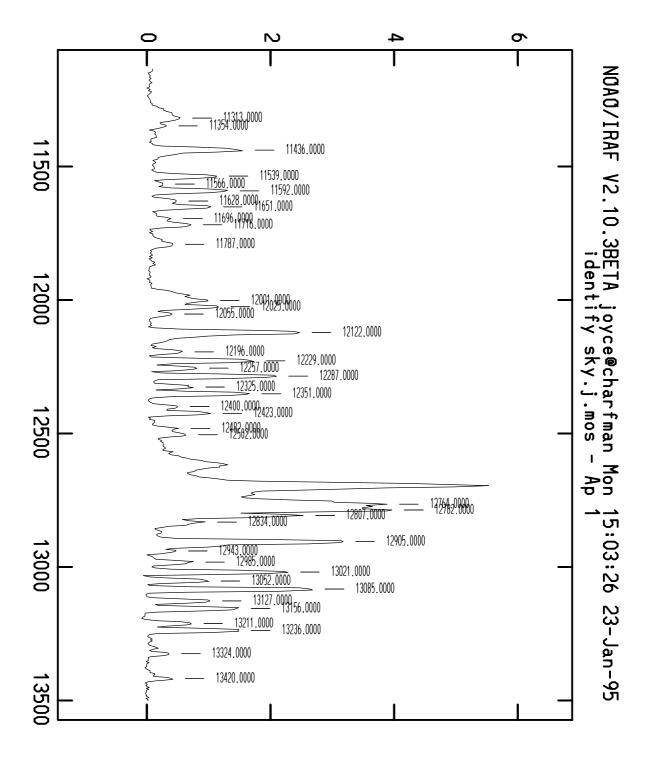


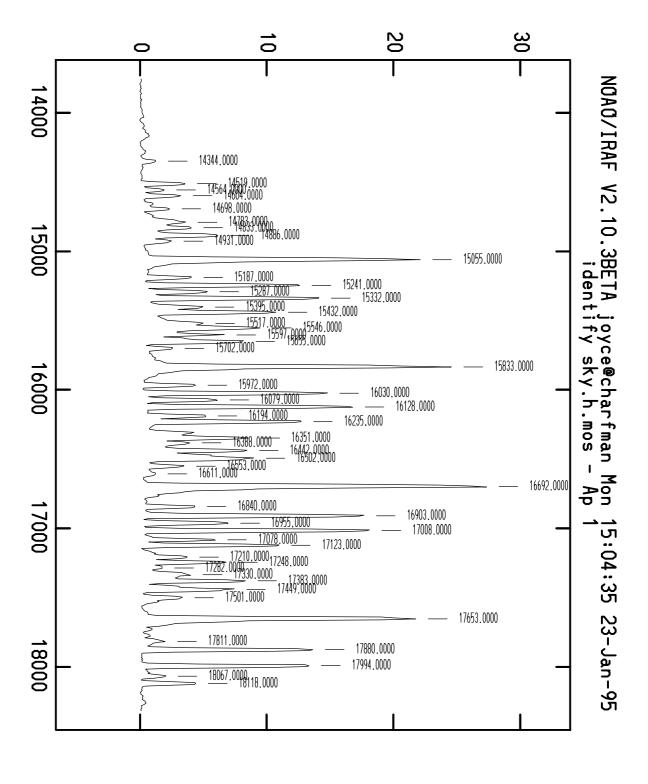


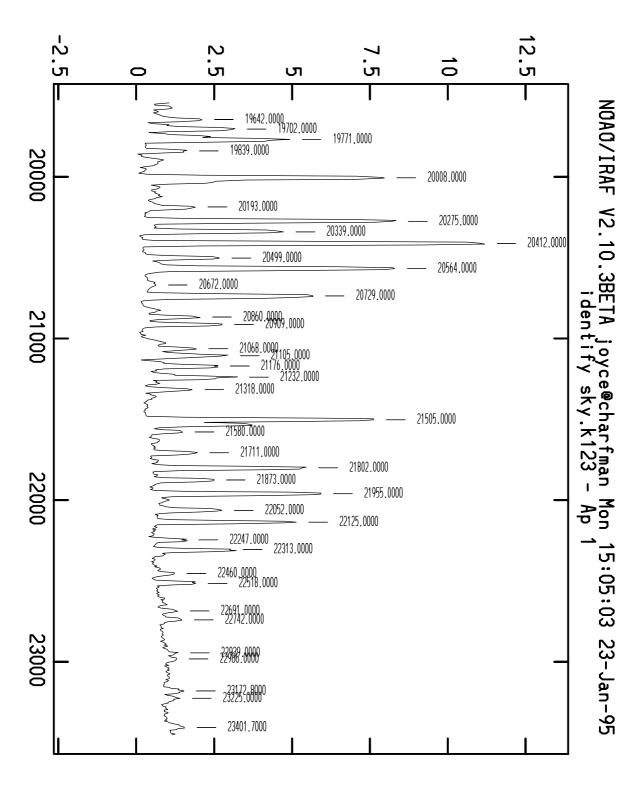


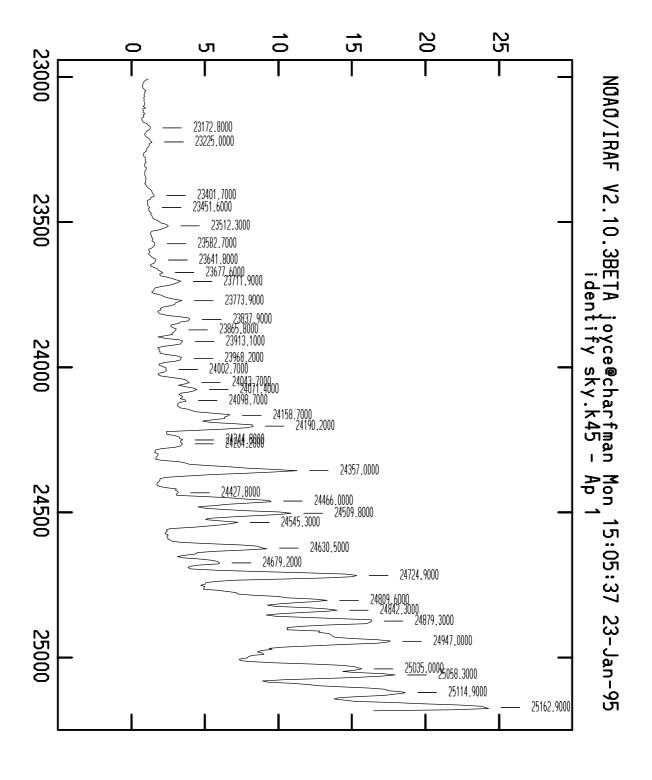


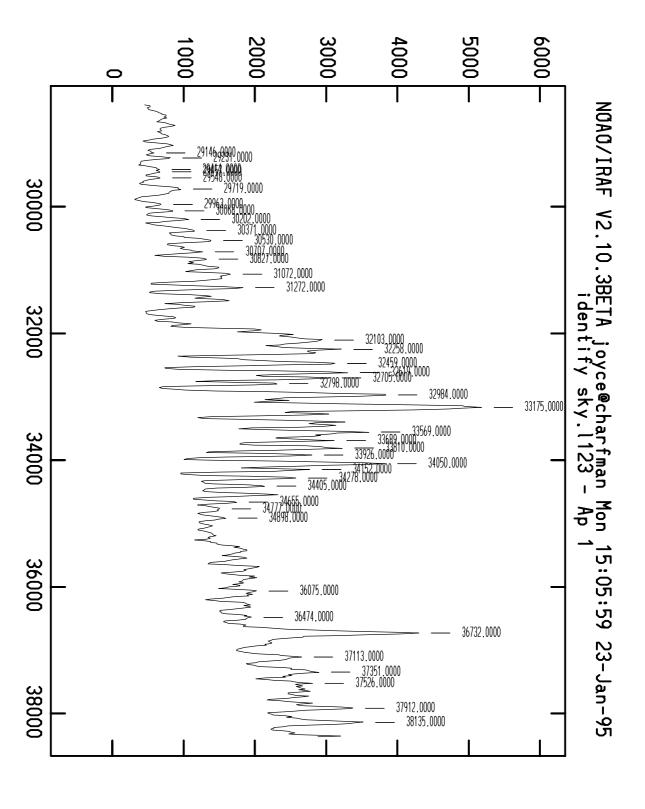


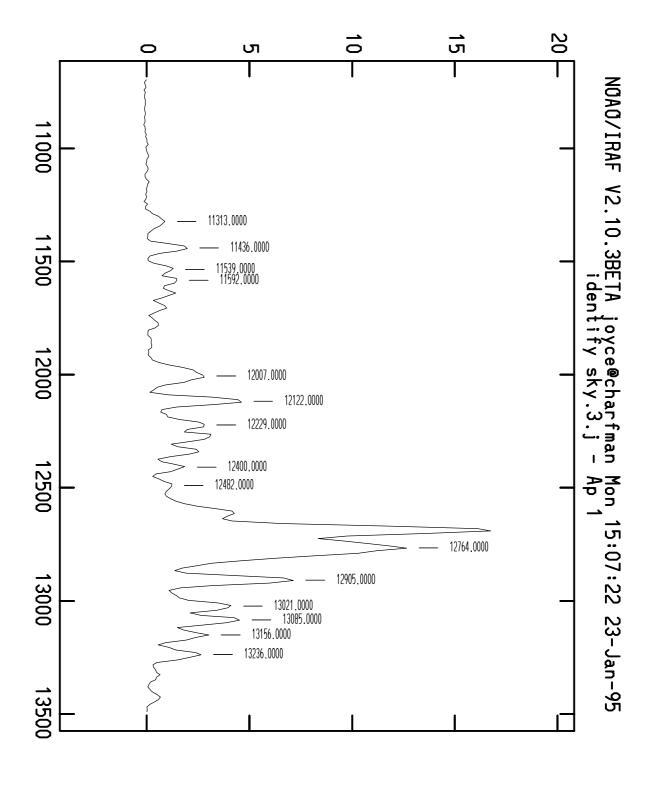


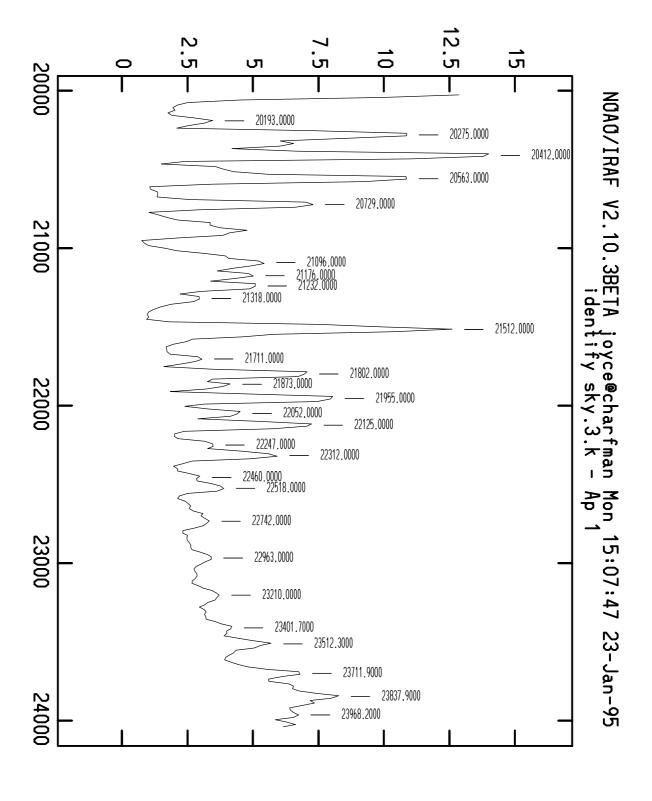












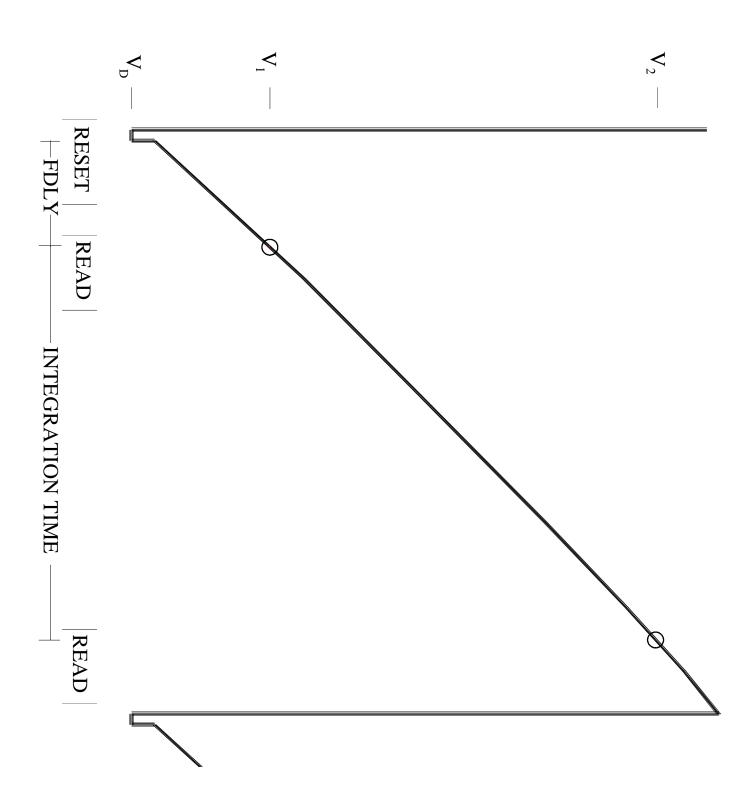
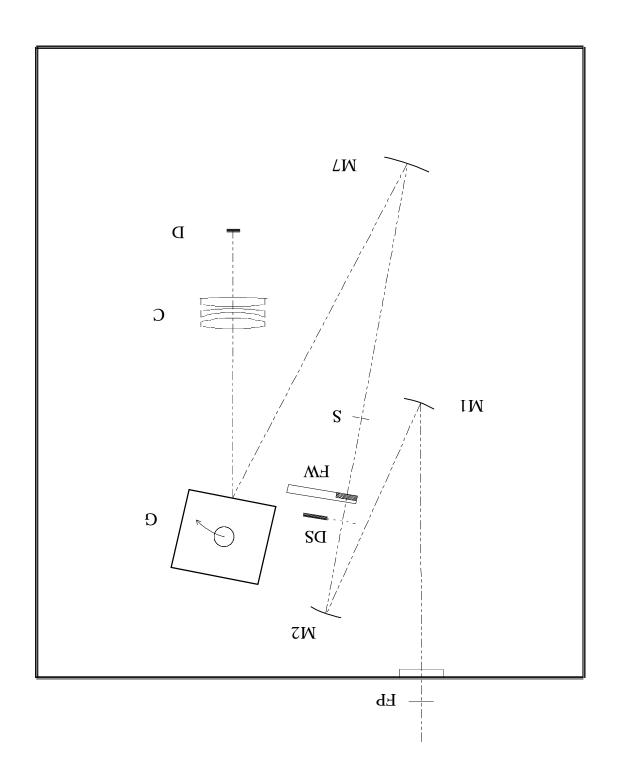


Fig. 1 Schematic representation of the voltage on a single pixel as a function of time. The intervals labeled "reset" and "read" represent the time over which these operations occur on the entire array, and are not to scale.



 $\operatorname{Fig.} 2\,$ Optical layout of CRSP. The elements are identified in the text.

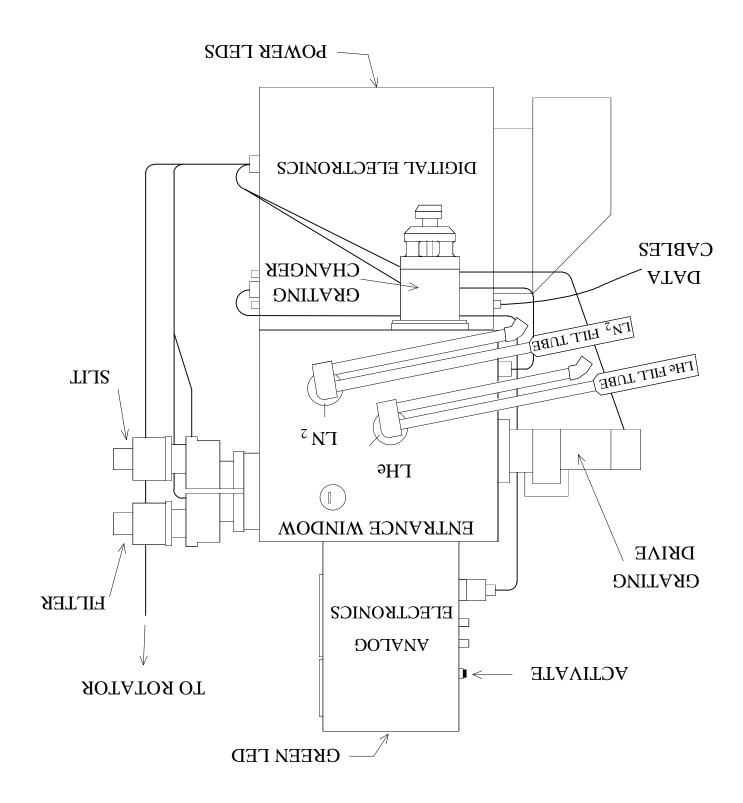


Fig. 3 Top view of CRSP

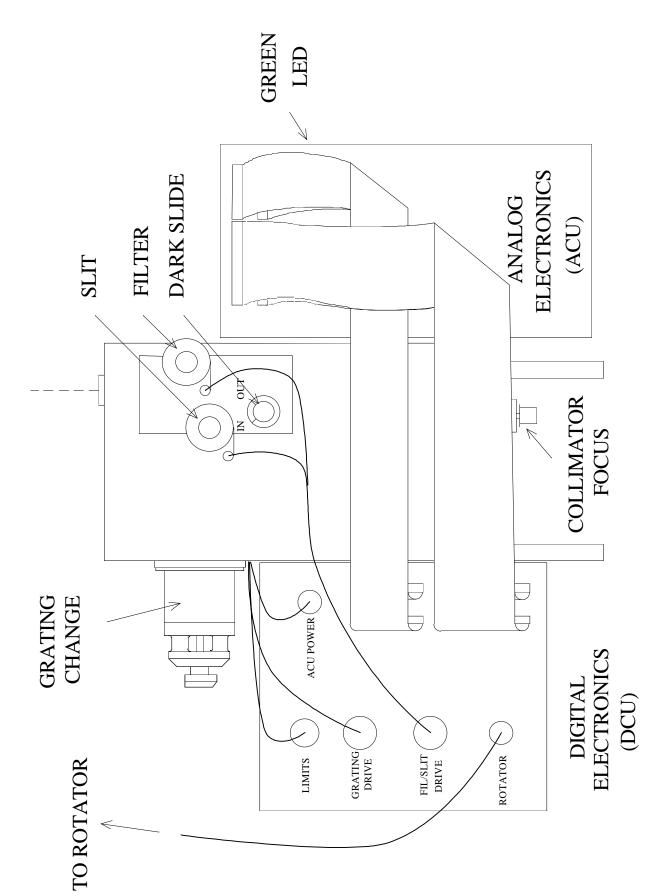


Fig. 4 Side view of CRSP, showing cable connections from DCU to ACU and motors.

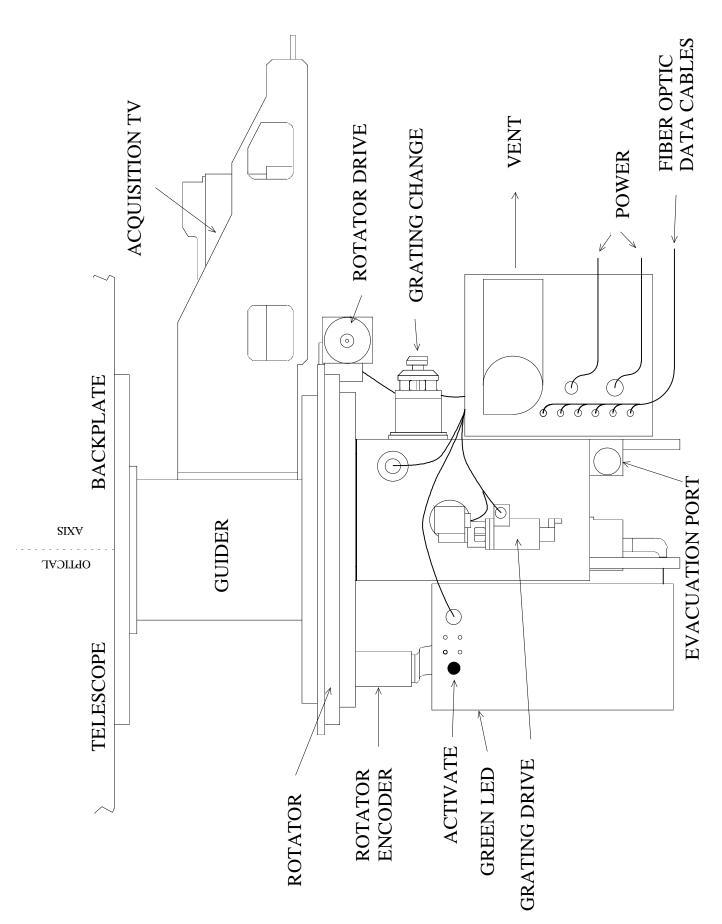


Fig. 5 Side view of CRSP opposite to that in Fig. 4, showing guider installation.

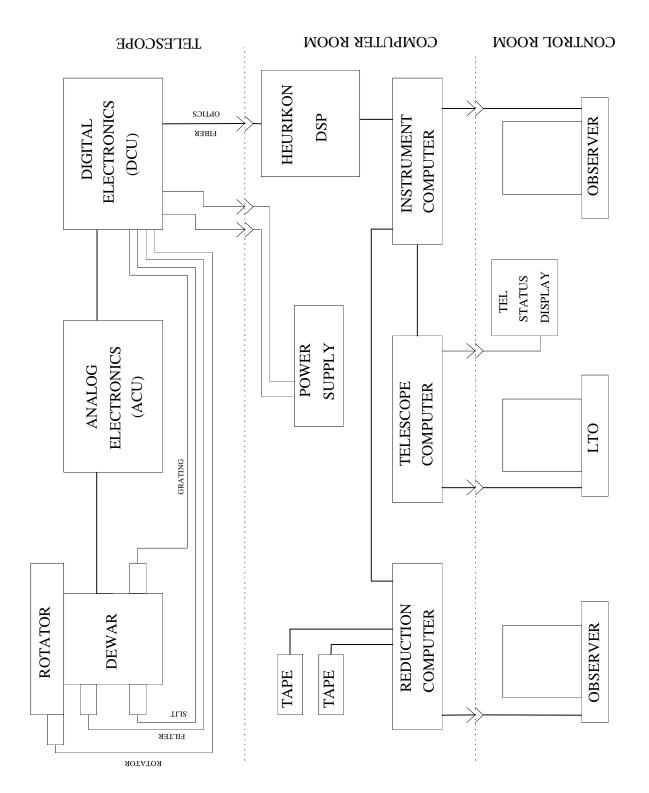


Fig. 6 Schematic configuration of electronics and computers used with CRSP/WILDFIRE

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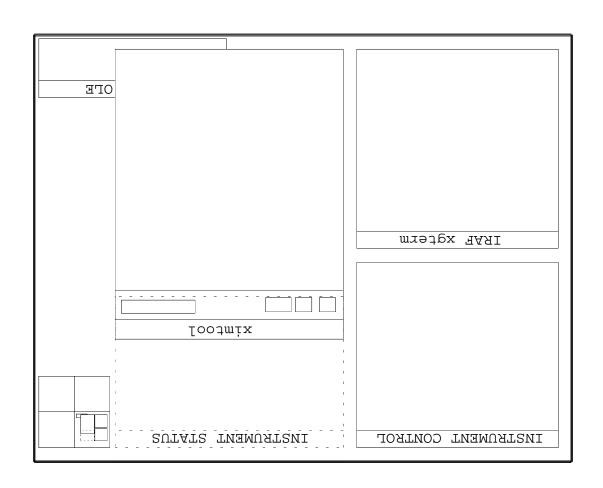


Fig. 7 Windows layout after initiation of WILDFIRE