

# RUIXIANG JIANG

BACHELOR

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EDUCATION	<b>Department of Engineering, Southern University of Science and Technology</b> Shen-zhen, China <i>B.E. in Computer Science and Engineering</i> 2021 - 2025 <ul style="list-style-type: none"><li>GPA: 3.73/4.00</li></ul>
PUBLICATIONS	1. <b>Ruixiang Jiang</b> , Yufan Jia, Juntao You, Yinqian Zhang. Patent on Asterinas IMA (Ap-plying), 2024.
PROJECTS	<b>Supported Firecracker virtualization for Asterinas</b> 2023.09 - 2024.01 <i>Teecert Lab</i> <b>Implemented IMA for Asterinas TDX version</b> 2024.02 - 2024.06 <i>Teecert Lab</i> <b>Built benchmarks for UNSAFE-to-FFM automatic transformation</b> 2024.07 - 2024.09 <i>Prof. Joshua's Lab</i> <b>Implementing HSM for Asterinas</b> 2024.09 - current <i>Teecert Lab</i>
AWARDS AND HONORS	<ul style="list-style-type: none"><li><b>First Prize</b>, SUSTech Freshman Scholarship    2021</li><li><b>Second Prize</b>, SUSTech Outstanding Student Scholarship    2022</li><li><b>Top Ten Volunteers Awards</b>, SUSTech    2022</li><li><b>Second Prize</b>, SUSTech Outstanding Student Scholarship    2023</li><li><b>Outstanding Student Assistant</b>, Engineering College, SUSTech    2024</li><li><b>Team Champion</b>, Engineering College Badminton Competition, SUSTech    2024</li></ul>
SKILLS	<b>Languages:</b> Chinese, English. <b>Programming:</b> Python, C/C++, Rust, Java. <b>Interests:</b> Cybersecurity, Operating Systems, Software Engineering