

# Ruixiang Jiang

Curriculum Vitae

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github [github.com/RuixiangJiang](https://github.com/RuixiangJiang)

## KEY

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♥+ A CSE undergraduate at Southern University of Science and Technology (SUSTech), China

♥+ Interests: security system, software engineering

## EDUCATION

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♥+ **Undergraduate:** Computer Science & Technology, SUSTech

↪ GPA: 3.73/4.00

↪ Learnt courses: Software Engineering, Computer Security, Operating Systems, Computer Networks, Compilers, Embedded Systems and Microcomputer Principle, etc.

## WORK EXPERIANCE

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♥+ Fall 2021: entered SUSTech

♥+ Fall 2022: entered **Professor Yinqian**'s lab

♥+ Summer 2024: entered **Professor Joshua**'s lab for UCInspire

## PROJECTS

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♥+ Fall 2023: supported Firecracker virtualization for Asterinas platform

♥+ Spring 2024: a paper and a patent about implementing IMA for Asterinas (still in progress)

♥+ Summer 2024: tried to transform projects using UNSAFE to FFM API

## ACHIEVEMENTS

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♥+ First Prize, National Olympiad in Informatics in Provinces (NOIP, China) Senior, 2016

♥+ First Prize, SUSTech Freshman Scholarship, 2021

♥+ Second Prize, SUSTech Outstanding Student Scholarship, 2022

♥+ Top Ten SUSTech Volunteer Awards, 2022

♥+ Second Prize, SUSTech Outstanding Student Scholarship, 2023