

Ruixiang Jiang

Curriculum Vitae

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github github.com/RuixiangJiang

KEY

- ♥+ A CSE undergraduate at Southern University of Science and Technology (SUSTech), China
- ♥+ Research interests: Security, Operating Systems, Software Engineering
- ♥+ Language skills: C/C++, Java, Python, Rust

EDUCATION

- ♥+ **Undergraduate:** Bachelor of Engineering (Computer Science & Technology, SUSTech)
- ~> GPA: 3.73/4.00
- ~> Learnt courses: Software Engineering, Computer Security, Operating Systems, Computer Networks, Compilers, Embedded Systems and Microcomputer Principle, etc.

WORK EXPERIENCE

- ♥+ Fall 2021: Entered SUSTech
- ♥+ Fall 2022: Entered [Professor Yinqian](#)'s lab
- ♥+ Summer 2024: Entered [Professor Joshua](#)'s lab for [UCInspire](#)

PUBLISH

- ♥+ Fall 2024: Patent on Asterinas IMA (Applying)
- ~> The file is written in Chinese, shown [here](#).

PROJECTS

- ♥+ Fall 2023: Supported Firecracker virtualization for Asterinas platform
- ~> Firecracker is an open-source virtualization technology developed by Amazon, tailored for the modern cloud computing landscape. At the same time, Asterinas is a secure, fast, and general-purpose OS kernel, written in Rust and providing Linux-compatible API. The purpose is to support Firecracker virtualization for the Asterinas platform.
- ~> Source: <https://github.com/RuixiangJiang/2023Fall-CS321-Project>
- ♥+ Spring 2024: Implemented IMA for Asterinas TDX version
- ~> Integrity measurement and attestation mechanisms play a pivotal role in ensuring the trustworthiness and security of computing systems. It explores the application of such mechanisms, focusing on the case study of Asterinas, a representative system. Through a comprehensive analysis of Asterinas, this study elucidates the intricacies of implementing and leveraging integrity measurement and attestation support. Furthermore, it discusses the challenges and advancements in this domain. The insights derived from Asterinas serve as valuable guidelines for enhancing integrity assurance in various computing environments.
- ~> Source: <https://github.com/CXSJ2024/asterinas/tree/jrx>
- ♥+ Summer 2024: Built benchmarks for UNSAFE-to-FFM automatic transformation

↪ Some Java projects used UNSAFE to deal with problems relative to objects' addresses. However, OpenJDK published FFM API in Java 22, which was potentially able to replace UNSAFE. The purpose is to develop an automatic tool to do the transformation. My work was building benchmarks for such an automatic tool.

↪ Source: <https://github.com/Software-Aurora-Lab/Benchmarks-for-LLM>

ACHIEVEMENTS

- 🏆 First Prize, National Olympiad in Informatics in Provinces (NOIP, China) Senior, 2016
- 🏆 First Prize, SUSTech Freshman Scholarship, 2021
- 🏆 Second Prize, SUSTech Outstanding Student Scholarship, 2022
- 🏆 Top Ten SUSTech Volunteer Awards, 2022
- 🏆 Second Prize, SUSTech Outstanding Student Scholarship, 2023
- 🏆 Team Champion, Badminton competition hold by Engineering College, SUSTech, 2024