PreDP revision questions Answers for Section B of the exam

Unit 5 and 6: Electricity and Electronics

(a) (i	0 (A) / zero Unit penalty if wrong unit	B1
(ii	12 V	B1
(b) (i	V/R OR $V = IR$ in any form, letters, words or numbers 0.5 A	C1 A1
(iii)	8 × candidate's (i) OR 8/24 × 12 4 V OR 4.0 V e.c.f.	C1 A1
5. 12	R_1 + 1/ R_2 = 1/ R OR R = R_1R_2 / (R_1 + R_2) in any form 3 (Ω) OR 5½ (Ω) OR 16/3 (Ω) 2 / candidate's R 25 A c.a.o.	B1 C1 C1 A1
Al	ternatively: 12/16 (= 0.75) OR 12/8 (= 1.5) 12/16 (= 0.75) AND 12/8 (= 1.5) Currents added 2.25 A c.a.o.	C1 C1 C1 A1 [10]

(a)	A B	NOT or inverter AND	B1 B1
(b)	(acc	cept 1 or ON for HIGH, and 0 or OFF or NOT HIGH for LOW throughout)	
	(i)	A – HIGH and B – LOW (both) no e.c.f.	B1
	(ii)	A – HIGH and B – HIGH (both) no e.c.f.	B1
	(iii)	A – LOW and B – LOW (both) no e.c.f.	B1
(c)	(i)	B cannot provide enough power / current for lamp, or equiv. OR allows remote lamp	B1
	(ii)	the second one / dark and warm / HIGH, HIGH e.c.f. from (b)	B1
	(iii)	warning if temperature in a closed / dark space (e.g. refrigerator, kiln) reaches too high a value	
		N.B. "to switch on a lamp when it is dark and warm" not accepted	B1
			[8]
(a)	NO	T or inverter	B1
(b)	(i)	thermistor NOT thermal resistor	B1
	(ii)	resistance increases OR voltage across it increases	B1
(c)	(i)	LOW or 0 or off or NOT HIGH	B1
	(ii)	(much) larger/ large / higher / high	B1
	(iii)	low temperature e.c.f. from (c) (ii)	B1
(d)	to a	allow adjustment of the temp. at which relay will close / heater comes on	B1
(e)		omatic control or wtte of heating system / air-conditioning / automatic room heate thermostat	er
	OR	any other sensible suggestion involving control of heating	<u>B1</u>

(a)	dio	de			B1
(b)	(i)	2 Ω			В1
	(ii)	24 OR 2	$2+2(\Omega)$ seen		C1
		1 / R = 1	$/R_1 + 1/R_2 (+ 1/R_3)$ OR $(R =) \frac{R_1 R_2}{R_1 + R_2}$		
		seen or	used with any 2 resistors xtra resistance added to expression for R in equation		C1
		6 Ω			A1
(c)	N.B	s. marks n	nay be scored anywhere in (c)		
	(cui	rrent =) ze	ero / <u>very</u> small		M1
	OR		e biased wrong OR facing wrong way nly conducts R / + to L / -		A 1
(d)	use OR	of R = 8 (R = 4 / 0.	OR P = VI OR P=V ² /R symbols, numbers or words Ω) & correct calculation to give 2W $S=8~(\Omega)$ OR R = $4^2/2=8~(\Omega)$ r calculation(s) using (I = V / R & P = VI) OR P = V ² /R to deduce 8 (M1 Ω) M1	
			n B (NOTE: this is dependent on both M1s being scored) lculations using 2 Ω	<u>A1</u>	[10]
(a)	ana	logue	any reading possible/ <u>idea of continuous</u> variation of value of quantity		B1
	digi	tal	idea of two states only		B1
(b)			are 1/high, the output is 1/high		B1
	OR	if either	previous line or both inputs are 0/low, then output is 0/low answers in form of a truth table)		B1

(a)	(E =) Pt symbols or numbers OR 100 × 13 × 3600 OR 0.1 × 13 OR 3 960 000 OR 4 320 000 4 680 000 J OR 4.68 MJ OR 1.3 kWh OR 1300 Wh									
(b)	EIT	HER								
	I = P/V in any form OR P/V OR 100/250 OR 0.4 A $Q = It$ OR 0.4 × 13 × 3600 OR candidate's current × 13 × 3600 OR candidate's current × candidate's time in s									
	18 7	720 C e.c.f	A1							
	OR									
	468	s = joules/coulombs in any form 30000/250 OR candidate's E/250 720 C e.c.f	C1 C1 A1							
(c)	(los	t as/changed to) heat/light OR lost to air/surroundings	B1							
(a)		reases (as current increases) an increasing rate	M1 A1							
(b)	(i)	25 Ω	B1							
	(ii)	<i>IR</i> in any form OR 0.070 x 25 1.7/1.8 V	C1 A1							
	(iii)	$(P=)$ IV OR I^2R OR V^2/R in any form, numbers, symbols or words 0.12 W e.c.f. from (i)/(ii)	C1 A1							
(c)	(i)	answer to (b)(ii)	B1							
	(ii)	use of $1/R = 1/R_1 + 1/R_2$ OR $R = R_1R_2/(R_1 + R_2)$ 12.5 Ω	C1 A1							

(a) energy supplied / work done (per unit charge) to В1 drive charge round a (complete) circuit B1 p.d. / voltage across battery / power source В1 **(b) (i)** P = IV OR (I =) P/V OR (I =) 60/240C1 = 0.25 A OR 1/4 A Α1 (ii) I = V/R OR other version OR (R =)V/IC1 OR(R =)240/0.25OR $P=V^2/R$ or other version e.g. $(R=) V^2/P$ OR (R=) 240²/60 $R=960 \Omega$ Α1 (c) current in series circuit = 240 / 972 = 0.247 A В1 current suits both bulbs, (so both light up so Y is correct) В1 p.d. across bulb $A = 240 \times (960/972) = 237 \text{ V}$ p.d. across bulb B = $240 \times 12/972 = 2.96 \text{ V}$ B1

В1

B1

В1

[8]

(a) correct symbol for OR gate

(b) output is low / zero / off if both inputs are low / zero / off

p.d. suits both bulbs, (so both light up so Y correct)

output is high / one / on if one input is high / one / on
BUT this mark is not scored if candidate puts output low when both inputs high
B1

(c)switches in doors are on if doors are open or vice versaB1(switches in) doors provide inputs (to gate)B1output (of gate) is connected to buzzer / warning light / alarmB1[6]

(a)	(i)	4 V	B1					
	(ii)	12V	B1					
(b)	(i)	6Ω	B1					
	(ii)	$1/R = 1/3 + 1/6$ OR $(3 \times 6)/(3 + 6)$ 2Ω	C1 A1					
(c)		OR 12/candidate's (ii) ecf	C1 A1					
(d)	(i)	stays same	B1					
	(ii)	decreases	B1	[9]				
(a)	(i)	 resistance is constant / doesn't vary resistance increases 	B1 B1					
	(ii)	7 V	B1					
(b)	resistance of resistor = $4/2.6$ (= 1.54Ω) resistance of lamp = $4/3.6$ (= 1.11Ω) $1/R = 1/R_1 + 1/R_2$ OR $(R =) R_1R_2/(R_1 + R_2)$ OR either eq. with numbers 0.645 or 0.65Ω OR current through resistor = $2.6 A$ current through lamp = $3.6 A$ total current = $2.6 + 3.6 = 6.2 A$							
	0.64	15Ω OR 0.65Ω OR $R=4/\text{sum}$ of candidate's currents	(A1)	[7]				

accept R value based on no. of sig. figs. for resistors used by candidate

(a)	decreases / low / very low / zero									B1	[1]			
(b)	(i)	(i) ecf from (a), both answers must be consistent with candidate's (a) e.g. decreases / low / very low / zero increases / high / v. high / > 5V light high OR 1 light low OR 0 AND dark low OR 0 AND dark high OR 1										B1		
	(ii)	AND	switch switch	-			high Iow	OR OR	1 0				B1	[2]
(c)	AN	D gate											B1	[1]
(d)	trar	nsistor											B1	[1]
(e)										M1 A1	[2]			

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(a) (i) I_1 = I_2 + I_3
                                                                                                     В1
    (ii) I_1 = I_4 OR same
                                                                                                     В1
(b) (i) (V = IR = 0.80 \times 3.0 =) 2.4 \text{ V}
                                                                                                     Α1
    (ii) I = V/R in any algebraic form OR 2.4 / 2 OR (b)(i) / 2
                                                                                                    C1
         OR any voltage divided by 2
         (I_3 = V/R = 2.4 / 2 =) 1.2 A
                                                                                                     A1
         OR
         I_3/I_2 = 3/2
                                                                                                   (C1)
         I_3 = 3/2 \times 0.8 A = 1.2 A
                                                                                                   (A1)
   (iii) (I_2 + I_3 \text{ OR Current through } R = 0.8 + 1.2) = 2.0 \text{ (A)}
         OR 6V/2A used
                                                                                                    C1
         Parallel combination formula: 1/r = 1/r_1 + 1/r_2
                                                                                                    C1
         OR (r =) r_1 r_2 / (r_1 + r_2)
         Use of formula: combined resistance = 1.2(\Omega)
                                                                                                    C1
         (R + 1.2 = 6/2 = 3.0 \Omega R = )1.8 \Omega
                                                                                                    Α1
         Current through R = 0.8 + 1.2 = 2.0 (A)
                                                                                                   (C1)
         P.D. across R = 6.0 - 2.4
                                                                                                   (C1)
         = 3.6(V)
                                                                                                   (C1)
         R = 3.6 / 2.0 = 1.8 \Omega
                                                                                                   (A1)
for (b) and (d) accept HIGH/LOW or ON/OFF
(a) NOR
                                                                                               B1
                                                                                                     [1]
(b) outputs 1, 0, 0, 0
    lose 1 mark e.e.o.o.
                                                                                               B2
                                                                                                     [2]
(c) (i) OR and NOT gates either order
                                                                                               B1
    (ii) both symbols correct
                                                                                               B1
         OR then NOT, connected
                                                                                               B1
                                                                                                     [3]
(d) logic level at Y, 0
                                                                                               B1
    logic level at Z, opposite to candidate's answer to Y
                                                                                               B1
                                                                                                     [2]
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(a)	Tran	sistor		B1						
(b)	Resistor / variable resistor / rheostat identified Light-dependent resistor / LDR identified Resistor or alternative in gap A; LDR in gap B									
(c)		rmistor / thermal resistor / heat or temperature dependent resistor identified rmistor (or alternative name) in gap A <u>and</u> resistor in gap B		B1 B1						
(a)	in or	der downwards: 1 1 1 0 c.a.o.	B1							
(b)	(i)	1 AND 0 (e.c.f. from (b)(i))	B1							
	(ii)	NOT (gate) (allow NOR (gate))	B1							
(c)	R = T =	1 AND S = 0 (e.c.f. from (b)(i)) 1	B1 B1	[5]						
	(i)	input high/on/1, output low/off/0 input low/off/0, output high/on/1 OR reverses/inverts state of input OR output opposite to input resistance changes as temperature changes		B1 B1						
(a)	(ii)	resistance changes as temperature changes		ы						
	(i)	at low temperature resistance of thermistor is high OR when temperature falls resistance of thermistor rises p.d. across thermistor is high OR p.d. across R is low (voltage) input to gate is low output of gate is high (and warning light is on)		B1 B1 B1						
	(ii)	changes the temperature/set value at which the lamp comes on		В1						

(a)	row	1	0	0	accept low/off		B1							
	row	2	0	1	accept low/off and high/on		B1							
	row	3	1	1	accept high/on									
(b)		2 wires to flat (input) side, 1 wire from curved (output) side lo not accept pointed curved side or small circle												
(c)	NOT gate connected to output of AND gate accept labelled boxes for gates do not allow any extra gates or inputs													
	NOT gate correct way round													
(a)	(i)	1. 2.	con	sists of er ana	same variation (with time) as the data f (a series of) 'highs' and 'lows' alogue is continuously variable (between limits) ital has no intermediate values	B1 B1	[3]							
	(ii)		ex	tra dat	egenerated / noise can be eliminated a can be added to check / correct transmitted signal nable suggestions, 1 each)	B2	[2]							