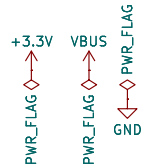
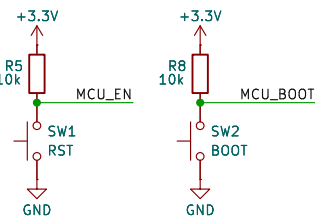


MCU_SCL0 — TP13 SCL0
MCU_SDA0 — TP14 SDA0



The diagram shows two horizontal green lines representing the USB_D- and USB_D+ signals. Between these lines, there are two red rectangular boxes, each labeled '0R'. The top box is labeled 'R6' above it and 'R7' below it. The left side of the top box is connected to the top green line (USB_D-), and the right side is connected to the top green line (MCU_USB_D-). The left side of the bottom box is connected to the bottom green line (USB_D+), and the right side is connected to the bottom green line (MCU_USB_D+).



Pin 1 connection diagram for the I2S module. The diagram shows a 3-pin header with pins TP3, TP7, TP8, and TP9. TP3 is connected to +3.3V. TP7 is connected to I2S_D, which is also connected to MCU_I2S_D. TP8 is connected to BCLK, which is also connected to MCU_BCLK. TP9 is connected to LRCLK, which is also connected to MCU_LRCLK. The other end of the header is connected to TP12 GND, which is also connected to GND.

MAX98357A_I2S

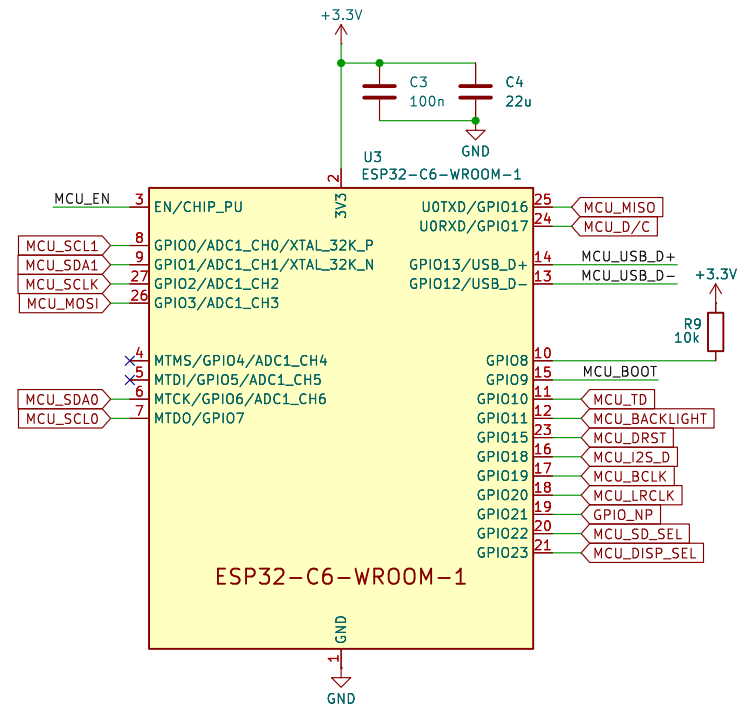
AMP_DIN<	MCU_I2S_D
AMP_BCLK<	MCU_BCLK
AMP_LRCLK<	MCU_LRCLK

BME688

```
graph LR
    BME688_SDA[BME688_SDA] --> MCU_SDA0[MCU_SDA0]
    BME688_SCL[BME688_SCL] --> MCU_SCL0[MCU_SCL0]
```

Pin connections for the display module:

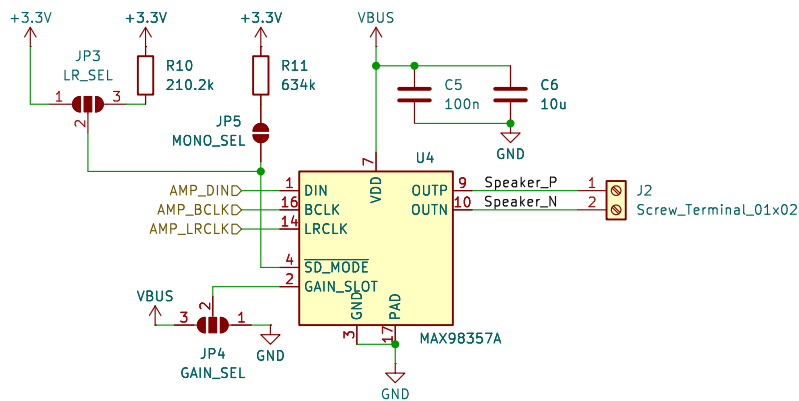
- BACKLIT_PWM → MCU_DRST
- TS_TOUCH_DETECT → MCU_TD
- DISPLAY_RST → MCU_BACKLIGHT
- TS_SDA0 → MCU_SDA0
- TS_SCL0 → MCU_SCL0
- SPI_MISO → MCU_MISO
- SPI_CLK → MCU_SCLK
- SPI_MOSI → MCU_MOSI
- DISP_CS → MCU_DISP_SEL
- SD_CARD_CS → MCU_SD_SEL
- D/C_Display → MCU_D/C



Id: 1/4

I2S Speaker Amp

VDDIO = 3.3V, so Rlarge = 634k and Rsmall = 210.2k



Sheet: /MAX98357A_I2S/
File: HOPE-DawnWave-Speaker-Amp.kicad_sch

Title:

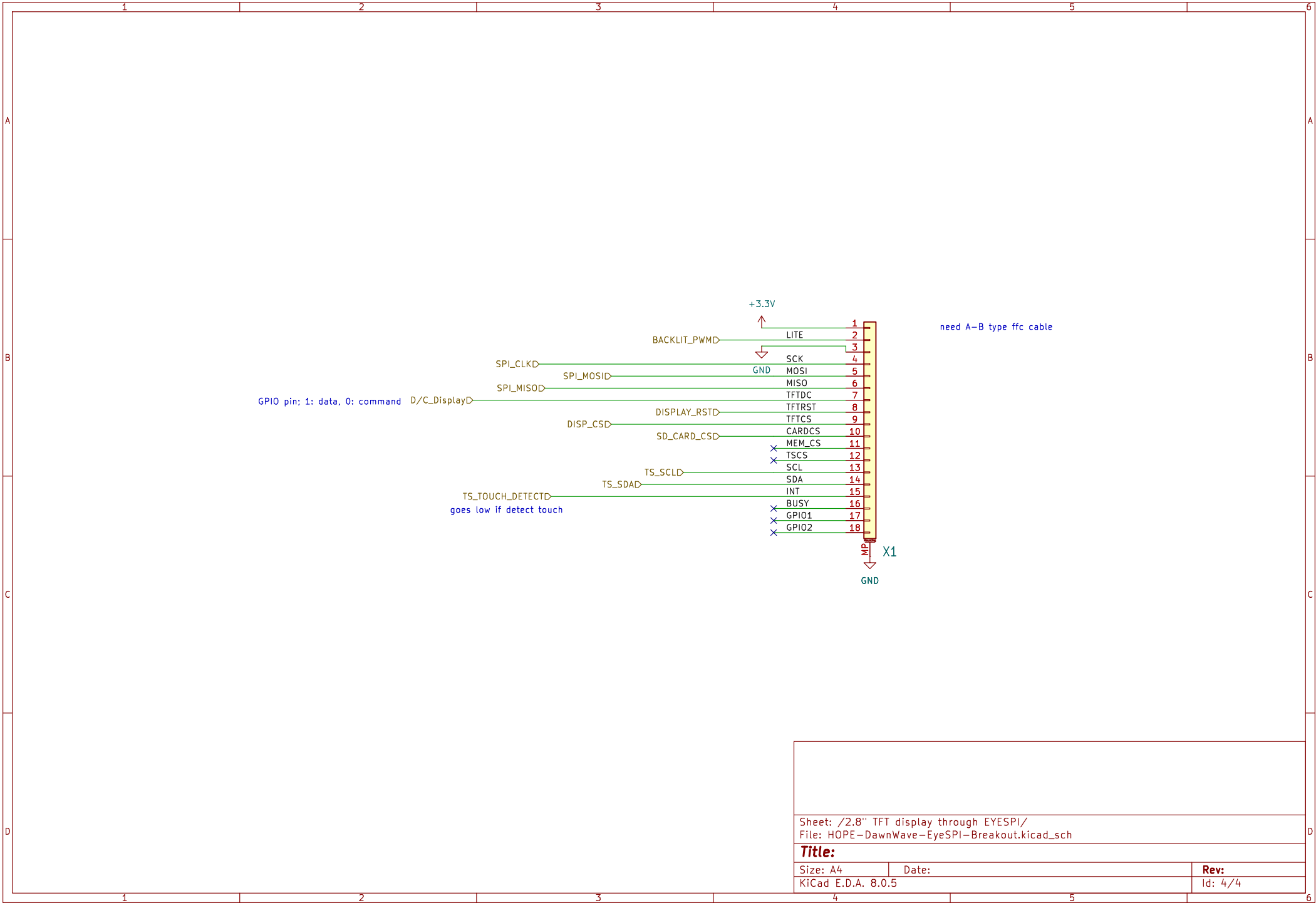
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Date:

Rev:

KiCad E.D.A. 8.0.5

Id: 2/4



Sheet: /2.8" TFT display through EYESPI/
File: HOPE-DawnWave-EyeSPI-Breakout.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.5

Rev:

Id: 4/4