## 四川大学期末考试试题 (闭卷)

## (2017~2018 学年第1学期)

A卷

课程号:	31114304	<b>0</b> 课	呈名称: <b>系</b>	统级编程	, , ,		任课教师	j <b>.</b>	
适用专业	·业年级: <b>软件工程 2015 级</b>		015 级	学号:			姓名:		
1、己接 2、不带	要求将考试禁」 手机进入考场;	上携带的文具	る规则》和《四, 具用品或与考试 告有违规行为,	有关的物品放	生考试违纪位置在指定地,	点;	(修订)》,郑重 <b>考生签名:</b>	重承诺:	
题	₹ -(40	)%)	二(10%)	三(13%	5) [2	耳(12%)	五(15%)	テ	√(10%)
得 ター・参面总分	-		教师签名				阅卷时间		
2. 请将答案全部填写在本试题纸上; 3. 考试结束,请将试题纸、添卷纸和草稿纸一并交给监考老师。  ———————————————————————————————————									
·			选、多选或未			I _			
1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20
1. Which one of the following languages is hardware dependent?  A. C  B. Assembly  C. C++  D. JAVA  2. Which expression is false?  A. !(-5)  B. ~0x01  C. ~0x00  D. !0									
St	atement 1	l: char	ode segme str1[]="abo 'str2 ="abo	.".	ch state	ment is I	NOT true?		

教务处试题编号: 311-33

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Statement 3: char \*str3 = "abc"

- I. statement 1 requires 3 bytes from stack
- II. statement 2 requires 4 bytes from stack
- III. expression (str2 = = str3) is true
- IV. expression (str1 = = str2) is true
- A. II III and IV
- B. II and III
- C. Land IV
- D. II and IV
- 4. The machine code generated from source code by a compiler
  - A. executes more quickly than the source code
  - B. does not preserve all the information given in the source code
  - C. can be easily inspected to check the correctness of the compiler
  - D. associates variable values with their names
- 5. After execution of the following code in a 32 bits system, which statement is TRUE?

```
char a[8]={'1','2','3','4','5','6','7','8'};
double *p = (double *)&a;
*p= -1.5f;
```

- A. a[0] is '1'
- B. a[7] is 0
- C. a[8] is 1
- D. None of the above
- 6. In C, local variables allocated inside functions are allocated
  - A. on the stack
- B. in static storage
- C. in the heap
- D. in a fifo
- 7. Which statement will copy the value in register BX to AX?
  - A. LDS AX,BX
- B. LEA AX,BX
- C. MOV AX,BX
- D. STOS AX,BX
- 8. Consider the following segment of C source code.

```
int a = 8;
int b = *&a:
```

What is the value of variable b at the end of execution of the segment?

- A. a
- B. (int) &a
- C. (int) &b
- D. &a
- 9. A memory leak is caused by a

A.failure to free allocated memory

- B.bug in which too much memory is allocated, causing internal fragmentation
- C.bug in the memory allocator that fails to free memory
- D.function that allocates a large amount of memory from the heap
- 10. What properties of a variable are specified by the static keyword in c?
  - i.the variable will be statically allocated.
  - ii.the variable name will be visible only to functions defined within the same file.
  - iii.the variable's value does not change very often. the compiler uses this fact to focus optimizations on other variables.

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A. i only
B. i and iii only.
C. i and ii only.
D. iii only

- 11. We use dynamic memory because:
  - A. The heap is significantly faster than the stack.
  - B. The stack is prone to corruption from buffer overflows.
- C. Storing data on the stack requires knowing the size of that data at compile time.
  - D. None of the above.
- 12. Which one of the following statements is NOT true of storage allocators?
- A. In the best case, coalescing with boundary tags is linear in the number of free blocks.
  - B. Segregated free list typically approximates best fit search.
  - C. Payloads must be aligned to some boundary.
  - D. Explicit lists are typically faster than implicit list.
- 13. A garbage collector starts from some "root" set of places that are always considered "reachable", such as
  - i. CPU registers
  - ii. stack
  - iii. global variables
  - A. i & ii B. i & iii
    C. iii D. all of them
- 14. Which on the following is a reason next-fit might perform better than first-fit?
- A. If a large number of small blocks are stored at the beginning of the free list, next-fit avoids walking through those small blocks upon every allocation.
  - B. First fit requires a traversal of the entire free list, but next-fit does not.
- C. First-fit requires that both allocated and unallocated blocks be examined, and next-fit examines only free blocks.
- D. Next-fit is an approximation of best-fit, so it reduces internal fragmentation compared to first-fit.
- 15. Which of the following cannot be used to estimate time consumption of a program?
  - A. RDTSC
  - B. QueryPerformanceFrequency
  - C. GetTickCount
  - D. clock() / CLOCKS\_PER\_SEC
- 16. from the time when a c program is written, to the time when it is running as a process on windows, what should be done?

i.compile

ii.link

iii.load.

A.i and ii only.

B.i and iii only.

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C.i, ii and iii.

D.ii only.

17. a lock is a software mechanism that

A.temporarily makes memory read-only.

B.limits access to a critical section.

C.implements password protection to data.

D.prevents execution except in debug mode.

18. which of the following is not optimization technique?

A.code motion

B.loop unrolling

C.constant folding

D.memory aliasing

- 19. in ia32 or x86, which exception is used to implement system call
  - A. interrupt
  - B. trap
  - C. fault
  - D. Abort
- 20. which facts about the cache can be determined by calling the following function? int data[1 << 20];

```
void callee(int x) {
  int i, result;
  for (i = 0; i < (1 << 20); i += x) {
     result += data[i];
  }
}</pre>
```

i cache line size

ii cache size

iii cache speed

A. i and ii only

B. i only

C. i and iii only

D. i, ii, and iii

<sub>评阅教师</sub> 得分 二、bit operation (本大题共 10 分)。

Now complete the following functions according to the following rules.

Each "Expr" is an expression using ONLY the following:

- 1. Integer constants 0 through 0xFFFFFFF inclusive.
- 2. Function arguments and local variables (no global variables).
- 3. Some of the problems restrict the set of allowed operators.

You are expressly forbidden to:

1. Use any control constructs such as if, do, while, for, switch, etc.

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2. Define or use any macros. 3. Define any additional functions in this file. 4. Call any functions. 5. Use any other operations, such as &&,  $|\cdot|$ , -, ?, or  $|\cdot|$ : 6. Use any form of casting. You may assume that your machine: 1. Uses 2s complement, 32-bit representations of integers. 2. Performs right shifts arithmetically. 3. Has unpredictable behavior when shifting an integer by more than the word size. /\* NegativeNum using only ~ and & , ignore 0 \* Example: NegativeNum(-5) retrun -5, NegativeNum(5) retrun -5, Negative(0) can return any value \* Legal ops: ~ & \* Max ops: 8 \*/ int NegativeNum (int x) } \* SetByte - Set byte n from word x to 0xFF Bytes numbered from 0 (LSB) to 3 (MSB) Examples: SetByte(0x12345678,1) = 0x1234FF78Legal ops: ! ~ & | << >> Max ops: 6\*/ int SetByte (int x,int n)

评阅教师 得分 三、stack discipline(本大题共 13 分)。

Consider the following C code and assembly code for two mutually recursive functions:

```
int even(unsigned int n)
                           0x080483e4 <even+0>:
                                                   push
                                                          %ebp
                           0x080483e5 <even+1>:
                                                          %esp, %ebp
                                                   mov
   if(!n)
                           0x080483e7 <even+3>:
                                                  sub
                                                          $0x8,%esp
                           0x080483ea <even+6>:
                                                          $0x0,0x8(%ebp)
                                                   cmpl
                           0x080483ee <even+10>:
                                                          0x80483f9 <even+21>
        return 1;
                                                   jne
                           0x080483f0 <even+12>:
                                                   movl
                                                          $0x1,-0x4(%ebp)
                           0x080483f7 <even+19>:
                                                   jmp
                                                          0x804840a <even+38>
   return odd(n - 1);
                           0x080483f9 <even+21>:
                                                          0x8(%ebp),%eax
                                                   mov
                           0x080483fc <even+24>: sub
                                                          $0x1,%eax
                           0x080483ff <even+27>: mov
                                                          %eax, (%esp)
                           0x08048402 <even+30>: call
                                                          0x804840f <odd>
                           0x08048407 <even+35>:
                                                          %eax, -0x4(%ebp)
                                                   mov
                           0x0804840a <even+38>:
                                                          -0x4(%ebp), %eax
                                                   mov
                           0x0804840d <even+41>:
                                                   leave
                           0x0804840e <even+42>:
                                                   ret
                           0x0804840f <odd+0>:
int odd(unsigned int n)
                                                          %ebp
                                                   push
                           0x08048410 <odd+1>:
                                                   mov
                                                          %esp, %ebp
   if(!n)
                           0x08048412 <odd+3>:
                                                   sub
                                                          $0x8,%esp
                           0x08048415 <odd+6>:
                                                          $0x0,0x8(%ebp)
                                                   cmpl
       return 0;
                           0x08048419 <odd+10>:
                                                          0x8048424 <odd+21>
                                                   jne
                           0x0804841b <odd+12>:
                                                movl
                                                          $0x0,-0x4(%ebp)
                           0x08048422 <odd+19>: jmp
                                                          0x8048435 <odd+38>
                           0x08048424 <odd+21>:
   return even(n - 1);
                                                   mov
                                                          0x8(%ebp), %eax
                           0x08048427 <odd+24>:
                                                   sub
                                                          $0x1,%eax
                                                  mov
                           0x0804842a <odd+27>:
                                                          %eax,(%esp)
                           0x0804842d <odd+30>:
                                                  call 0x80483e4 <even>
                                                   mov
                           0x08048432 <odd+35>:
                                                          %eax, -0x4(%ebp)
                           0x08048435 <odd+38>:
                                                          -0x4(%ebp),%eax
                                                   mov
                           0x08048438 <odd+41>:
                                                   leave
                           0x08048439 <odd+42>:
```

Imagine that a program makes the procedure call **even(3)**. Also imagine that prior to the invocation, the value of ESP is 0xffff1000 - that is, 0xffff1000 is the value of ESP immediately before the execution of the **call** instruction.

- 1. Note the the call even(3) will result in the following function invocations: even(3), odd(2), even(1), and odd(0). Full in the stack diagram with the values that would be present immediately before the execution of the ret instruction for odd(0). Cross out each blank for which there is insufficient information to complete.
- 2. What are the values of ESP and EBP immediately before the execution of the ret instruction for odd(0)?

$ESP=_$	 		 
EBP=			

++	
1	0xfffff1004
++	
1	0xfffff1000
++	
1	0xffff0ffc
+	
1	0xffff0ff8
++	
1	0xffff0ff4
++	
1	0xffff0ff0
+	
1	0xffff0fec
++	
1	0xffff0fe8
+	
1	0xffff0fe4
++	
1	0xffff0fe0
+	
1	0xffff0fdc
+	
1	0xffff0fd8
+	
i i	0xffff0fd4
+	
i i	0xffff0fd0
+	ONLLLLOIGO
i i	0xffff0fcc
+	0.111110100
i i	0xffff0fc8
+	
1	0xffff0fc4
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	0xffff0fc0
t	OVIIIIOICA

评阅教师 得分

四、Performance Optimization (本大题共12分)。

1. Consider the following functions:

```
int min(int x, int y) { return x < y ? x : y; }
int max(int x, int y) { return x < y ? y : x; }
void incr(int *xp, int v) { *xp += v; }
int square(int x) { return x * x; }</pre>
```

Here are three code fragments that call these functions

```
A. for (i = min(x, y); i < max(x,y); incr(&i, 1)) \{ t += square(i); \}
```

- B. for  $(i = max(x, y) 1; i \ge min(x,y); incr(&i, -1)) \{ t += square(i); \}$
- C. int low = min(x, y); int high = max(x, y); for (i = low; i < high; incr(&i, 1)) { t += square(i); }

assume x = 10 and y = 100. Fill in the table below indicating the number of times each of the four functions is called for each of these code fragments.

Code	min	max	incr	square
A				
В				
С				

2. The following problem illustrates the way memory aliasing can cause unexpected program behavior. Consider the following procedure:

Can this procedure be used to swap two values? And why?

评阅教师	得分

五、Cache(本大题共15分)。

1. Analyse the locality the following code with nested loop. Note: This is the pseudo-code.

```
integer data(M, N); (5 \%)
for (i = 0; i < M; i=i+1) {
for (j = 0; j < N; j=j+1) {
sum += data(i, j);
}
```

课程名称:系统级编程

2.You are evaluating a system's cache performance on a machine with a 1024-byte direct-mapped data cache with 16-byte blocks (B =16). You are given the following definitions:  $(10\,\%)$ 

```
struct position {
    int x;
    int y;
    int z1;
    int z2;
};
struct position grid[16][16];
int total_x = 0, total_y = 0, total_z = 0;
int i, j, k;
```

You should also assume:

- sizeof(int) == 4.
- grid begins at memory address 0.
- The cache is initially empty.
- The only memory accesses are to the entries of the array grid. Variables i, j, total\_x, total\_y and total\_z are stored in registers.

Determine the cache performance of the following code:

```
for (i = 0; i < 16; i++)
{
     for (j = 0; j < 16; j++)
     {
        total_x += grid[i][j].x;
        total_y += grid[i][j].y;
        total_z += grid[i][j].z1+ grid[i][j].z2;
     }
}</pre>
```

- A. What is the total number of reads? \_\_\_\_\_
- B. What is the total number of reads that miss in the cache? \_\_\_\_\_
- C. What is the miss rate?
- D. What would the miss rate be if the cache were twice as big? And explain every answer briefly.

评阅教师	得分

## 六、Answering Question (本大题共10分)。

- 1. What is the mean of our textbook of CSAPP? Please give your understanding about it, such as what you have learned from this course or this textbook. (5 %)
- 2. What is exception? Please list the four types of exception in IA32, and draw a figure to show the different. (5 %)