## 四川大学期末考试试题(闭卷)

## (2016~2017 学年第 2 学期)

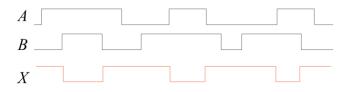
A卷

课程号:	311039030	课程名称	·: 数字逻辑,	应用与设计	任课教	师: <b>李辉,</b> 应	5三丛,陈媛	媛
	年级: <b>软件</b>							
1、己按要 <b>2</b> 、不带手	]读并知晓《四/ 京求将考试禁止拉 机进入考场; ]间遵守以上两项	携带的文具用品.	》和《四川大学 或与考试有关的	考生承诺 学本科学生考证 的物品放置在指	送记作弊处分 新定地点;	规定(修订)》,		
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题号		二(10%)	三(10%)	四(10%)	五(10%)	六(10%)	七(10%)	八(20%)
得   分     ************************************			阅卷时间					
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<ul> <li>→ → → → → → → → → → → → → → → → → → →</li></ul>								
1	2	3	4	5 6	6 7	8	9	10
1. Which	odd-parity	code is in err	or?	IL	<u>                                  </u>			
(a) 11000111 (b) 00101010 (c) 10101010 (d) 10101011								
2. The number 1010 in BCD is								
(a). equal to decimal eight (b). equal to decimal ten (c). equal to decimal twelve (d). invalid								
3. The binary number 10011011 is equal to the decimal number								
(a) 121 (b) 233 (c) 155 (d) 9B								
4. The su	ım of 1111 <b>+</b>	- 1111 in bin	ary equals					
(a) 0000 (b) 2222 (c) 11110 (d) 11111								
5. The BCD number for decimal 67 is								
(a) 01000011 (b) 01110011 (c) 01110110 (d) 01100111								
6. The binary number 101100111001010100001 can be written in octal as								

注: 试题字迹务必清晰, 书写工整。 本题共 03 页, 本页为第 1 页

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- **(a)** 5471230 **(b)** 5471241
- **(c)** 2634521
- **(d)** 23162501
- 7. A 2-input gate produces the output shown. (X represents the output.) This is a(n)
- (a). OR gate
- (b). AND gate
- (c). NOR gate (d). NAND gate



- 8. The expression A'BCD + ABCD' + AB'C'D
- (a) cannot be simplified
- (b) can be simplified to A'BC + AB'
- (c) can be simplified to ABCD' + A'BC' (d) None of these answers is correct
- 9. An example of a sum-of-products expression is
- (a) A + B(C + D)
- (b) A'B + AC + AB'C (c) (A' + B)(A + B') (d) both answers (a) and (b)

- 10. A JK flip-flop is Reset when
- (a) J = 0, K = 0
- (b) J = 0, K = 1 (c) J = 1, K = 0
- (d) J = 1, K = 1

评阅教师	得分

## 填空题(本大题共9空,每空2分,共18分)。

1. (18pts) Complete the following table of equivalent values. Use binary numbers with a sign bit and 7 bits for the value.

Decimal	Signed Magnitude	2's Complement code	1's Complement code
		01111111	
-127			
	10011111		

评阅教师	得分

## 三、分析计算题 (本大题共 6 小题 , 共 42 分 )。

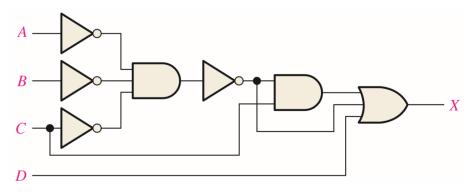
1. (共 5 分) Expand the expression to a standard SOP form F(A,B,C) = A + BC

**注**:试题字迹务必清晰,书写工整。

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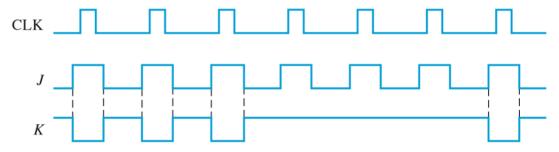
- 2. (共6分) Implement the basic logic gates (AND, OR, NOT) only by the NOR gates respectively.
- 3. (共 15 分)Simplify the following expressions using Karnaugh maps.
  - (1) f(A, B, C) = A'B'C' + A'B'C + A'BC + ABC
  - (2) f(w, x, y, z) = w'xy+wz'+xyz'
  - (3)  $f(a, b, c, d) = \sum (0.3, 4.7, 8) + \sum d(10.11, 12.13, 14.15)$
- 4. (共 6 分) Write the switching expressions for the following logic circuits and Simplify it.



5. (共5分) Realize the function shown below using a 3-to-8 decoder and the appropriate logic gates.

$$F=A'B'+AC+A'C'$$

6. (共 5 分) For a negative edge-triggered J-K flip-flop with the inputs shown below, develop the Q output waveform relative to the clock. Assume that Q is initially LOW



评阅教师	得分	四、设计题 (本大题共2小题,共20分)。

- 1. (共8分) Design a logic circuit to produce a HIGH output only if the input, represented by a 4-bit binary number, is greater than twelve or less than three. Develop the truth table, give the simplified Bool expression and then draw the logic diagram
- 2. ( $\pm 12$ 分) Design a counter to produce the following binary sequence. Use J-K flip-flops  $1,4,3,5,7,6,2,1,\ldots$

**注:** 试题字迹务必清晰, 书写工整。 本题共 03 页, 本页为第 3 页