## Presentation Topic

Name of the course: - Basic Electronics Engineering

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(An Autonomous Institute affiliated to Savitribai Phule Pune University)
(NBA and NAAC accredited, ISO 9001:2015 certified)



## Unit 4:Linear Integrated circuit

- Introduction to operational amplifiers
- Block diagram of OP-AMP
- Ideal characteristics of OP-AMP
- Positive feedback and Negative feedback
- Inverting & Non inverting Amplifier
- Comparators
- Summing amplifier
- Difference amplifier

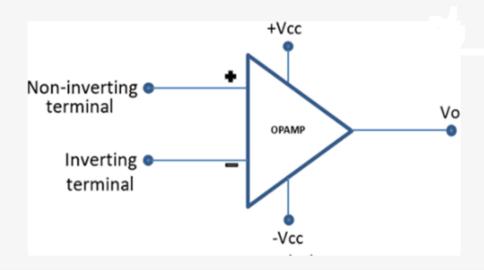


## Introduction to operational amplifier

- The basic job of operational amplifier is to <u>amplify the signal</u>.
- Op-amp circuit is built using <u>different capacitors and registers.</u>
- Op-amp is able to perform different mathematical operations such as <u>addition</u>, <u>subtraction</u>, <u>differentiation and integration</u>.

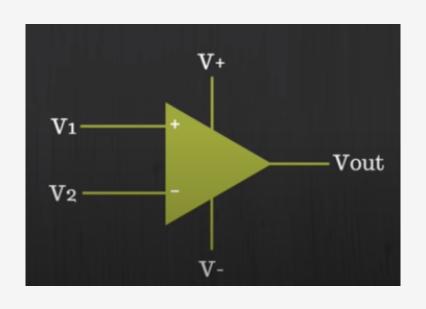


## Symbol of OPAMP





## Symbol of op-amp



- V1: Non-inverting input
- V2:inverting input
- V+:positive power supply
- V-: negative power supply
- Vout:Output of opamp



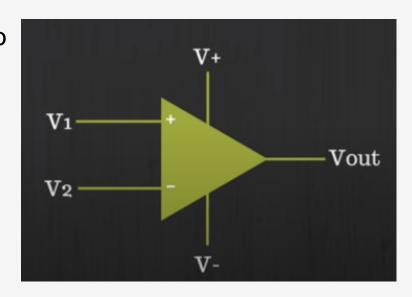
# Op-amp, a kind of differential amplifier

- It amplifies difference between two Inputs.
- Suppose A is gain of op-amp.

#### Then

$$Vout=A(v1-v2)$$

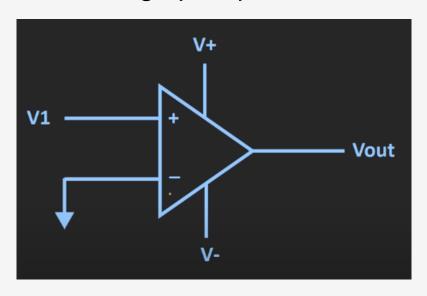
- Op-amp is high gain amplifier .
- Gain in the range of 10<sup>5</sup> to 10<sup>6</sup>.





## **Non-Inverting Terminal of Op-amp**

Non-inverting Op-amp

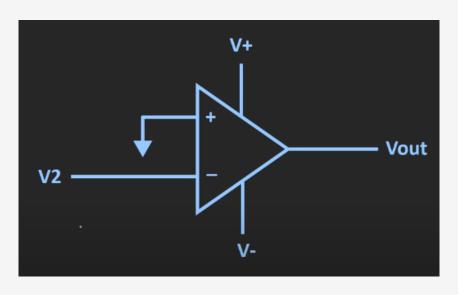


- Sinusoidal input is applied at v1,
- Amplified Output will be in phase with input sinusoidal wave .
- Hence non-inverting terminal



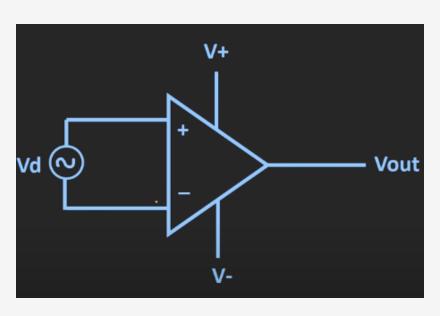
## **Inverting Terminal of Op-amp**

Inverting Op-amp



- Sinusoidal input is applied at v2,
- Amplified Output will be 180 degree out of phase (i.e.Inverted)
- Hence inverting terminal

## **Operational Amplifier**

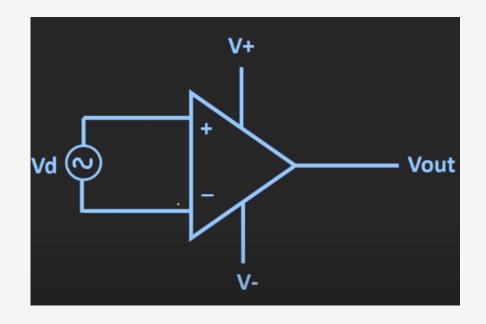


- Vd is input voltage applied between two terminals.
- A:open loop gain of op-amp
- (open loop because output is not fed back to input)
- Then vout= A\*Vd

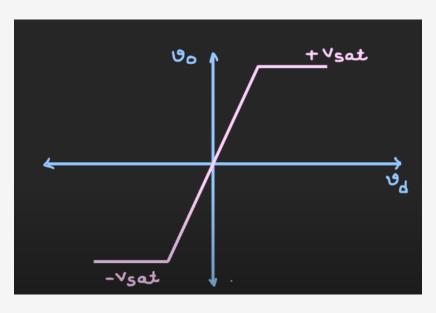
## **Operational Amplifier**

- As gain high 10<sup>5</sup> to 10<sup>6</sup>
- Consider A=10^5
- ►If Vd=1mv
- ► Vout=100v (theoretically)

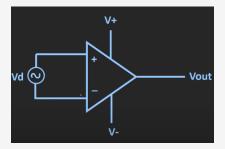
- ►If Vd=1v
- ► Vout=10^5v(theoretically)
- But it is not possible practically



## Voltage transfer curve of op-amp



- X axis: input voltage, Vd applied between two input terminal
- Y axis: output voltage, Vo of opamp
- Slope: gain A of op-amp



## Voltage transfer curve of op-amp

• In open loop configuration,

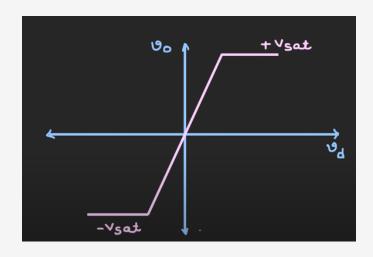
The output is saturated at +Vsat and -Vsat.

+Vsat < V+

-Vsat > V-

(value of V+ can be from +5v to +15v)

(value of V- can be from -5v to -15v)



## **OP-AMP**

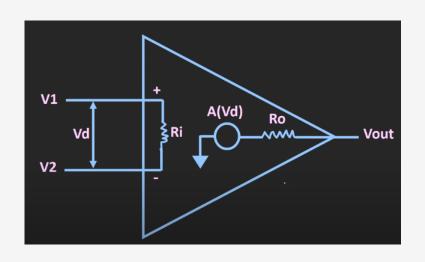
#### It can be used as

- comparator
- Adder
- **>**Sub-tractor
- In filter circuit

Thus Op-amp is very versatile IC.

It is versatile because of its different characteristics.

# **Equivalent circuit of op-amp and characteristics of ideal op-amp**



#### Characteristics of ideal op-amp

- Ri,Input resistance: infinite
- Ro,Output resistanec :zero
- Gain:infinite
- Bandwidth :infinite (accept signal of all frequencies)



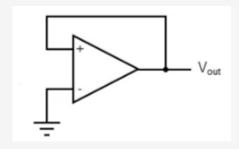
#### Open and close loop configuration

- If the feed back is not given it is called as open loop configuration.
- If the feedback is given it is called as close loop configuration.

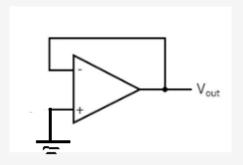


## **Positive feedback and Negative feedback**

#### **Positive feedback**

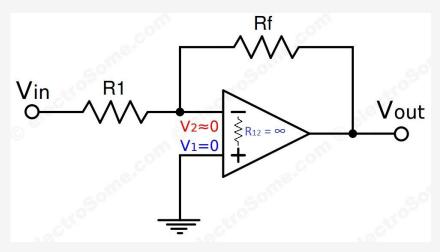


#### **Negative feedback**





### **Concept of virtual ground**



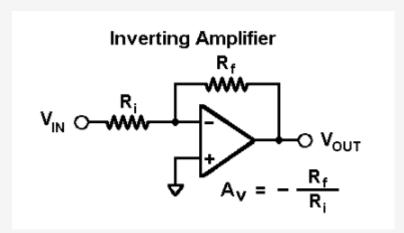
- This is known as inverting configuration of opamp .(with negative feedback)
- Input impedance of op-amp is very high
- Hence op-amp never draws any current at it's input, hence input current is always zero.
- lin =0 amperes
- For current to be zero the voltage Va must be zero, thus even though the input is applied, the inverting terminal behaves as ground terminal at node 'a'.
- This concept is known as virtual ground concept.



## **Inverting & Non inverting Amplifier**

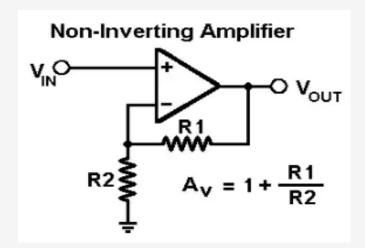
Inverting Amplifier (negative feedback)

Gain of inverting amplifier



Inverting Amplifier (negative feedback)

Gain of inverting amplifier

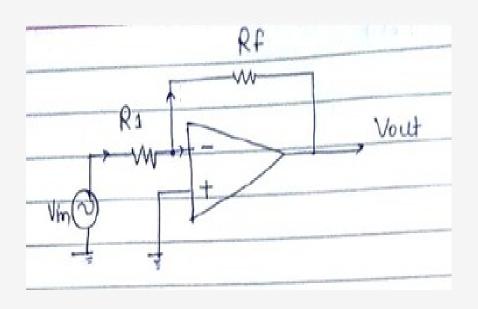




Ex:Find the gain and output voltage of op-amp .

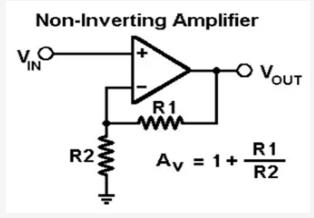
- Given:
- Rf=2Kohm,
- R1=1Kohm,
- Vin=1V

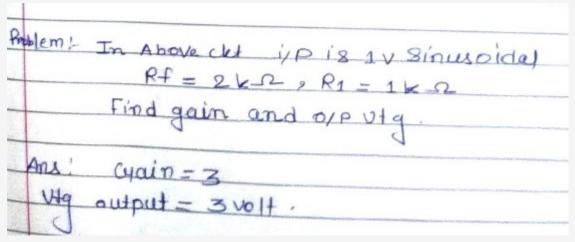
• ans:-2V





## Problem on gain of non-inverting op-amp





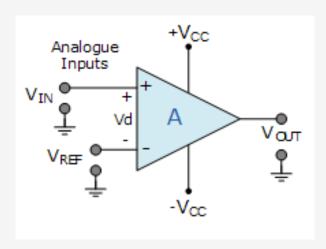


## **Op-amp as comparator**

- The Op-amp as a comparator <u>compares</u> one analogue voltage level with another analogue voltage level, and produces an <u>output signal based on this voltage</u> <u>comparison</u>.
- Out of these two voltages ,One voltage is considered as reference voltage.
- When the <u>applied input voltage is greater than the reference voltage</u>, the <u>output of op-amp is high.</u>
- When the <u>applied input voltage is less than the reference voltage</u>, the <u>output of opamp is low</u>.

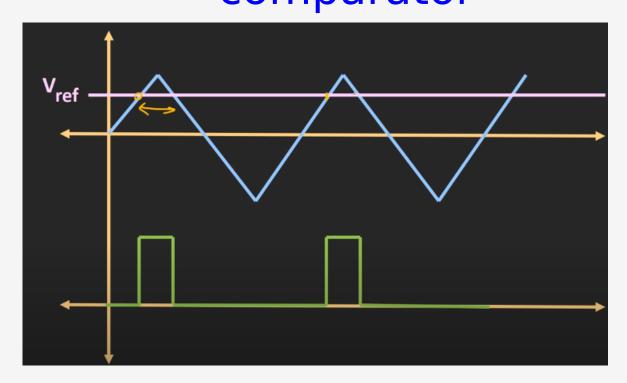


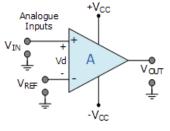
## **Op-amp as comparator**



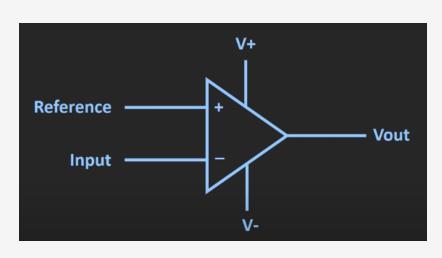
- When VIN>VREF, output will be high
- When VIN<VREF, output will be low .</li>
- Shown here is the non-inverting comparator because the input is applied to the non-inverting terminal of op-amp.

Non-inverting Op-amp as a comparator





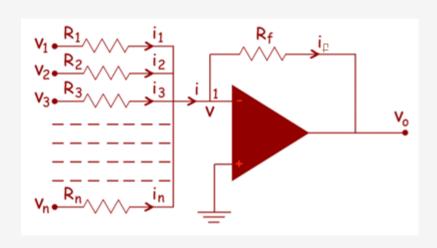
## inverting op-amp as comparator



 Shown here is the inverting comparator because the <u>input</u> is applied to <u>the inverting terminal</u> of op-amp.



## **Inverting Op-amp adder**



- Here, n numbers of input voltages are connected in parallel and given as input to the inverting terminal of op-amp.
- The non-inverting terminal of the op amp is connected to ground.

$$v_0 = -(\frac{R_f}{R_1}v_1 + \frac{R_f}{R_2}v_2 + \frac{R_f}{R_3}v_3 + \cdots + \frac{R_f}{R_n}v_n)$$

This indicates that output voltage  $\underline{v_0}$  is weighted sum of numbers of input voltages.

In above equation ,when all values of the resistances are made equal, (i.e.all input resistances and feedback resistance) It will have unity gain, thus it will act as adder.

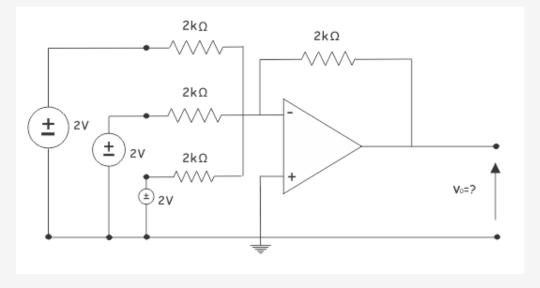
$$v_0 = - (v_1 + v_2 + v_3 + \dots + v_n)$$

(Note: As it is inverting summer the output of voltage adder will be out of phase with respect to the input by 180 degree )



# **Example on inverting op-amp** summer

• If 3 input voltages are applied at inverting terminal .Find output voltage .





# **Example on inverting op-amp** summer

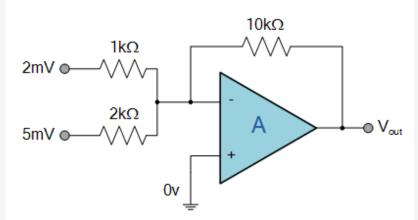
As per equation

$$egin{aligned} v_0 &= -(rac{2k\Omega}{2k\Omega} imes 2V + rac{2k\Omega}{2k\Omega} imes 2V + rac{2k\Omega}{2k\Omega} imes 2V) \ \Rightarrow v_0 &= -(2V + 2V + 2V) \ \Rightarrow v_0 &= -6V \end{aligned}$$

• Thus we obtained addition of all 3 voltages.



## Example no.2



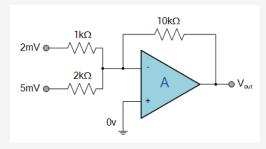
- Find the output voltage .
- As all the resistance values are not equal, the gain will not be unity.
- The output will not be -7mv.



## **Example no.2**

### By using the formula

$$v_0 = -(rac{R_f}{R_1}v_1 + rac{R_f}{R_2}v_2 + rac{R_f}{R_3}v_3 + \cdots + rac{R_f}{R_n}v_n)$$

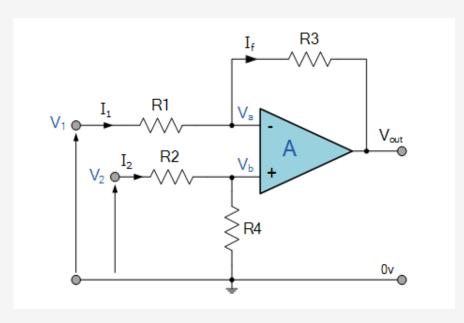


Vout= - 45Volts

Here it has given the weighted sum of all input voltages.

It is called as <u>scaling summing</u> <u>amplifier.</u>

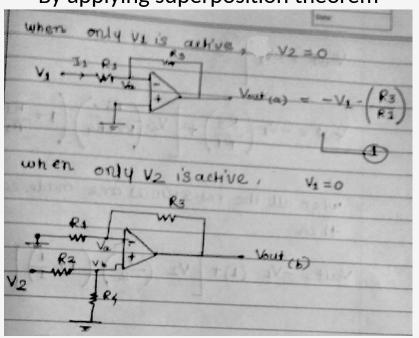
## **Op-amp as difference amplifier(sub-tractor)**



- This is the circuit for op-amp as substractor.
- To find the output voltage, the superposition theorem is used.
- It states that, when there are multiple input sources, the output voltage of that circuit can be determined by summing the individual responses achieved by considering each voltage source acting separately.

## **Op-amp as difference amplifier(sub-tractor)**

By applying superposition theorem



This is the noninverting configuration of opamp.

Vout(b) = ·Vb· 
$$\left(1 + \frac{R^3}{R^1}\right)$$

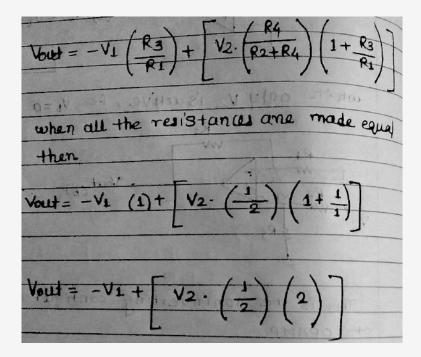
How Vb is given by

 $Vb = \left(\frac{R^4}{R^2 + R^4}\right) \cdot V^2 \cdot OR \quad V^2 \cdot \left(\frac{R^4}{R^2 + R^4}\right)$ 

Vout(b) =  $V^2 \cdot \left(\frac{R^4}{R^2 + R^4}\right) \cdot \left(\frac{1}{1} + \frac{R^3}{R^1}\right) = \frac{2}{2}$ 

# **Op-amp as difference amplifier(sub-tractor)...**

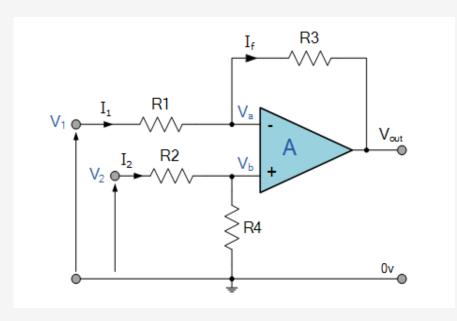
• By adding equation 1 and 2



Op-amp as difference amplification by  $V_0 = -V_1 + \left[ V_2 \cdot \left( \frac{1}{2} \right) \left( \frac{1}{2} \right) \right]$ 

Thus op-amp will act as <u>sub-tractor</u>.

## **Ex on Op-amp as sub-tractor**



- Find output voltage using superposition theorem.
- The values given are
- V1=2,V2=5
- R1=2 Kohm, R2=2 Kohm,
- R3=10 Kohm, R4=4 Kohm

Sol: when V1 is active; Vout1=-10

When V2 is active; Vout2=20

Adding Vout1 and Vout2

Vout = 10

When all the resistances are made equal then

Vout= V2-V1

Vout=3



## Thank you