

pin for the slave select for the digital pot
Model for sensor register initialization
Data rate for serial transmission
Reset the CPLD (bus_write)

-int: 7
-byte: 3
921600
(-address: 0x07 | **0x80** -data: 0x80)- delay 100-
(-address: 0x07 | **0x80** -data: 0x00) - delay 100

To check if SPI bus is ok:

1. (bus_write)
2. Read address from 1. OK if 0x55

(-address: 0x00 | **0x80** -data: 0x55)

InitCam

wrSensorReg16_8(0x3008, 0x80);

wrSensor**Regs**16_8(OV5642_QVGA_Preview);
{0x3103 ,0x93},

{0x3008 ,0x82},
{0x3017 ,0x7f},
{0x3018 ,0xfc},
{0x3810 ,0xc2},
{0x3615 ,0xf0},
{0x3000 ,0x00},
{0x3001 ,0x00},
{0x3002 ,0x5c},
{0x3003 ,0x00},
{0x3004 ,0xff},
{0x3005 ,0xff},
{0x3006 ,0x43},
{0x3007 ,0x37},
{0x3011 ,0x08},
{0x3010 ,0x10},
{0x460c ,0x22},
{0x3815 ,0x04},
{0x370c ,0xa0},
{0x3602 ,0xfc},
{0x3612 ,0xff},
{0x3634 ,0xc0},

{0x3613 ,0x00},
{0x3605 ,0x7c},
{0x3621 ,0x09},
{0x3622 ,0x60},
{0x3604 ,0x40},
{0x3603 ,0xa7},
{0x3603 ,0x27},
{0x4000 ,0x21},
{0x401d ,0x22},
{0x3600 ,0x54},
{0x3605 ,0x04},
{0x3606 ,0x3f},
{0x3c01 ,0x80},
{0x5000 ,0x4f},
{0x5020 ,0x04},
{0x5181 ,0x79},
{0x5182 ,0x00},
{0x5185 ,0x22},
{0x5197 ,0x01},
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{0x5505 ,0x7f},
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{0x300e ,0x18},
{0x4610 ,0x00},
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{0x4708 ,0x06},
{0x3808 ,0x02},
{0x3809 ,0x80},
{0x380a ,0x01},
{0x380b ,0xe0},
{0x380e ,0x07},
{0x380f ,0xd0},
{0x501f ,0x00},
{0x5000 ,0x4f},
{0x4300 ,0x30},
{0x3503 ,0x07},
{0x3501 ,0x73},
{0x3502 ,0x80},
{0x350b ,0x00},
{0x3503 ,0x07},
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{0x380e ,0x03},
{0x380f ,0xe8},
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{0x3a0e ,0x03},
{0x3818 ,0xc1},
{0x3705 ,0xdb},
{0x370a ,0x81},
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{0x3803 ,0x08},
{0x3827 ,0x08},
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{0x3804 ,0x05},
{0x3805 ,0x00},
{0x5682 ,0x05},
{0x5683 ,0x00},
{0x3806 ,0x03},
{0x3807 ,0xc0},
{0x5686 ,0x03},
{0x5687 ,0xbc},
{0x3a00 ,0x78},
{0x3a1a ,0x05},
{0x3a13 ,0x30},
{0x3a18 ,0x00},
{0x3a19 ,0x7c},
{0x3a08 ,0x12},
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{0x5188 ,0x14},

{0x5189 ,0x69},

{0x518a ,0x60},

{0x518b ,0xa2},

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{0x518d ,0x36},

{0x518e ,0x34},

{0x518f ,0x54},

{0x5190 ,0x4c},

{0x5191 ,0xf8},

{0x5192 ,0x04},

{0x5193 ,0x70},

{0x5194 ,0xf0},

{0x5195 ,0xf0},

{0x5196 ,0x03},

{0x5197 ,0x01},

{0x5198 ,0x05},

{0x5199 ,0x2f},

{0x519a ,0x04},

{0x519b ,0x00},

{0x519c ,0x06},

{0x519d ,0xa0},

{0x519e ,0xa0},

{0x528a ,0x00},

{0x528b ,0x01},

{0x528c ,0x04},

{0x528d ,0x08},

{0x528e ,0x10},

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{0x538B ,0x2B},
{0x538C ,0x00},
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{0x54B5 ,0xf0},
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{0x5505 ,0x7f},

{0x5025 ,0x80},
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{0x3a1b ,0x30},
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{0x3a1f,0x10},
{0x5688,0xfd},
{0x5689,0xdf},
{0x568a,0xfe},
{0x568b,0xef},
{0x568c,0xfe},
{0x568d,0xef},
{0x568e,0xaa},
{0x568f,0xaa},

{0xffff,0xff},
```

Delay 100
Delay 100

To set as JPEG:

Delay 100

wrSensorRegs16_8(OV5642_JPEG_Capture_QSXGA):

```
// OV5642_ QXGA _YUV7.5 fps
// 24 MHz input clock, 24Mhz pclk
// jpeg mode 7.5fps

{0x3503,0x07},      //AEC Manual Mode Control
{0x3000,0x00},      //SYSTEM RESET00
{0x3001,0x00},      //Reset for Individual Block
{0x3002,0x00},      //Reset for Individual Block
{0x3003,0x00},      //Reset for Individual Block
{0x3005,0xff},      //Clock Enable Control
{0x3006,0xff},      //Clock Enable Control
{0x3007,0x3f},      //Clock Enable Control
{0x350c,0x07},      //AEC VTS Output high bits
{0x350d,0xd0},      //AEC VTS Output low bits
{0x3602,0xe4},      //Analog Control Registers
{0x3612,0xac},      //Analog Control Registers
{0x3613,0x44},      //Analog Control Registers
{0x3621,0x27},      //Array Control 01
{0x3622,0x08},      //Analog Control Registers
```

{0x3623 ,0x22},	//Analog Control Registers
{0x3604 ,0x60},	//Analog Control Registers
{0x3705 ,0xda},	//Analog Control Registers
{0x370a ,0x80},	//Analog Control Registers
{0x3801 ,0x8a},	//HS
{0x3803 ,0x0a},	//VS
{0x3804 ,0x0a},	//HW
{0x3805 ,0x20},	//HW
{0x3806 ,0x07},	//VH
{0x3807 ,0x98},	//VH
{0x3808 ,0x0a},	//DVPHO
{0x3809 ,0x20},	//DVPHO
{0x380a ,0x07},	//DVPVO
{0x380b ,0x98},	//DVPVO
{0x380c ,0x0c},	//HTS
{0x380d ,0x80},	//HTS
{0x380e ,0x07},	//VTS
{0x380f ,0xd0},	//VTS
{0x3810 ,0xc2},	//Horizontal and Vertical Offset setting
{0x3815 ,0x44},	//Possibly DVP PCLK divider control?
{0x3818 ,0xc8},	//Mirror NO, Compression enable
{0x3824 ,0x01},	//RSV
{0x3827 ,0x0a},	//RSV
{0x3a00 ,0x78},	//AEC System Control 0
{0x3a0d ,0x10},	//60 Hz Max Bands in One Frame
{0x3a0e ,0x0d},	//50 Hz Max Bands in One Frame
{0x3a10 ,0x32},	//Stable Range Low Limit (enter)
{0x3a1b ,0x3c},	//Stable Range High Limit (go out)
{0x3a1e ,0x32},	//Stable Range Low Limit (go out)
{0x3a11 ,0x80},	//Step Manual Mode, Fast Zone High Limit
{0x3a1f ,0x20},	//Step Manual Mode, Fast Zone Low Limit
{0x3a00 ,0x78},	//AEC System Control 0
{0x460b ,0x35},	//RSV VFIFO Control 0B
{0x471d ,0x00},	//DVP CONTROL 1D
{0x4713 ,0x03},	//COMPRESSION MODE SELECT mode3
{0x471c ,0x50},	//RSV
{0x5682 ,0x0a},	//AVG X END
{0x5683 ,0x20},	//AVG X END
{0x5686 ,0x07},	//AVG Y END
{0x5687 ,0x98},	//AVG Y END
{0x5001 ,0x4f},	//ISP CONTROL 01, UV adjust/Line stretch/UV average/Color matrix/AWB enable
{0x589b ,0x00},	//RSV

{0x589a ,0xc0},	//RSV
{0x4407 ,0x08},	//COMPRESSION CTRL07 Bit[5:0]: Quantization scale 0x02
{0x589b ,0x00},	//RSV
{0x589a ,0xc0},	//RSV
{0x3002 ,0x0c},	//Reset for Individual Block, Reset SFIFO/compression
{0x3002 ,0x00},	//Reset for Individual Block
{0x3503 ,0x00},	//AEC Manual Mode Control, Auto enable
//{0x3818, 0xa8},	
//{0x3621, 0x17},	
//{0x3801, 0xb0},	
{0x5025, 0x80},	//ISP control 37
{0x3a0f, 0x48},	//controls image luminance: Stable range high limit (enter)
{0x3a10, 0x40},	//Stable range low limit (enter)
{0x3a1b, 0x4a},	//Stable range high limit (go out)
{0x3a1e, 0x3e},	//Stable range low limit (go out)
{0x3a11, 0x70},	//Step Manual mode, fast zone high limit
{0x3a1f, 0x20},	//step manual zone, fast zone low limit
{0xffff, 0xff},	

wrSensor**Regs**16_8(ov5642_320x240):

{0x3800 ,0x1 },	//TIMING HS
{0x3801 ,0xa8},	//TIMING HS
{0x3802 ,0x0 },	//TIMING VS
{0x3803 ,0xA },	//TIMING VS
{0x3804 ,0xA },	//TIMING HW
{0x3805 ,0x20},	//TIMING HW
{0x3806 ,0x7 },	//TIMING VH
{0x3807 ,0x98},	//TIMING VH
{0x3808 ,0x1 },	//TIMING DVPHO
{0x3809 ,0x40},	//TIMING DVPHO
{0x380a ,0x0 },	//TIMING DVPVO
{0x380b ,0xF0},	//TIMING DVPVO
{0x380c ,0xc },	//TIMING HTS
{0x380d ,0x80},	//TIMING HTS
{0x380e ,0x7 },	//TIMING VTS
{0x380f ,0xd0},	//TIMING VTS
{0x5001 ,0x7f},	//ISP CONTROL 01

{0x5680 ,0x0 },	//AVG X START
{0x5681 ,0x0 },	//AVG X START
{0x5682 ,0xA },	//AVG X END
{0x5683 ,0x20},	//AVG X END
{0x5684 ,0x0 },	//AVG Y START
{0x5685 ,0x0 },	//AVG Y Y START
{0x5686 ,0x7 },	//AVG Y END
{0x5687 ,0x98},	//AVG Y END
{0x3801, 0xb0},	//TIMING TC REG18
{0xffff, 0xff},	

Delay 100

wrSensorReg16_8(0x3818, 0xa8);	//TIMING TC REG18
wrSensorReg16_8(0x3621, 0x10);	//ARRAY CONTROL 01 - binning related
wrSensorReg16_8(0x3801, 0xb0);	//TIMING CONTROL HS
wrSensorReg16_8(0x4407, 0x04);	//COMPRESSION CTRL07
wrSensorReg16_8(0x5888, 0x00);	//LENC BRV SCALE

myCAM.write_reg(ARDUCHIP_TIM, VSYNC_LEVEL_MASK):

(write_bus) (-address: ARDUCHIP_TIM | **0x80** -data: VSYNC_LEVEL_MASK)

Where

ARDUCHIP_TIM = 0x03 //timing control

VSYNC_LEVEL_MASK = 0x02