[Full Marks - 60

Q.11 a) Write important any four feature of pentium processor.
b) Explain RAM organization of 8051 microcontroller. 4
c) Write possible applications of 8051 microcontroller. 3

First Year B.C.A (Semester - II) Examination Paper - 15BCA113 Microprocessor

Time : Three hours]

N.B.	:	i)	All questions carry equa	al mar	·ks		
			Question No. 1 is comp				
			Due credit will be given				
			dimensions.		_		
		iv)	Assume suitable data wh	ne re ve	er necessary.		
			Illustrate your answer n		=		
			neat skectches.				
		vi)	Use Blue/Black ink/refi	llonly	for writing the		
			answer book.	,	C		
Q.1		100S6	5				
	a)	Ad	dress bus of 8086 microprocessor is				
		i)	16 bit		20 bit		
			24 bit		32 bit		
	b)		group.				
					Arithmatic		
			Logical	iv)	Branch control		
	c)		B means				
			Destination Byte		Define Byte		
			Data Byte		Double		
d) 80			86 μp have data/Address pins in multiplexing				
		mo					
		i)	8	ii)	16		
			20		None of these		
	e)		e 8051 microcontroller ha		_		
			One	ii)	Two		
		iii)	Three	iv)	Four		

Q.2	a) Explain the software or programming model of 80	086	b)	Write ALP program to find maximum number in		
	microprocessor.	6		array.		
	b) Explain the function of each flag bit in flag register.	5. 5				
	OR		OR			
Q.3 Q.4	 a) Draw the internal block diagram of 8086 microprocand explain the function of each block. b) Explain the evolution of microprocessor. a) Explain the addressing modes of 8086 microprocessor. b) Explain the meaning of following instructions. 	6 5	,	Explain the meaning of following assembler directive and their use. i) ALIGN ii) END iii) EQU iv) DW v) PTR vi) ORG Write ALP program to find minimum number in array.		
	i) MOV AX, [2000] ii) MUL BX iii) INC BX iv) OR AX, BX v) CMP AX, BX		~ /	Draw the pin diagram of 8086 μp . Explain the function of interrupt in interrupt vector table.	65	
	OR			OR		
Q.5	 a) Explain the MOV instruction in Six various address mode. b) Explain the meaning of following instructions. i) ADD AX, BX ii) ADD AX, [2000] iii) DIV BL iv) TES TAL, BH v) XOR AL, BL 	ssing 6 5	Q.9 a)	Explain the function of following pins. i) $AD0 - AD15$ ii) $R\overline{D}$ iv) MN/\overline{MX} v) $HOLD \& HLDA$ vi) $INTR$ vii) ALE viii) M/\overline{IO} ix) $TEST$ x) $READY$	11 6	
Q.6	 a) Explain the meaning of following instruction. i) JMP BX ii) CALL WORD PTR [BX] iii) RET iv) STC v) CMC vi) STI 	6	,	State the possible differences between microproces and microcontroller. Explain following concept in pentium microprocess i) Super scaler pipeline architecture ii) RSIC concept	5	