PARALLEL COMPUTING MINI PROJECT

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Parallelizing AES Cryptographic Algorithm using MPI

What is AES Algorithm?

Advanced Encryption Standard (AES) is a specification for the encryption of

electronic data established by the U.S National Institute of Standards and

Technology (NIST) in 2001. AES is widely used today as it is a much stronger

than DES and triple DES despite being harder to implement.

Points to remember

• AES is a block cipher.

• The key size can be 128/192/256 bits.

• Encrypts data in blocks of 128 bits each.

That means it takes 128 bits as input and outputs 128 bits of encrypted cipher

text as output. AES relies on substitution-permutation network principle which

means it is performed using a series of linked operations which involves

replacing and shuffling of the input data.

Working of the cipher:

AES performs operations on bytes of data rather than in bits. Since the block

size is 128 bits, the cipher processes 128 bits (or 16 bytes) of the input data at a

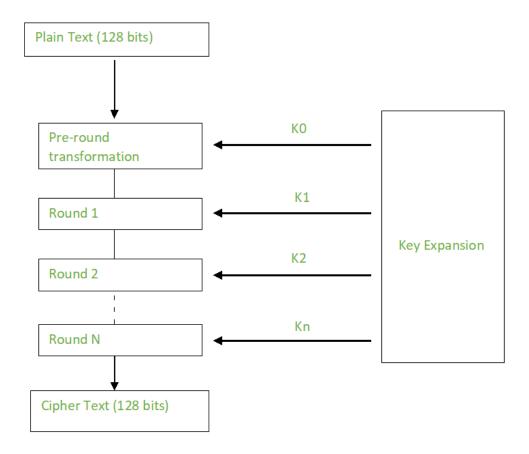
time.

The number of rounds depends on the key length as follows:

- 128 bit key 10 rounds
- 192 bit key 12 rounds
- 256 bit key 14 rounds

## Creation of Round keys:

A Key Schedule algorithm is used to calculate all the round keys from the key. So the initial key is used to create many different round keys which will be used in the corresponding round of the encryption.



# **Encryption:**

AES considers each block as a 16 byte (4 byte  $\times$  4 byte = 128) grid in a column major arrangement.

```
[ b0 | b4 | b8 | b12 |
| b1 | b5 | b9 | b13 |
| b2 | b6 | b10 | b14 |
| b3 | b7 | b11 | b15 ]
```

Each round comprises of 4 steps:

- SubBytes
- ShiftRows
- MixColumns
- Add Round Key

The last round doesn't have the MixColumns round.

The SubBytes does the substitution and ShiftRows and MixColumns performs the permutation in the algorithm.

### SubBytes:

This step implements the substitution.

In this step each byte is substituted by another byte. Its performed using a lookup table also called the S-box. This substitution is done in a way that a byte is never substituted by itself and also not substituted by another byte which is a compliment of the current byte. The result of this step is a 16 byte  $(4 \times 4)$  matrix like before.

The next two steps implement the permutation.

### ShiftRows:

This step is just as it sounds. Each row is shifted a particular number of times.

- The first row is not shifted
- The second row is shifted once to the left.
- The third row is shifted twice to the left.
- The fourth row is shifted thrice to the left.

(A left circular shift is performed.)

```
[ b0
      | b1
            | b2
                   | b3
                         ]
                                     [ b0
                                           | b1
                                                  | b2
                                                        | b3
                                                               ]
| b4
      | b5
             | b6
                   | b7
                                     | b5
                                           | b6
                                                  | b7
                                                        | b4
                               ->
| b8 | b9
                                     | b10 | b11 | b8
            | b10 | b11 |
                                                        | b9
[ b12 | b13 | b14 | b15 ]
                                     [ b15 | b12 | b13 | b14 ]
```

### MixColumns:

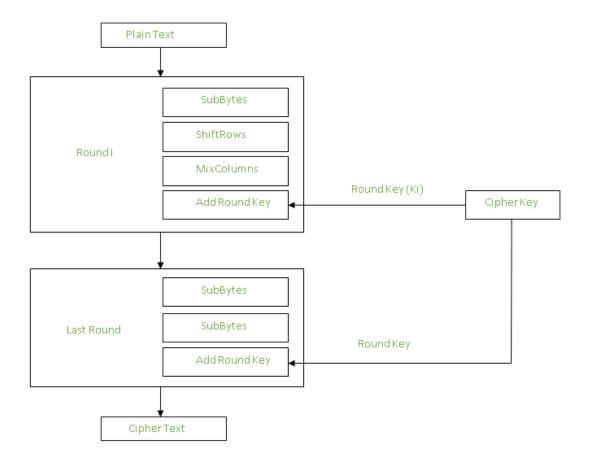
This step is basically a matrix multiplication. Each column is multiplied with a specific matrix and thus the position of each byte in the column is changed as a result.

This step is skipped in the last round.

```
1 ]
[ c0 ]
                 [ 2
                                   [ b0 ]
| c1 |
                 | 1
                      2
                          3
                             1 |
                                       | b1 |
| c2 |
             | 1
                   1
                      2
                          3 |
                                   | b2 |
[ c3 ]
             [ 3
                   1
                          2 ]
                      1
                                   [ b3 ]
```

### Add Round Keys:

Now the resultant output of the previous stage is XOR-ed with the corresponding round key. Here, the 16 bytes is not considered as a grid but just as 128 bits of data.



After all these rounds 128 bits of encrypted data is given back as output. This process is repeated until all the data to be encrypted undergoes this process.

# **Decryption:**

The stages in the rounds can be easily undone as these stages have an opposite to it which when performed reverts the changes. Each 128 blocks goes through the 10,12 or 14 rounds depending on the key size.

The stages of each round in decryption is as follows:

- Add round key
- Inverse MixColumns
- ShiftRows

### Inverse SubByte

The decryption process is the encryption process done in reverse so i will explain the steps with notable differences.

#### Inverse MixColumns:

This step is similar to the MixColumns step in encryption, but differs in the matrix used to carry out the operation.

```
[ b0 ]
                 [ 14
                        11
                            13
                                        [ c0 ]
                                 9
| b1 |
                 1 9
                        14
                            11
                                 13 |
                                            | c1 |
| b2 |
             | 13 9
                         14
                             11 |
                                        | c2 |
[ b3 ]
                 [ 11
                       13
                            9
                                 14 ]
                                            [ c3 ]
```

### **Inverse SubBytes:**

Inverse S-box is used as a lookup table and using which the bytes are substituted during decryption.

### **Summary:**

AES instruction set is now integrated into the CPU (offers throughput of several GB/s) to improve the speed and security of applications that use AES for encryption and decryption. Even though its been 20 years since its introduction we have failed to break the AES algorithm as it is infeasible even with the current technology. Till date the only vulnerability remains in the implementation of the algorithm.

## **Parallel Implementation**

- · As mentioned before, AES is rather sequential in nature due to the fact that each successive round depends on the output of the prior round
- · So we're not interested so much in speeding up AES encryption itself, but rather encrypting the blocks in parallel
- · Being able to do this will afford us huge gains in efficiency and speedup

Used MPI for parallelization

This code is implementing parallel processing using the Message Passing Interface (MPI) standard. MPI is a library specification for message-passing that is commonly used for parallel processing on distributed memory systems.

In this code, the MPI library is used to perform encryption and decryption of data in parallel across multiple processes. The MPI initialization is done by creating a communicator object `comm` and getting the rank and size of the current process group.

The `main()` function is the main program that is executed by each process. The program prompts the user to select an option for encryption or decryption, and then prompts for the input data (plaintext or ciphertext) and the AES key. The input data and the AES key are broadcasted across all processes using the `bcast()` method.

The input data is then divided into chunks and distributed across the processes using the `scatter()` method. Each process performs encryption or decryption on its assigned data chunk using the `encrypt\_data()` or `decrypt\_data()` function, which uses the AES encryption algorithm. The encrypted or decrypted data is then gathered back into the root process using the `gather()` method.

Finally, the execution time is computed and printed by the root process. By dividing the input data into smaller chunks and distributing them across multiple processes, the encryption or decryption can be performed in parallel, which reduces the overall execution time.