Connections Between PTCB and STM32F4VExx MCU Module

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As described in the "Hardware" document, the prototype Pertec Tape Controller board was implemented using the STM32F4VEXX development board described here: https://stm32-base.org/boards/STM32F407VET6-STM32-F4VE-V2.0.html.

The connections from the controller board to the MCU were made using wire-wrap techniques, but a cable using Dupont connectors should probably be the preferred method. Another possibility is to construct a "mezzanine" printed circuit board that plugs into the controller board and mates with the microcontroller development board of choice.

In any case, the software supplied with the board uses the following pin assignments. In all cases, the first reference is to the controller board pin, the second is the microcontroller connection:

J8:

```
+5 \rightarrow \text{any 5V}
GND \rightarrow \text{any GND}
```

Data:

```
E00 \rightarrow PE0 \quad E02 \rightarrow PE2 \quad E04 \rightarrow PE4 \quad E06 \rightarrow PE6

E01 \rightarrow PE1 \quad E03 \rightarrow PE3 \quad E05 \rightarrow PE5 \quad E07 \rightarrow PE7
```

Status:

E08
$$\rightarrow$$
 PE8 E10 \rightarrow PE10 E12 \rightarrow PE12 E14 \rightarrow PE14
E09 \rightarrow PE9 E11 \rightarrow PE11 E13 \rightarrow PE13 E15 \rightarrow PE15

Control:

$$C00 \rightarrow PC0$$
 $C02 \rightarrow PC2$ $C04 \rightarrow PC4$ $C06 \rightarrow PC6$ $C01 \rightarrow PC1$ $C03 \rightarrow PC3$ $C05 \rightarrow PC5$ $C07 \rightarrow PC7$

Register Select:

```
CMD ENA \rightarrow PD1 DDIR \rightarrow PD5 LDBUF \rightarrow PD4 SSEL \rightarrow PD3 CMD SEL0 \rightarrow PD7 CMD SEL1 \rightarrow PD6 TACK \rightarrow PD0 GND \rightarrow any GND
```

Note that it's possible to change any of these association by editing the **gpiodef.h** file. All other uses of the pin names in the software are symbolically referenced.