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- Cross compiling ?
- Targets
- glibc vs musl
- The main machine
- The other machine
- Cross compiling with rust
- Examples

Everything in this talk will be a TLDR version.

Definitions

Host: Your desktop or working environment.



 Target: Your embedded device or the machine that will run the code that does not uses the same architecture.



Definitions

• Compiling: Create binaries from the source code.

Contents of section .text.main:
0000 4883ec18 8a050000 00004863 cf488d3d H......Hc.H.=

```
fn main() {
    println!("Hello, world!");
}
```

 Opcode: Operation code or human friendly representation of the CPU instruction.

```
Disassembly of section .interp:
000000000000002e0 <.interp>:
2e0:
       2f
                                 (bad)
2e1:
                                        (%dx),%es:(%rdi)
2e2:
       69 62 36 34 2f 6c 64
                                        $0x646c2f34,0x36(%rdx),%esp
       2d 6c 69 6e 75
2e9:
                                        $0x756e696c, %eax
2ee:
       78 2d
                                        31d <_ZN3std9panicking18update
2f0:
       78 38
                                        32a <_ZN3std9panicking18update
       36 2d 36 34 2e 73
                                        $0x732e3436,%eax
2f8:
       6f
                                       %ds:(%rsi),(%dx)
       2e 32 00
                                        %cs:(%rax),%al
```

```
Disassembly of section .text:
00010100 <elf_try_debugfile>:
                       stmdb sp!, {r4, r5, r6, r7,
           e92d 4ff0
                        sub sp, #44 ; 0x2c
                        ldr r4, [pc, #164] ; (101ac <
  1010a:
  1010c:
                        ldr r0, [pc, #160] ; (101b0 <
  1010e:
           9a15
                        ldr r2, [sp, #84]
                        add r4, pc
           447c
                        str r1, [sp, #24]
                        str r3, [sp, #28]
                        mov r3, r4
                        str r2, [sp, #16]
                        ldr r2, [r4, r0]
  1011a:
                        ldr r3, [sp, #80]
  1011c:
  1011e:
           9804
                        ldr r0, [sp, #16]
                             r3, r7, r3
                        str r3, [sp, #12]
```

objdump: Display information from object files.

 strings: Search printable characters from object files.

Cross compiling ?

GCC

```
fn main() {
    println!("Hello, world!");
}
```

x86 64

```
Disassembly of section .interp:
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        69 62 36 34 2f 6c 64
                                       $0x646c2f34,0x36(%rdx),%esp
        2d 6c 69 6e 75
                                        $0x756e696c,%eax
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                                       31d <_ZN3std9panicking18update
 2ee:
 2f0:
        78 38
                                       32a <_ZN3std9panicking18update
 2f2:
        36 2d 36 34 2e 73
                                       $0x732e3436,%eax
 2f8:
        6f
                                outsl %ds:(%rsi),(%dx)
 2f9:
        2e 32 00
                                        %cs:(%rax),%al
```

Cross compiling ?

GCC

```
fn main() {
    println!("Hello, world!");
}
```

ARM-GCC

x86_64

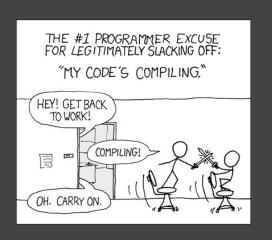
```
Disassembly of section .interp:
000000000000002e0 <.interp>:
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        2f
                                 (bad)
                                        (%dx), %es: (%rdi)
2e1:
                                        $0x646c2f34,0x36(%rdx),%esp
 2e2:
        69 62 36 34 2f 6c 64
        2d 6c 69 6e 75
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                                        32a < ZN3std9panicking18update
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        36 2d 36 34 2e 73
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 2f8:
        6f
                                 outsl %ds:(%rsi),(%dx)
 2f9:
        2e 32 00
                                        %cs:(%rax),%al
```

ARMv7

```
Disassembly of section .text:
00010100 <elf_try_debugfile>:
           e92d 4ff0 stmdb sp!, {r4, r5, r6, r7,
                        sub sp, #44 ; 0x2c
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           4c29
   1010a:
   1010c:
                        ldr r0, [pc, #160] ; (101b0 <
                        ldr r2, [sp, #84] ; 0x54
   1010e:
                        add r4, pc
           447c
            9106
                        str r1, [sp, #24]
                        str r3, [sp, #28]
                        mov r3, r4
                        str r2, [sp, #16]
           9204
                        ldr r2, [r4, r0]
   1011a:
   1011c:
           9b14
                        ldr r3, [sp, #80]
   1011e:
           9804
                        ldr r0, [sp, #16]
                        str r3, [sp, #12]
```

Cross compiling ?

Why can't you compile in your target computer?



```
# AMD Ryzen 7 2700 Eight-Core Processor
# 16 threads, 16GB Ram

cargo build
Finished dev [unoptimized + debuginfo] target(s) in 1m 48s

# ARMv7 Processor rev 4 (v71)
# 4 threads, 1GB Ram
Finished dev [unoptimized + debuginfo] target(s) in 33m 56s
```

Targets

List of available targets

rustup target list !! | wc -l # 81

Both are low-level libraries

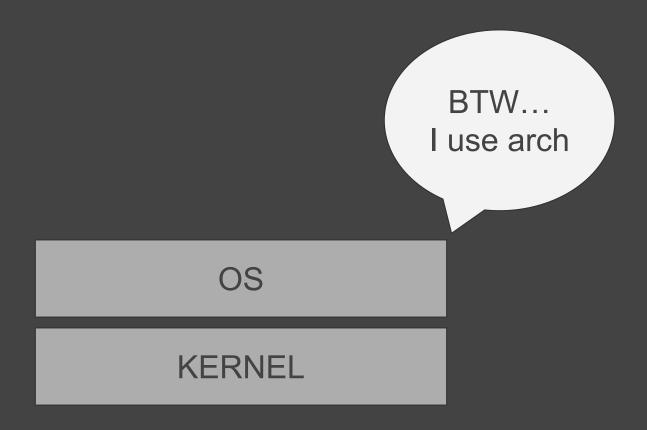
KERNEL

Both are low-level libraries

OS

KERNEL

Both are low-level libraries



Both are low-level libraries

glibc/musl

OS

KERNEL

Both are low-level libraries



- Both are low-level libraries
- glibc is backward compatible but not forward compatible
 - If you compile with glibc2 it'll run in a system with glibc6
 - If you compile with glibc6 it'll not run in a system with glib2
 - Linux Appimages should be compatible with the standard deployment OS [CentOS 6 (2011)]

- Both are low-level libraries
- glibc is backward compatible but not forward compatible
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 - If you compile with glibc6 it'll not run in a system with glib2
 - Linux Appimages should be compatible with the standard deployment OS [CentOS 6 (2011)]
- musl is a new standard library
- It's backward and forward compatible
- There is more advantages that you can check here:
 - https://www.musl-libc.org/

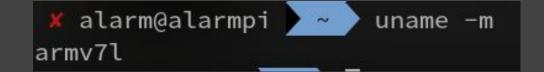
The main machine

macbook pro 2017

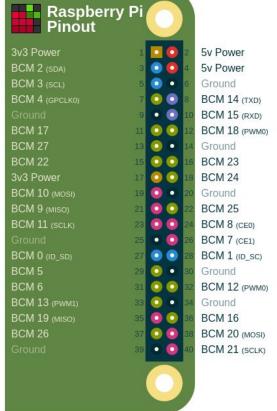
```
patrick@mac // uname -m
x86_64
```

The other machine

Raspberry Pi 3B







Legend

- GPIO (General Purpose IO)
- SPI (Serial Peripheral Interface)
- I²C (Inter-integrated Circuit)
- UART (Universal Asyncronous Receiver/Transmitter)
- Ground
- O 5V (Power)
- 3.3V (Power)

- Install rust!
 - a. https://rustup.rs/
- 2. Create a new project
 - a. cargo new cross
- 3. Build the project locally
 - a. cargo build
- 4. Run it!
 - a. cargo run

HOST MACHINE

- 1. Install rust!
 - a. https://rustup.rs/
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HOST MACHINE

Note: linker not found = no compiler in your machine Try to install GCC

- Install rust-std for our target
 a. rustup target add armv7-unknown-linux-gnueabihf
- 2. Build the project locally
 - a. cargo build --target armv7-unknown-linux-gnueabihf

TARGET MACHINE

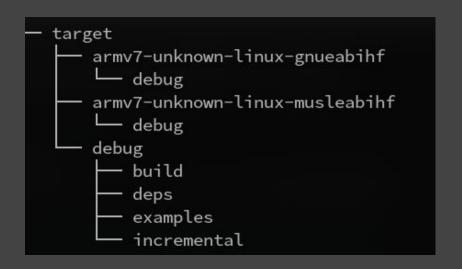
- Install rust-std for our target
 a. rustup target add armv7-unknown-linux-gnueabihf
- 2. Build the project locally
 - a. cargo build --target armv7-unknown-linux-gnueabihf

TARGET MACHINE

```
error: linking with `cc` failed: exit code: 1
```

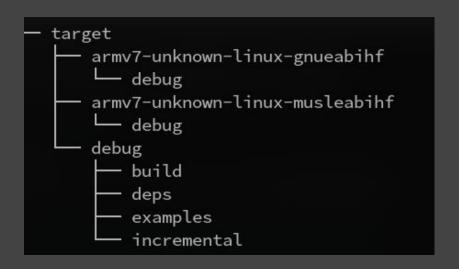
- Install rust-std for our target
 a. rustup target add armv7-unknown-linux-gnueabihf
- 2. Build the project locally a. cargo build --target armv7-unknown-linux-gnueabihf
- 3. Download the compiler
 - a. https://www.linaro.org/downloads/-arm-linux-gnueabihf[bin]
- 4. Make it visible a. export PATH=\$PATH:BIN_FOLDER TARGET MACHINE
- 5. Edit cargo configuration file
 - a. \$EDITOR ~/.cargo/config[target.armv7-unknown-linux-gnueabihf]linker = "arm-linux-gnueabihf-gcc"
- 6. Build it 🎶 one more time 🎶
 - a. cargo build --target armv7-unknown-linux-gnueabihf

- 1. Install rust-std for our target a. rustup target add armv7-unknown-linux-musleabihf
- 2. Download the compiler
 - a. https://musl.cc/ armv7l-linux-musleabihf-cross.tgz
- 3. Make it visible
 - a. export PATH=\$PATH:BIN_FOLDER
- 4. Edit cargo configuration file TARGET MACHINE
 - a. \$EDITOR ~/.cargo/config[target.armv7-unknown-linux-musleabihf]linker = "armv7l-linux-musleabihf-gcc"
- 5. Build the project locally
 - a. cargo build --target armv7-unknown-linux-musleabihf



- 1. cargo build
 - a. file executable:
 - cross: ELF 64-bit LSB pie executable, x86-64, version 1 (SYSV), dynamically linked, interpreter...
 - b. Idd executable:

```
linux-vdso.so.1 (0x00007ffca0df7000)
libdl.so.2 => /usr/lib/libdl.so.2 (0x00007fe61751b000)
libpthread.so.0 => /usr/lib/libpthread.so.0 (0x00007fe6174f9000)
libgcc_s.so.1 => /usr/lib/libgcc_s.so.1 (0x00007fe6174df000)
libc.so.6 => /usr/lib/libc.so.6 (0x00007fe617318000)
/lib64/ld-linux-x86-64.so.2 => /usr/lib64/ld-linux-x86-64.so.2
(0x00007fe61755d000)
```



- 1. cargo build --target armv7-unknown-linux-gnueabihf
 - a. file executable:
 - i. cross: ELF 32-bit LSB pie executable, ARM, EABI5 version 1 (SYSV),
 dynamically linked, interpreter ...
 - b. Idd executable:

```
linux-vdso.so.1 (0x7eff8000)
```

libc.so.6 => **Not found**

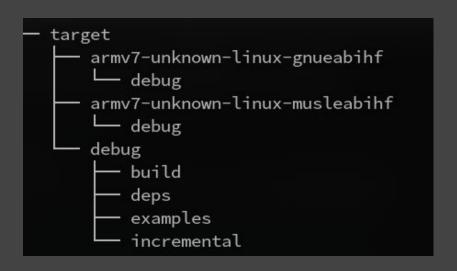
/lib/ld-linux-armhf.so.3 => /usr/lib/ld-linux-armhf.so.3 (0x76f49000)

libdl.so.2 => /usr/lib/libdl.so.2 (0x76a66000)

libpthread.so.0 => /usr/lib/libpthread.so.0 (0x76a3c000)

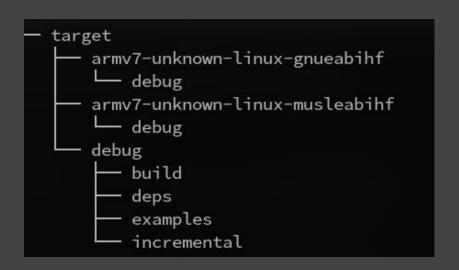
 $libgcc_s.so.1 => /usr/lib/libgcc_s.so.1 (0x76a0f000)$

libm.so.6 => **Not found**



- 1. cargo build --target armv7-unknown-linux-gnueabihf
 - a. file executable:
 - i. cross: ELF 32-bit LSB pie executable, ARM, EABI5 version 1 (SYSV),
 dynamically linked, interpreter ...
 - b. Idd executable:

```
linux-vdso.so.1 (0x7eff8000)
libc.so.6 => /usr/lib/libc.so.6 (0x76a79000)
/lib/ld-linux-armhf.so.3 => /usr/lib/ld-linux-armhf.so.3 (0x76f49000)
libdl.so.2 => /usr/lib/libdl.so.2 (0x76a66000)
libpthread.so.0 => /usr/lib/libpthread.so.0 (0x76a3c000)
libgcc_s.so.1 => /usr/lib/libgcc_s.so.1 (0x76a0f000)
libm.so.6 => /usr/lib/libm.so.6 (0x769a1000)
```



- 1. cargo build --target armv7-unknown-linux-musleabihf
 - a. file executable:
 - i. cross: **ELF 32-bit LSB** executable, **ARM, EABI5** version 1 (SYSV), **statically linked**, with ...
 - b. Idd executable:not a dynamic executable

Examples

- 1. https://github.com/patrickelectric/bridges
 - a. A bidirectional Serial-UDP bridge!







- 2. https://github.com/patrickelectric/mavlink2rest
 - a. REST server that provides mavlink information from a mavlink source



Demo









https://patrickelectric.work

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