

Indian Institute of Technology Indore
Computer Science & Engineering
End-Semester Examination: Logic Design (CS 206)
March 9, 2021

Full Marks: 40/ Duration: 3 hrs

1. Design an Asynchronous Mod 19 counter using JK FF.
(10 marks)
2. Design a counter that changes states from $1 \rightarrow 2 \rightarrow 4 \rightarrow 8 \rightarrow 16$
(10 marks)
3. Convert a JK-FF to D-FF and vice versa.
(10 marks)
4. Design a counter that counts both 0 to 15 and 15 down to 0.
(10 marks)