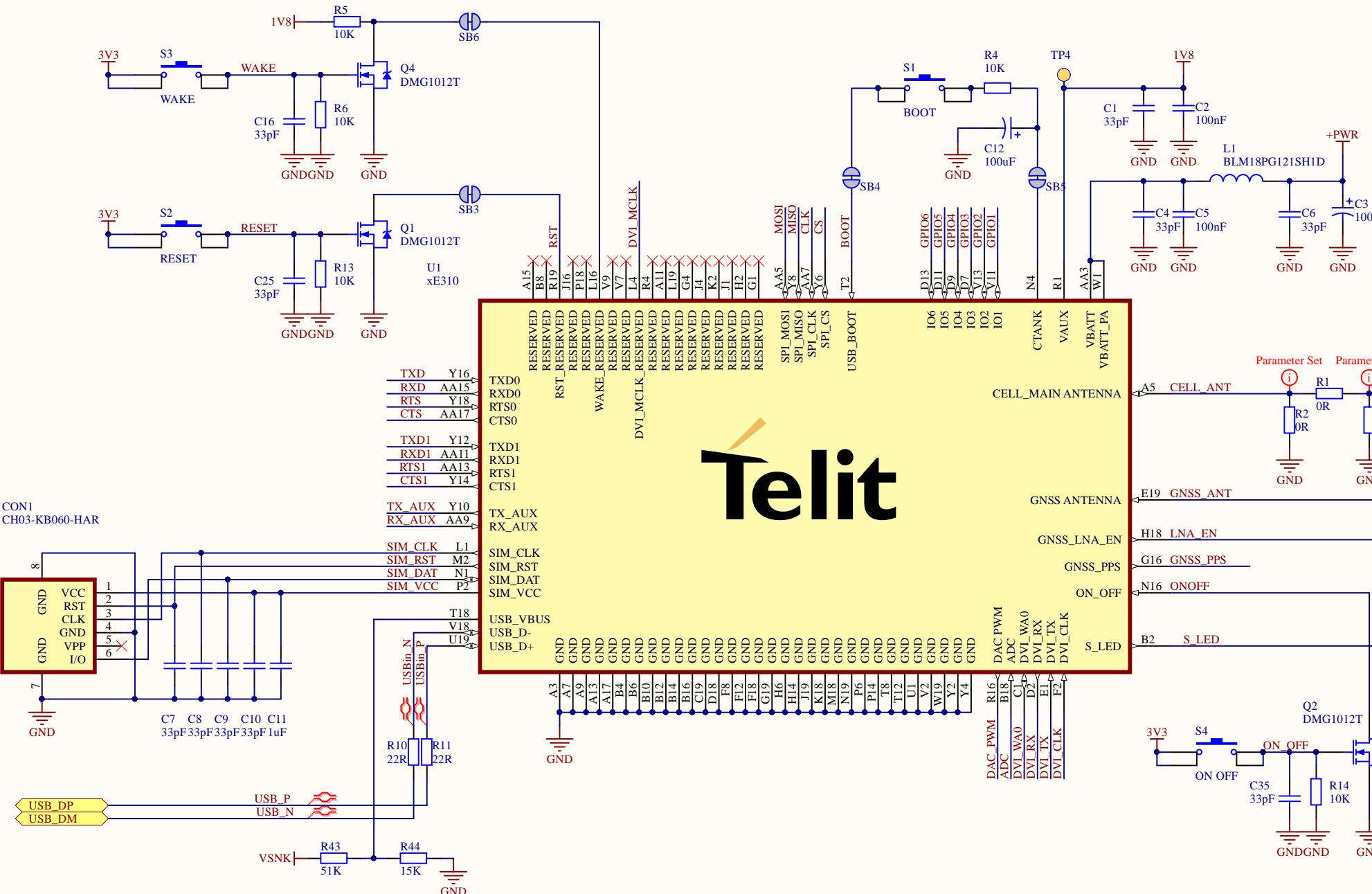
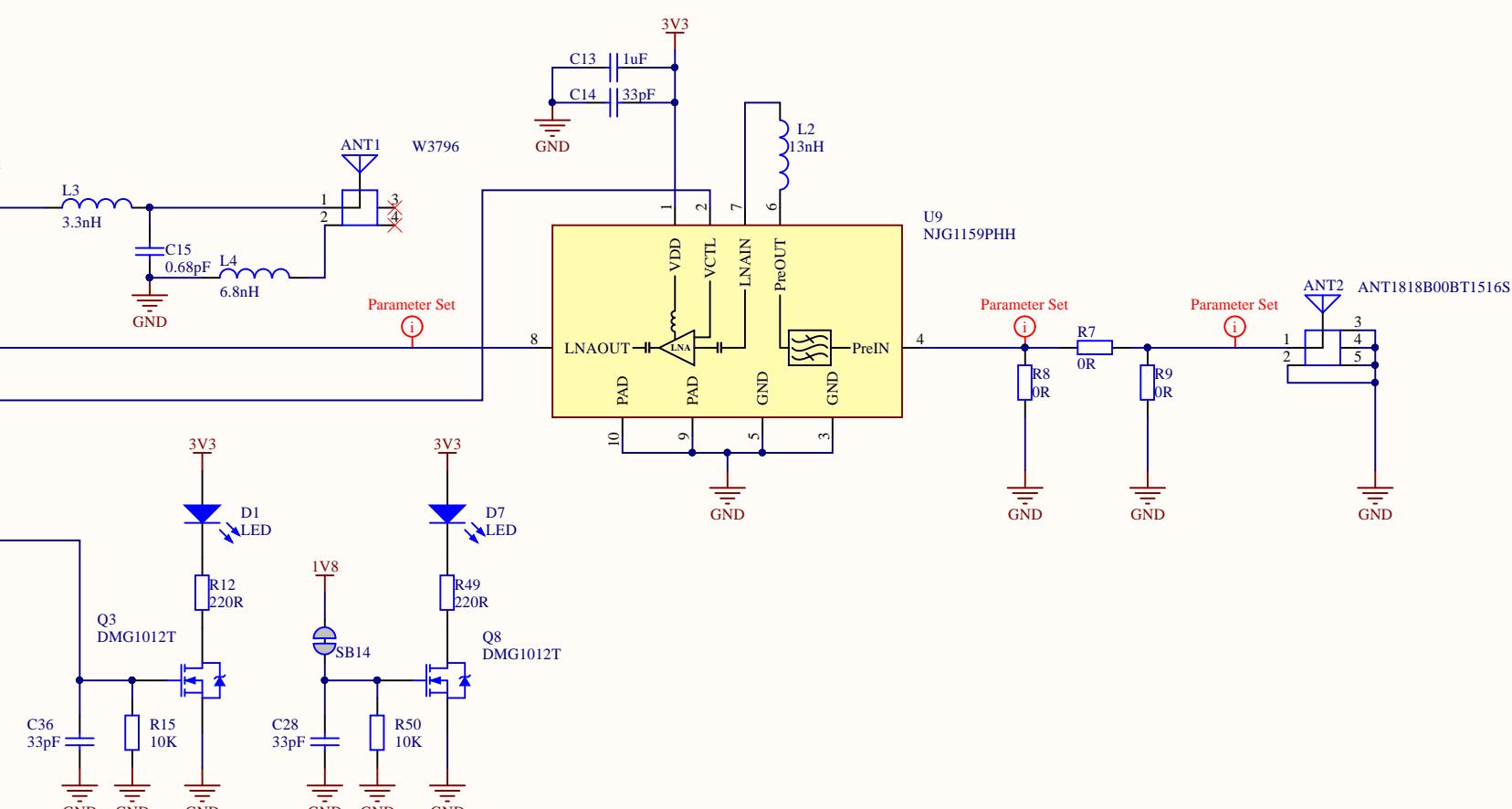


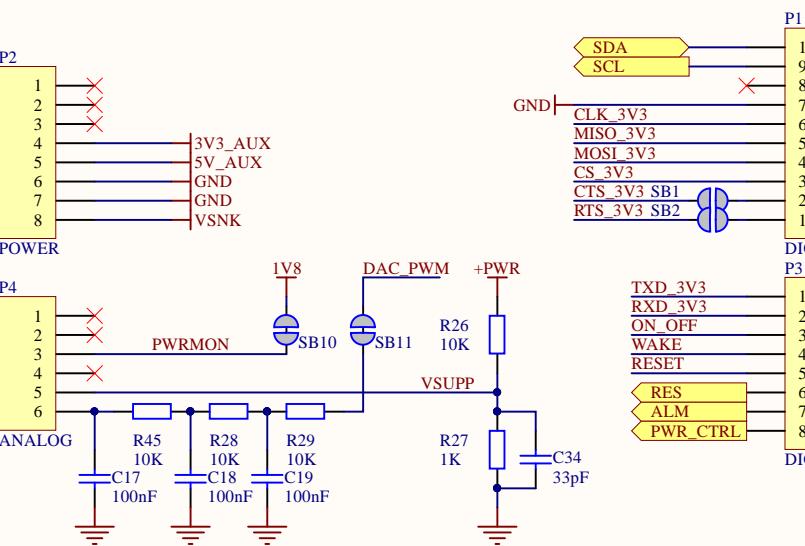
IoT Modem



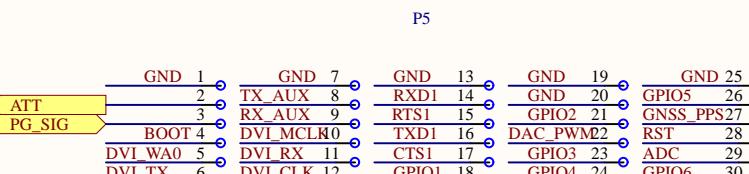
GNSS Amplifier



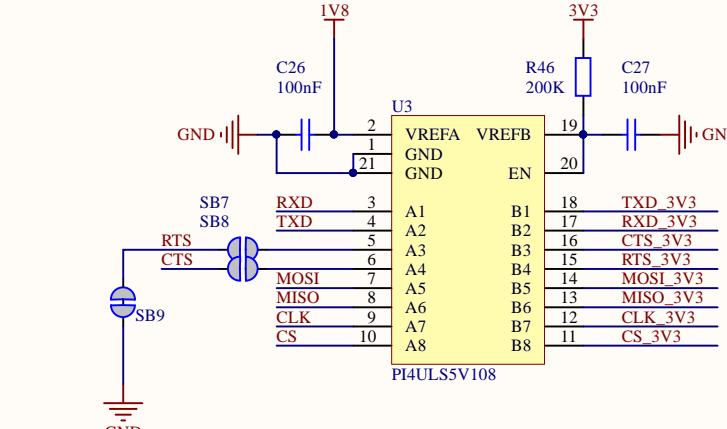
Arduino Connectors

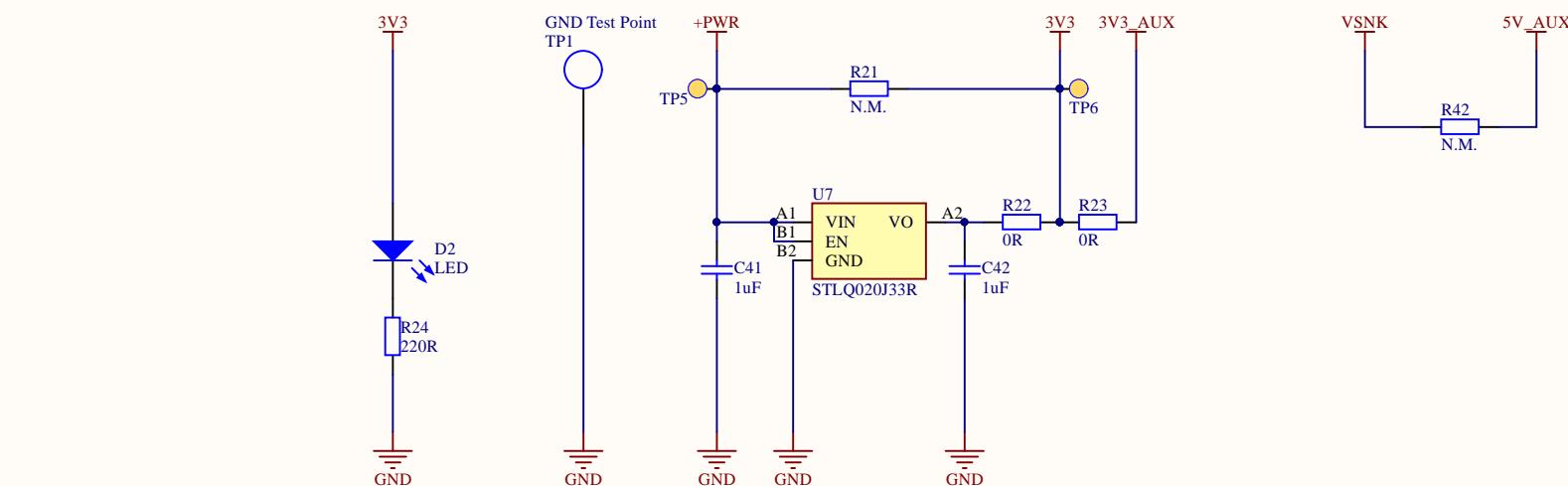
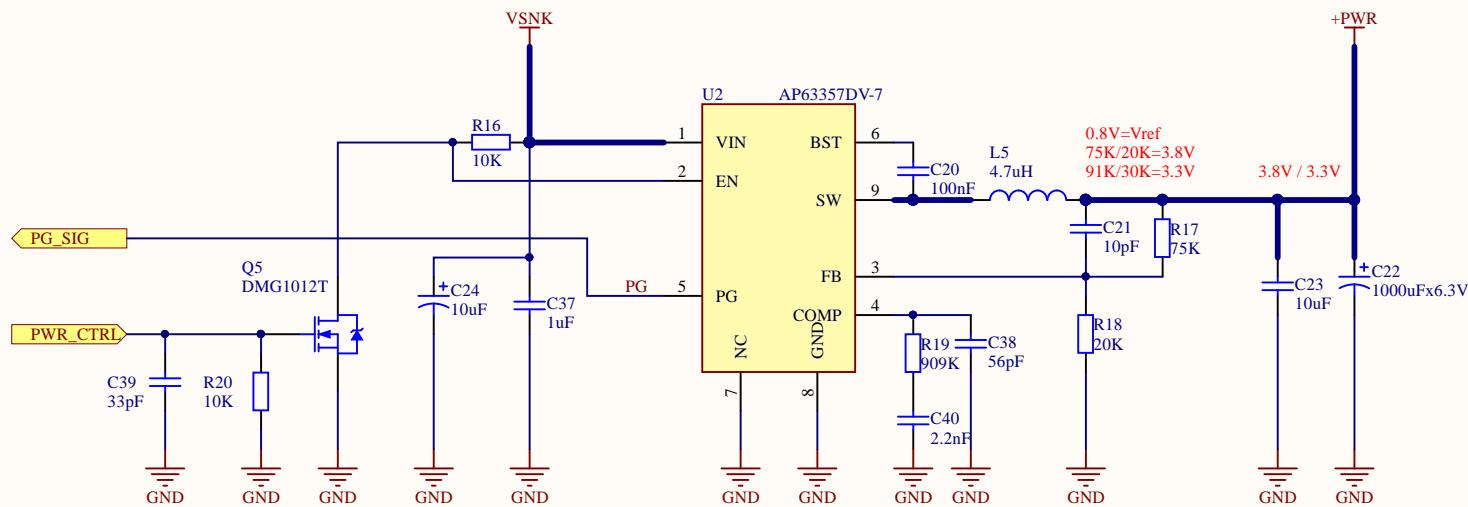


Pin Inputs/Outputs

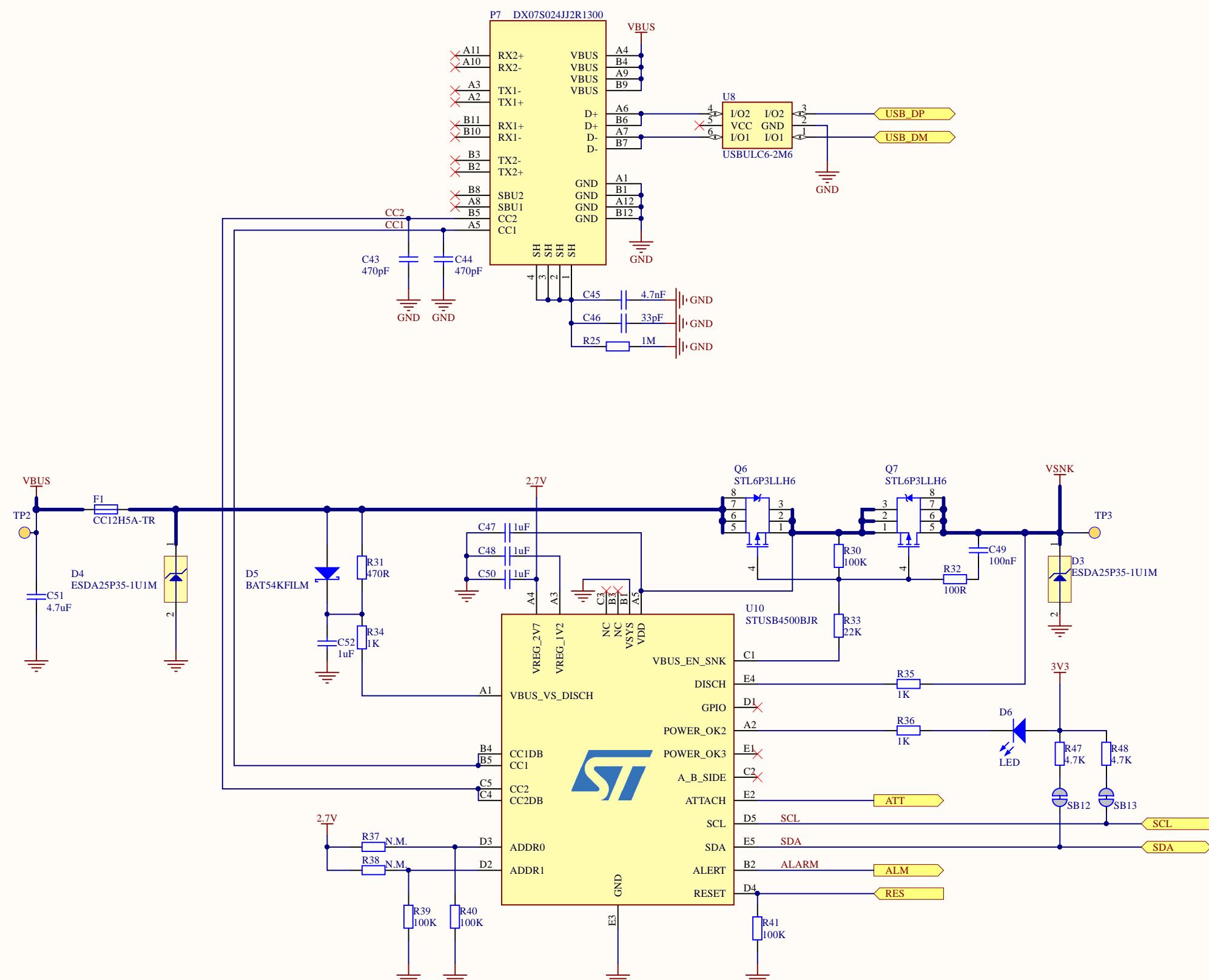


Logic Level Shifter



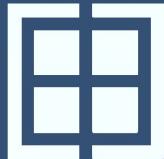


Title		
Size	Number	Revision
A4		
Date: 2020-03-17	Sheet of	
File: C:\Users\...\Power.SchDoc		Drawn By:



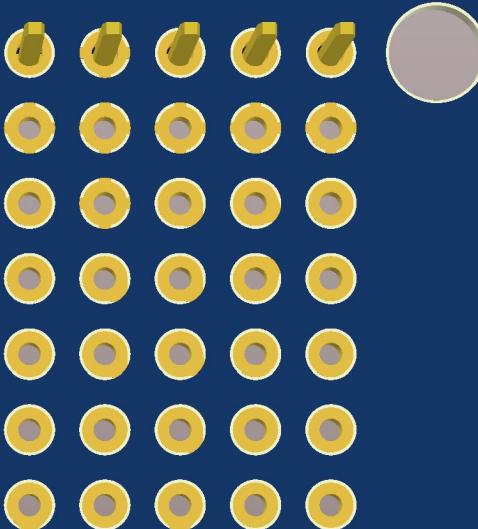
Title		
Size	Number	Revision
A3		
Date: 2020-03-17	Sheet of	
File: C:\Users\..\USBDP_Sink.SchDoc		Drawn By:

RUT_XE310
Rev1



This detailed schematic diagram illustrates the complex internal structure of a BeagleBoard (Revision E). The board features a central green octagonal component labeled 'Case B' and a large orange central processing unit (CPU) package. Key components include a TI OMAP3 processor, memory chips (SDRAM), and various passive components like resistors (R1-R46, C1-C27, L1-L4), capacitors, and inductors. Numerous connectors are present, including the USB port (CON1), SD card slot (S1-S3), and various pins (P1-P7) along the edges. Power management is handled by a U3 voltage regulator, and the board includes multiple antenna pads (ANT1, ANT2, ANT3) and a nano SIM card slot. A large central area is shaded gray, possibly representing a thermal shield or a specific functional block.

POWERED BY



P5		
1	GND	2 ATTACH
3	PG SIGNAL	4 BOOT
5	DVI WA0	6 DVI TX
7	GND	8 TX AUX
9	RX AUX	10 DVI MCLK
11	DVI RX	12 DVI CLK
13	GND	14 RXD1
15	RTS1	16 TxD1
17	CTS1	18 GPIO1
19	GND	20 GND
21	GPIO2	22 DAC PWM
23	GPIO3	24 GPIO4
25	GND	26 GPIO5
27	GNSS PPS	28 RST
29	ADC	30 GPIO6

P2	
1	n.c.
2	n.c.
3	n.c.
4	+3.3V
5	+5V
6	GND
7	GND
8	VIN/VSNK

P3	
1	TXD_3.3V
2	RXD_3.3V
3	ON OFF
4	WAKE
5	RESET
6	RST USBPD
7	ALM USBPD
8	PWR USBPD

P4	
1	n. c.
2	n. c.
3	n. c.
4	PWRMON
5	VSUPP
6	DAC PWM

P1	
1	RTS_3V3
2	CTS_3V3
3	SPI CS
4	SPI MOSI
5	SPI MISO
6	SPI CLK
7	GND
8	n. c.
9	SCL
10	SDA

