



Getting Started with CYB06447BZI-BLD53 Development Platform – **RDK3**

Registration & Download





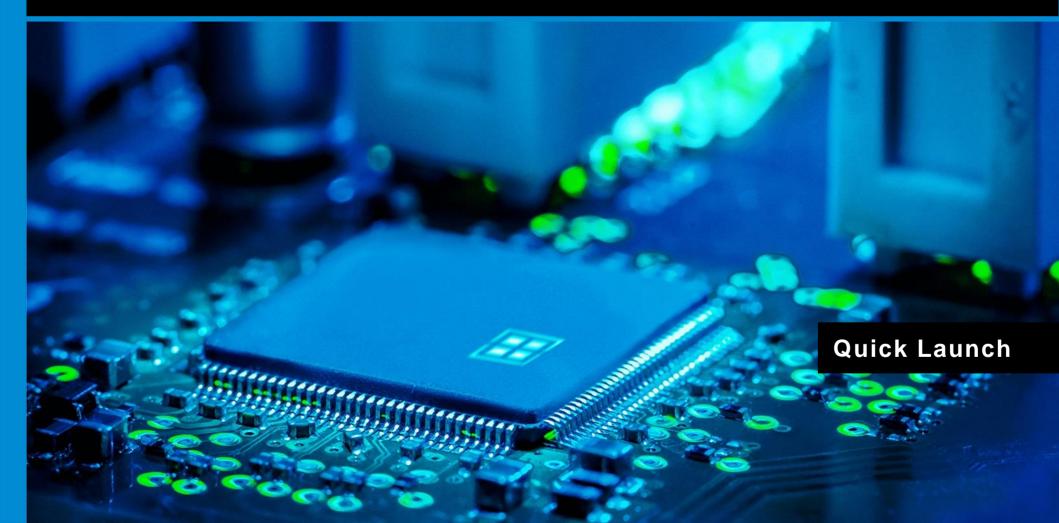
1.) Register or/and login to the Infineon website, press on "myInfineon" tab.

https://www.infineon.com

2.) Download and install the latest <u>ModusToolbox™</u> software.

3.) [Optional] Download and install yours prefered terminal emulator, for example: Putty, Tera Term, etc.





Connect the RDK3



Connect the RDK3 to your PC.



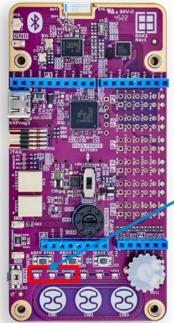
Look for the USB-C socket with a marking "KitProg3"

Connect it with your PC

Connect the RDK3



Check if the RDK3 is ready.



"POWER" and "DEBUG" LEDs must shine constantly. The "CHARGE" LED will be blinking if no battery is connected.







The "KitProg3" must be seen in the "Device Manager" window.

Working with the ModusToolbox and Rutronik PC

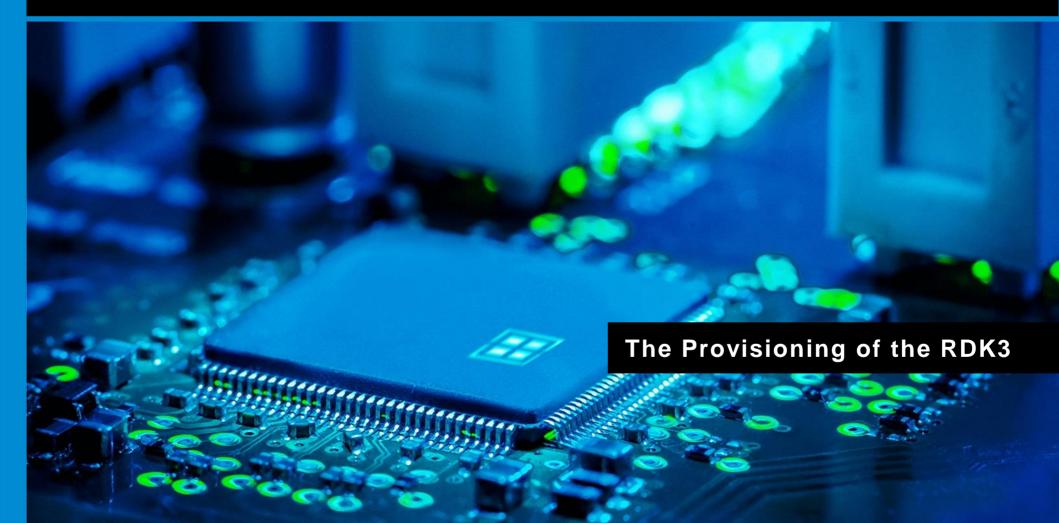


If you are working with your personal PC, (not the Rutronik provided Laptop PC) please skip this setup.

Open the File \rightarrow New \rightarrow "ModusToolbox Application" \rightarrow Settings \rightarrow Proxy server settings and enter the proxy address: http://iwsva.rut.local:8080

Proxy server	settings - Project Creator 2.0	×	
○ Direct ⑥ Manual [http://iwsva.rut.local:8080 OK	Cancel	







- The RDK3 is equipped with a PSoC™ 64 "Secure" MCU CYB06447BZI-BLD53.
- The PSoC™64 device must be provisioned with keys and policies before being programmed.
- If the unsigned or not properly signed image will be written to the RDK3 PSoC™64 – the microcontroller will not startup.
- You may also refer to the "Secure Policy" Configurator guide.

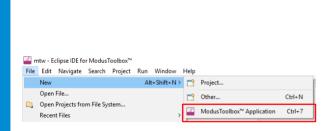
Additional Information

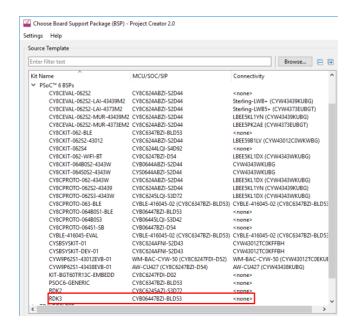
PSoC™ 64 - Secured MCU

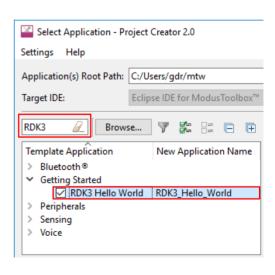
PSoC™ 64 Provisioning Specification



- 1.) Open the "Project Creator" tool: File → New → ModusToolbox™ Application
- 2.) Select the "RDK3" BSP. It is in PSoC™ 6 BSPs list.
- 3.) Click on "Next".
- 4.) Write a "RDK3" in a Search... window. Select the example from given categories list.
- 5.) Click on "Create".





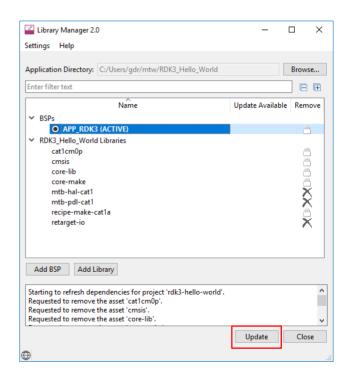




6.) After project creation is finished - update libraries with "Library Manager" tool.

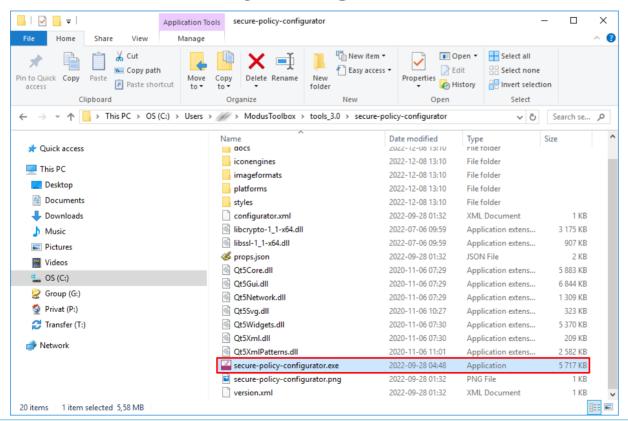






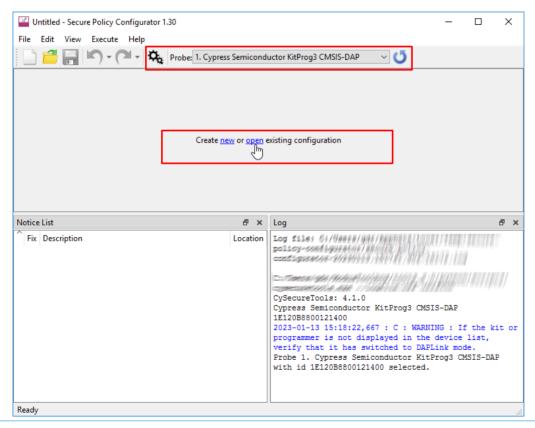


7.) Load the "Secure Policy Configurator".



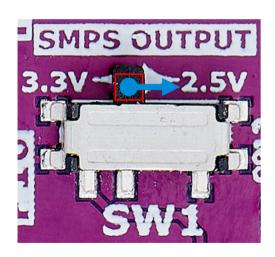


- 8.) Load the "Secure Policy Configurator".
- 9.) Select the probe: Cypress Semiconductor KitProg3 CMSIS-DAP [press the PROG MODE button if the CMSIS-DAP is not present in a list].
- 10.) Open existing configuration [Select the RDK3_Hello_World directory\policy\policy\single_CM0_CM4.json].

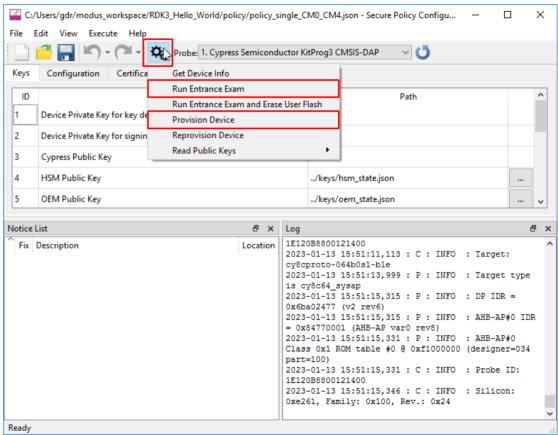




- 11.) Configure the settings according to your needs or leave them as it is.
- 12.) Set the SW1 "SMPS OUTPUT" to the 2.5V position.
- 13.) "Run The Entrance Exam" and then "Provision the Device"

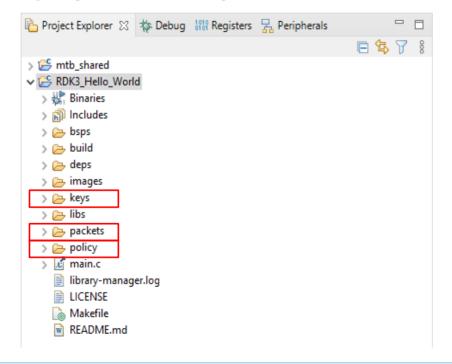






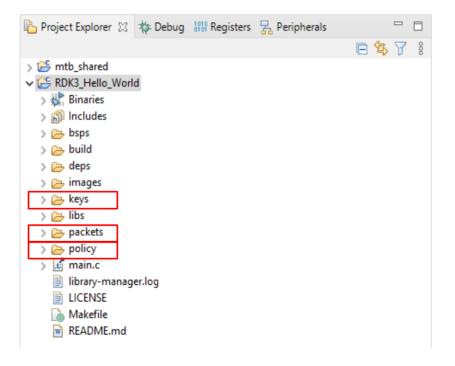


The provisioning procedure could also be done using a new project that was created using a RDK3 BSP. Load the "Secure Policy Configurator" and select Create "new" configuration. The "keys", "packets", and "policy" configurations will be created in your project directory.



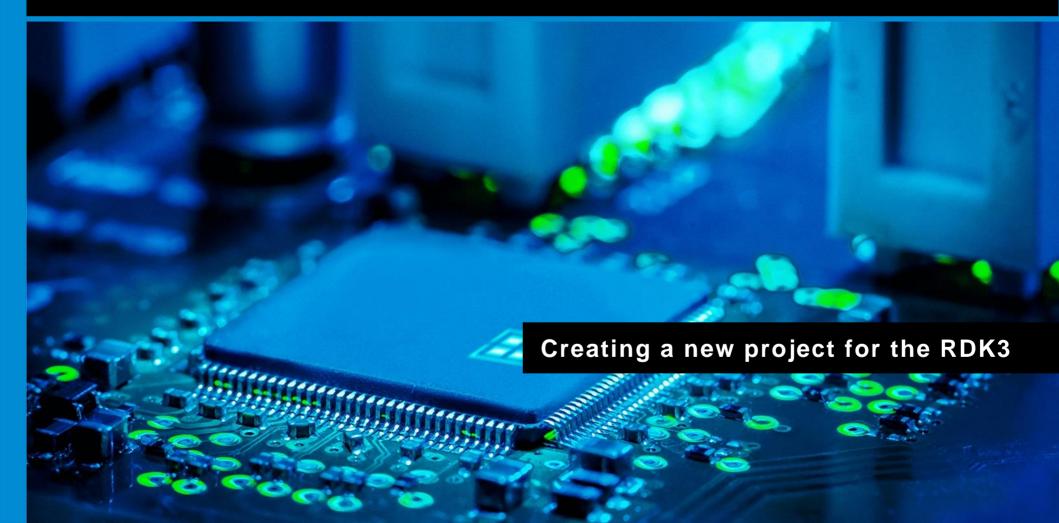


Please store the "keys", "policy" and "packets" folders with all the content in a safe location for later use.



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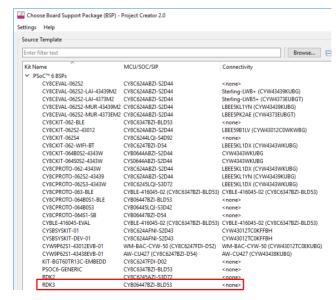
- 1.) Open the "Project Creator" tool: File → New → ModusToolbox™ Application
- 2.) Select the "RDK3" BSP. It is in PSoC™ 6 BSPs list.
- 3.) Click on "Next".

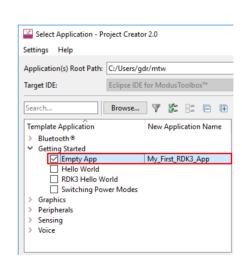
4.) Select a "Empty App" in a "Getting Started" category. Name it

"My_First_RDK3_App".

5.) Click on "Create".

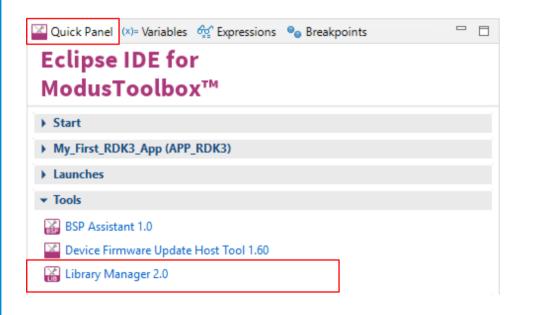




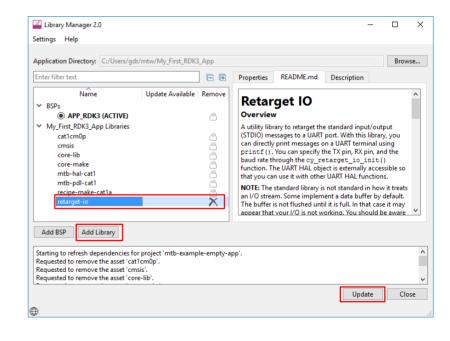




6.) Include the "retarget-io" library in a "Library Manager" tool and press "Update".

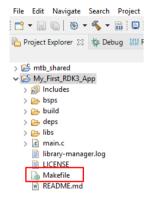








7.) Modify the "Makefile" to disable code optimisation*



APPNAME=my-first-rdk3-app CONFIG=Costum CFLAGS =-O0

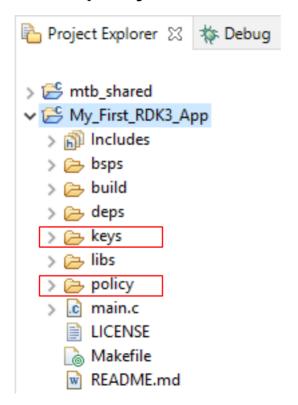
8.) Press "Generate Launches" in Quick Panel



*only for debugging, learning and demo purposes. Normally, code optimisations should never be disabled.



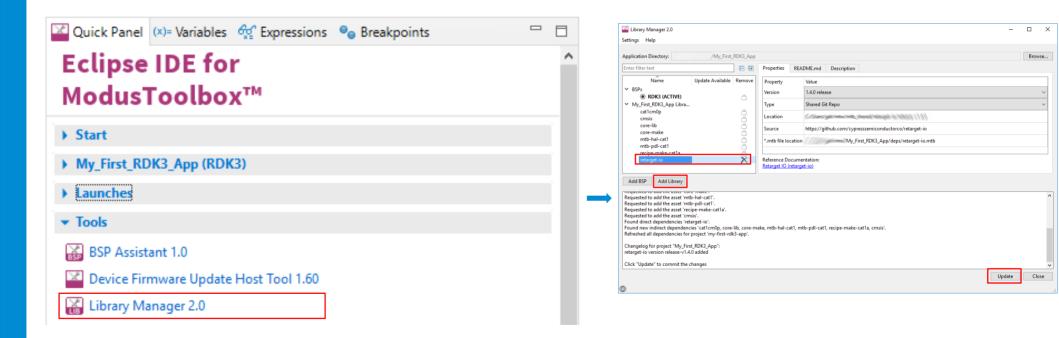
9.) Copy and paste the "keys" and "policy" folders with all the files into your project.



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10.) Select "retarget-io" library in a "Library Manager" tool and press "Update".



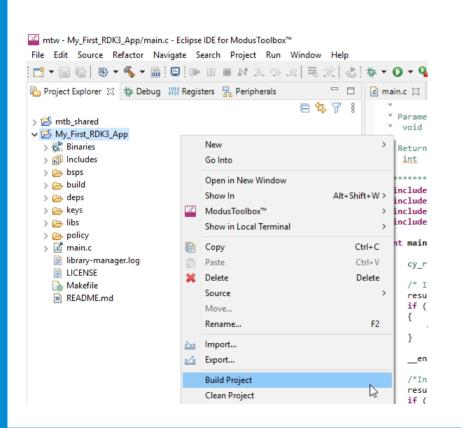


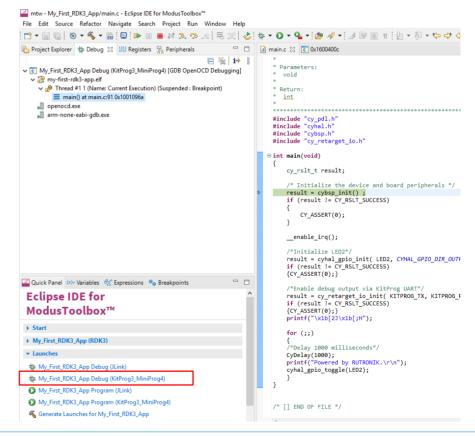
11.) Copy/Paste and save the code example to the "main.c" file.

```
#include "cy_pdl.h"
#include "cvhal.h"
#include "cvbsp.h"
#include "cy retarget io.h"
int main(void)
    cv rslt t result;
    /* Initialize the device and board peripherals */
    result = cybsp init();
    if (result != CY RSLT SUCCESS)
        CY ASSERT(0);
    __enable_irq();
    /*Initialize LED2*/
    result = cyhal gpio init( LED2, CYHAL GPIO DIR OUTPUT, CYHAL GPIO DRIVE STRONG, CYBSP LED STATE OFF);
    if (result != CY RSLT_SUCCESS)
    {CY ASSERT(0):}
    /*Enable debug output via KitProg UART*/
    result = cy retarget io init( KITPROG TX, KITPROG RX, CY RETARGET IO BAUDRATE);
    if (result != CY RSLT SUCCESS)
    {CY ASSERT(0);}
    printf("\x1b[2J\x1b[;H");
    for (;;)
    /*Delay 1000 milliseconds*/
    CyDelay(1000);
    printf("Powered by RUTRONIK.\r\n");
    cyhal gpio toggle(LED2);
```



12.) Build and Debug the active project.

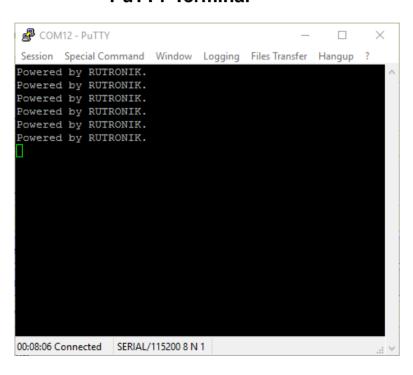




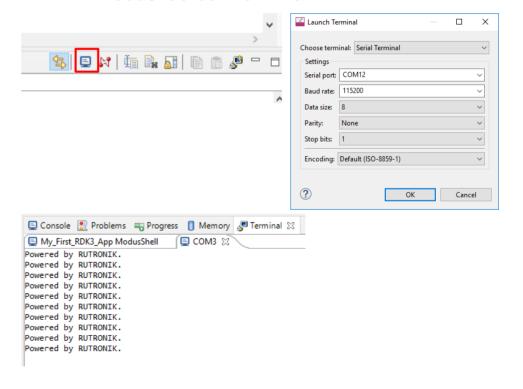


The final result is a blinking LED2 on the RDK3 board and text on the terminal window:

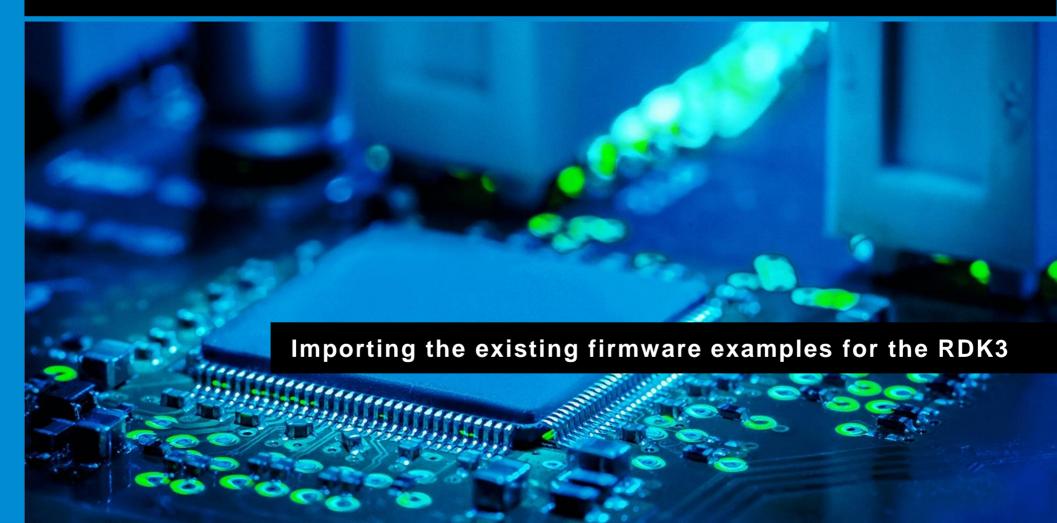
PuTTY Terminal



ModusToolbox Terminal



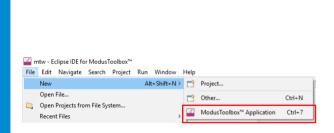


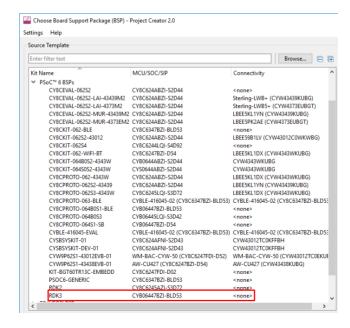


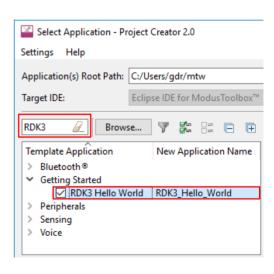
Importing firmware examples with "Project Creator" tool



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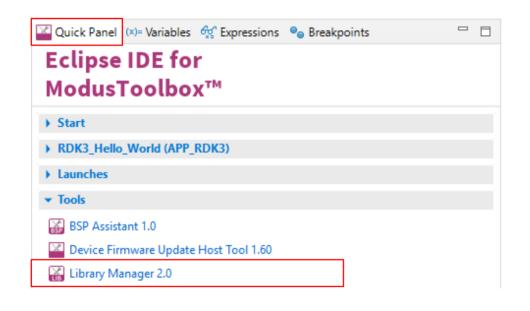




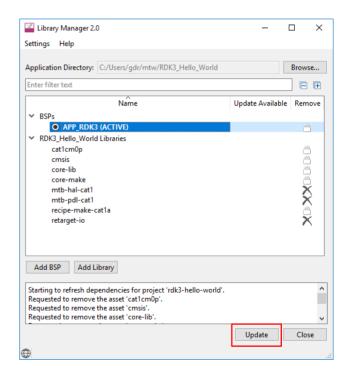
Importing firmware examples with "Project Creator" tool



6.) After project creation is finished - update libraries with "Library Manager" tool.



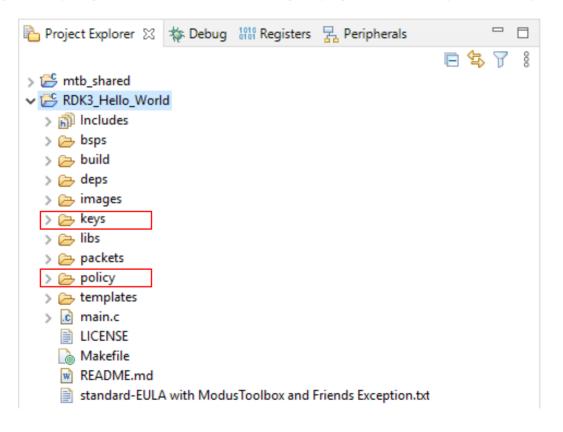




Importing firmware examples with "Project Creator" tool

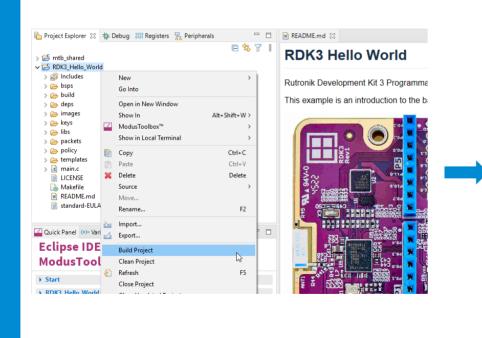


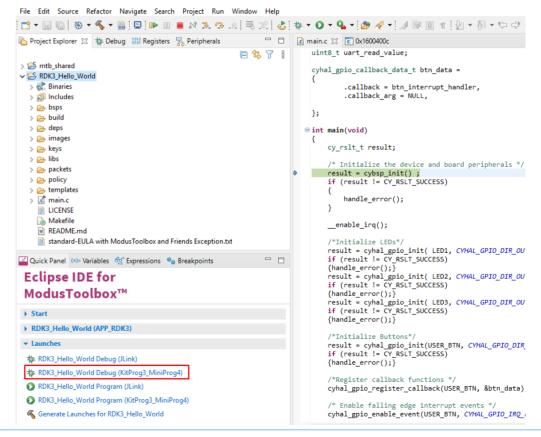
7.) Copy and paste the "keys" and "policy" folders with all the files into your project. The folder "packets" is optional, needed only for the provisioning.





8.) Build and Debug the project.

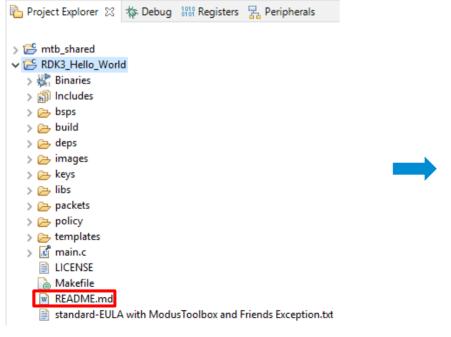


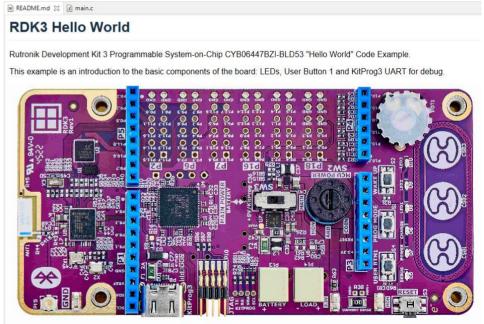


RDK3 README.md



Check the README.md file before starting to explore the code example. You may find important hints or what else is needed to have firmware running properly.









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