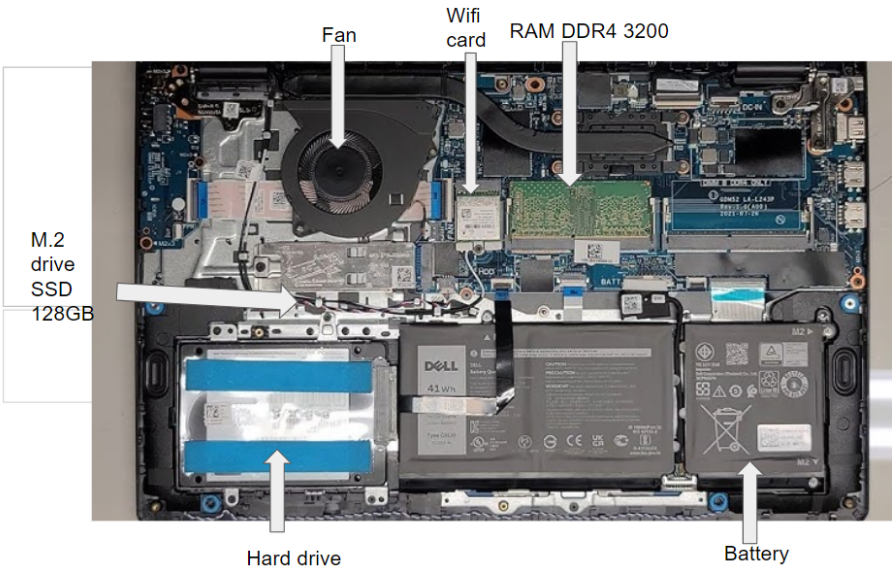
Name: Rutvi shah
Matriculation:23159043
Idm :to20raje

Device name	DESKTOP-94J83OI
Processor	11th Gen Intel(R) Core(TM) i7-1165G7 @ 2.80GHz 2.80 GHz
Installed RAM	16.0 GB (15.7 GB usable)
Device ID	E86529ED-8AA7-4A18-BCCE-9BC6A87CF414
Product ID	00342-22015-24084-AAOEM
System type	64-bit operating system, x64-based processor
Pen and touch	No pen or touch input is available for this display

Edition	Windows 11 Home
Version	22H2
Installed on	28-02-2023
OS build	22621.2428
Experience	Windows Feature Experience Pack 1000.22674.1000.0



Designing a Unified Programming Model for Heterogeneous Machines:

- The paper highlights the rapid evolution of mainstream computing devices towards heterogeneous architectures, including both CPUs and GPUs, to improve computational throughput and energy efficiency.
- The paper addresses a pressing issue in modern computing, where the hardware landscape is rapidly changing. It provides a potential solution that could make it easier for developers to harness the power of heterogeneous architectures.
- The paper introduces the Phalanx programming model, which aims to provide a unified approach for programming heterogeneous machines.
- It discusses the challenges that arise with these new architectures. Traditional programming languages have not kept up with these changes and often reflect a homogeneous processing model.