F	Ref				ArduinoIDE		SDK上の				電源	投入後	Arduin	io起動後	SDK起	型動後		ピングルー	- プ		CXD5602	GG ピン機能		最大絶対定格		
		.TE	ピン	回路図上の名前	上の名称	SDK上の名称	ピン番号	タイプ	dir	電圧	dir			初期値		刀期値	· 接続先 —	モード名		モード0	モード1	モード2	モード3	電圧(V)	拡張ボード上の機能	説明
JP1			1	GND	_	_	_	Power	_	_	_	_	_	_	_	_	-	_	_	-	_	-	_			
JP1			2	UART2_TX	D01	PIN_UART2_TXD	67	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_TXD	-	GPIO	2.5		
JP1			3	UART2_RX	D00	PIN_UART2_RXD	68	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO OPIO	UART2_RXD	-	GPIO OPIO	2.5		
JP1 JP1			5	UART2_RTS UART2_CTS	D28 D27	PIN_UART2_RTS PIN_UART2_CTS	70 69	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	UART2 UART2	P1n P1n	GPIO GPIO	UART2_RTS UART2_CTS	_	GPIO GPIO	2.5		
JP1			6	I2S0_BCK	D26	PIN_I2S0_BCK	93	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_BCK	_	GPIO	2.5		
JP1			7	I2S0_LRCK	D25	PIN_I2S0_LRCK	94	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_LRCK	_	GPIO	2.5		
JP1			8	SPI5_CS_X	D24	PIN_SPI5_CS_X	76	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CMD	SPI5_CS_X	GPIO	2.5		
JP1			9	SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5		
JP1			10	3.3V		_	_	Power	0	3.3	0	_	0		0	_	-		_	_	_	_	_			
JP1			11	1.8V			- 27	Power	0	1.8	0	- 11: 7	O	- 11: 7	0	- II: 7	- CVDE603CC	- CENTIDO IN	- D1 o	- CDIO	- CENTIDO IN	_	_	2.5		
JP1 JP1		+	12 13	SEN_IRQ SEN_AIN4	D22 A2	PIN_SEN_IRQ_IN LPADC2	37	Digital Analog	1/0	1.8 ~0.7	_ 	Hi-Z –	_	Hi-Z —	_	Hi-Z	CXD5602GG CXD5602GG	SEN_IRQ_IN	P1e _	GPIO _	SEN_IRQ_IN _	_		2.5 1.05		
JP2			1	XRST	_	(SPR_RST_X)	_	Digital	0	1.8	_	Low	0	High	0	High	CXD5602GG		_	_	_	_	_	1.03		
JP2			2	1.8V	_		_	Power	0	1.8	0	_	0		0	_	_	_	_	_	_	_	_			
JP2			3	3.7V(4.0V)	_	_	_	Power	1/0	3.6-4.4	0	_	0	_	0	_	-	_	_	-	_	_	-	7		
JP2			4	GPIO	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMCB	P1q	GPIO	EMMC_DATA3	-	GPIO	2.5		
JP2			5	GPIO	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8	-	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMCB	P1q	GPI0	EMMC_DATA2	_	GPIO	2.5		
JP2			6	I2SO_DATA_IN	D19	PIN_I2SO_DATA_IN	95	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	1250	P1v	GPIO GPIO	I2SO_DATA_OUT	_	GPIO GPIO	2.5		
JP2 JP2			/ Ω	I2S0_DATA_OUT SPR_SPI5_MISO	D18	PIN_I2SO_DATA_OUT PIN_SPI5_MISO	96 78	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z	CXD5602GG CXD5602GG	I2S0 EMMCA	P1v P1n	GPIO GPIO	I2SO_DATA_OUT EMMC_DATA1	SPI5_MISO	GPIO GPIO	2.5		
JP2	+ +		9	SPR_SPI5_MISU SPR_SPI5_MOSI	D17	PIN_SPI5_MOSI	77	Digital	1/0	1.8		Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG CXD5602GG	EMMCA	P1p P1p	GPIO GPIO	EMMC_DATA1	SPI5_MOSI	GPIO GPIO	2.5		
JP2	+		10	GND	_			Power	-		_	-	_	-	_	_	-	–		-	-	-	_	2.5		
JP2			11	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0	1.8	_	High	I	High		High	CXD5602GG	I2C0	P1j	GPIO	I2C0_BCK	_	_	2.5		
JP2			12	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0	1.8	_	High		High	I	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	_	2.5		
JP2			13	SEN_AIN5	A3	LPADC3		Analog	I	~0.7	I	_	1	_	1	_	CXD5602GG	-	_	-	-	-	_	1.05		
CN5	CN1		1	MCLK	_	_	_	Digital	0	1.8	_	_	Ο	Hi-Z	0	Hi-Z	26MHz TCXO		_	_	_	-	_	2.5		
CN5	CN1		2	GND	_		-	Power	-	1.0	_	-	_		_	-	-		- D17	-	-	- 0000 M400	-	0.5		
CN5	CN1 CN1		3	I2C_SDA		PIN_SPI0_MISO	20	Digital	1/0	1.8	_	High	_	High		High	CXD5602GG	12C2 12C2	P17 P17	GPIO GPIO	I2C2_BDT	SPIO_MISO	GPIO GPIO	2.5		
CN5 CN5	CN1		5	I2C_SCL XRS	D35	PIN_SPI0_MOSI PIN_SDIO_DIR1_3	91	Digital Digital	1/0	1.8	_	High Hi-Z	_	High Hi-Z	_	High Hi-Z	CXD5602GG CXD5602GG	SDIOC	P1t	GPIO	I2C2_BCK SDIO_DIR1_3	SPI0_MOSI GPIO	GPIO	2.5		
CN5	CN1		6	PWDN	D34	PIN_SDIO_DIR0	90	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_DIR0	GPIO	GPIO	2.5		
CN5	CN1		7	LDO_EN	_	(ACP_GPO4)	_	Digital	0	3.6-4.4	0	Hi-Z	Ο	Low	0	Low	CXD5247GF	_	_	_		_	_			
CN5	CN1		8	VDD_3.7V	_	_	_	Power	0	3.6-4.4	0	_	Ο	_	0	_	_	_	_	_	_	_	-			
CN5	CN1		9	IS_DATA4	_	PIN_IS_DATA4	63	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA4	GPIO	GPIO	2.5		
CN5	CN1		10	IS_DATA6	_	PIN_IS_DATA6	65	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_DATA6	GPIO	GPIO	2.5		
CN5	CN1		11	IS_DATA0	_	PIN_IS_DATA0	59	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	1	_	CXD5602GG	IS	P1m	GPIO OPIO	IS_DATA0	GPIO	GPIO OPIO	2.5		
CN5	CN1		12	IS_DATA5		PIN_IS_DATA5	66	Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z		_	CXD5602GG CXD5602GG	IS IS	P1m	GPIO GPIO	IS_DATA5	GPIO GPIO	GPIO GPIO	2.5		
CN5 CN5	CN1 CN1		13 14	IS_DATA5 IS_DATA2		PIN_IS_DATA5 PIN_IS_DATA2	64	Digital Digital	1/0	1.8	_	Hi-Z	_	Hi-Z Hi-Z		_	CXD5602GG CXD5602GG	IS IS	P1m P1m	GPIO GPIO	IS_DATA5 IS_DATA2	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1		15	IS_HSYNC	_	PIN_IS_HSYNC	58	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	 	_	CXD5602GG	IS	P1m	GPIO	IS_HSYNC	GPIO	GPIO	2.5		
CN5	CN1		16	IS_DATA3	_	PIN_IS_DATA3	62	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	1	_	CXD5602GG	IS	P1m	GPIO	IS_DATA3	GPIO	GPIO	2.5		
CN5	CN1		17	IS_VSYNC	_	PIN_IS_VSYNC	57	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	ı	_	CXD5602GG	IS	P1m	GPIO	IS_VSYNC	GPIO	GPIO	2.5		
CN5	CN1		18	IS_DATA1	_	PIN_IS_DATA1	60	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA1	GPIO	GPIO	2.5		
CN5	CN1		19	GND	_	_	_	Power	_	_	-	_	_	_	_	_	-	_	_	_	_	_	_			
CN5	CN1	0.014.51.7	20	IS_CLK	_	PIN_IS_CLK	56	Digital		1.8	1	_	<u> </u>		1	_	CXD5602GG	IS	P1m	GPIO	IS_CLK	GPIO	GPIO	2.5		
CN4[L] CN4[L] CN4[L]		CN4[L]	J T	3.3V_AU				Power	0	3.3	1	_	l I			_	_				_	_				
CN4[L] CN4[L] CN4[L]		CN4[L]	5 5	3.3V_AU ACP_MICA				Power Analog	ı	3.3 _	'	_	<u>'</u> 			_	CXD5247GF			_						
CN4[L] CN4[L]		CN4[L]	7	ACP_MICB	_	_	_	Analog	1		,	_	·		1	_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]	+ +	CN4[L]	9	ACP_MICC	_	_	_	Analog	ı	_	I	_		_		_	CXD5247GF	_	_	_	_	_	_			
CN4[L] CN4[L]		CN4[L]	11	ACP_MICD	_	-	_	Analog	I	_	I	-	١	_	ı	_	CXD5247GF	_	_	_	_	-	_			
CN4[L] CN4[L]		CN4[L]	13	ACP_MICBIASA	_	_	_	Analog	0	2	0	_	0	-	0	_	CXD5247GF	_	_	_	_	-	_			
CN4[L] CN4[L]	+	CN4[L]	15	ACP_MICBIASB	_	-	_	Analog	0	2	0	_	Ο	_	0	_	CXD5247GF	_	_	_	_	_				
CN4[L] CN4[L]		CN4[L]	17	AGND_MIC	D15			Power	-	1 0	_	– Ligh	_		-	_ _اا	- CVD5603CC	- 1200	_ D1:	- CDIO		-		٥٢		
CN4[L] CN4[L] CN4[L]	+	CN4[L]	21	SPR_I2C0_SCL SPR_I2C0_SDA	D15 D14	PIN_I2C0_BCK PIN_I2C0_BDT	44 45	Digital Digital	I/O	1.8	_	High High	 	High High		High High	CXD5602GG CXD5602GG	12C0 12C0	P1j P1j	GPIO GPIO	I2C0_BCK I2C0_BDT	_		2.5		
CN4[L] CN4[L]		CN4[L]	23	SPR_SPI4_SCK	D14	PIN_SPI4_SCK	72	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG CXD5602GG	SPI4	P10	GPIO GPIO	SPI4_SCK		GPIO	2.5	+	
CN4[L] CN4[L]		CN4[L]	25	SPR_SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO GPIO	SPI4_MISO	_	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	27	SPR_SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0	1.8		Hi-Z		Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MOSI	_	GPIO	2.5		
CN4[L] CN4[L]	+	CN4[L]	29	SPR_SPI4_CS_X	D10	PIN_SPI4_CS_X	71	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_CS_X	-	GPIO	2.5		
CN4[L] CN4[L]		CN4[L]	31	SPR_PWM2	D09	PIN_PWM2	48	Digital	1/0	1.8		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	2.5		
CN4[L] CN4[L]		CN4[L]	33	SPR_I2SO_LRCK	D25	PIN_I2S0_LRCK	94	Digital	I/O	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_LRCK	-	GPIO	2.5		
CN4[L] CN4[L]	+	CN4[L]	35	GND	— T D10	- DIN 1200 DATA OUT	-	Power	-	1 0	_	-	_	— Ц: 7	_		- CVDE603CC	-		- CDIO	- 1300 DATA OUT	_	- CDIO	٥٦		
CN4[L] CN4[L] CN4[L]		CN4[L]		SPR_I2S0_DATA_OUT SPR_PWM0	D18 D06	PIN_I2S0_DATA_OUT PIN_PWM0	96 46	Digital Digital	1/0	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	I2S0 PWMA	P1v P1k	GPIO GPIO	I2SO_DATA_OUT PWM0	_	GPIO –	2.5		
CN4[L] CN4[L]	+	CN4[L]	41	SPR_PWM1	D06	PIN_PWM1	46	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG CXD5602GG	PWMA	P1k P1k	GPIO GPIO	PWM1	GPIO		2.5		
CN4[L] CN4[L]		CN4[L]	43	SPR_I2S0_DATA_IN		PIN_I2S0_DATA_IN	95	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	1280	P1v	GPIO	I2S0_DATA_IN	-	GPIO	2.5		
CN4[L] CN4[L]	+	CN4[L]	45	SPR_PWM3	D03	PIN_PWM3	49	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	2.5		
CN4[L] CN4[L]		CN4[L]	47		D26	PIN_I2S0_BCK	93	Digital	1/0	1.8		Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_BCK	_	GPIO	2.5		

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CN4[L] CN4[L]	CN4[L]	49	SPR_UART2_TX	D01	PIN_UART2_TX	67	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	UART2	P1n	GPIO GPIO	UART2_TX	_	GPIO OPIO	2.5	
CN4[L] CN4[L]	CN4[L]	51	SPR_UART2_RX	D00	PIN_UART2_RX	68	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO GPIO	UART2_RX	_	GPIO OPIO	2.5	
CN4[L] CN4[L]	CN4[L]	53	SPR_UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RTS	_	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	55	SPR_UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO GPIO	UART2_CTS	-	GPIO OPIO	2.5	
CN4[L] CN4[L]	CN4[L]	57	SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO OPIO	EMMC_CLK	SPI5_SCK	GPIO OPIO	2.5	<u> </u>
CN4[L] CN4[L]	CN4[L]	59	SPR_EMMC_CMD	D24	PIN_EMMC_CMD	76	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO OPIO	EMMC_CMD	SPI5_CS_X	GPIO OPIO	2.5	<u> </u>
CN4[L] CN4[L]	CN4[L]	61	SPR_EMMC_DATA0	D16	PIN_EMMC_DATA0	7.0	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO GPIO	EMMC_DATA0	SPI5_MOSI	GPIO OPIO	2.5	
CN4[L] CN4[L]	CN4[L]	63	SPR_EMMC_DATA1	D17	PIN_EMMC_DATA1	78	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	EMMC A	P1p	GPI0	EMMC_DATA1	SPI5_MISO	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	65	SPR_EMMC_DATA2	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q	GPI0	EMMC_DATA2	_	GPIO GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	67	SPR_EMMC_DATA3	D21	PIN_EMMC_DATA3	80	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q	GPIO	EMMC_DATA3	_	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	69	GND	_	_	_	Power		_	_	_	_	_	_	_	_		_	_	_	-	_		
CN4[L] CN4[L]	CN4[L]	71	SPR_SPI2_SCK	D42	PIN_SPI2_SCK	28	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_SCK	UART0_RXD	I2C3_BDT	2.5	
CN4[L] CN4[L]	CN4[L]	73	SPR_SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO_RTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	75	SPR_SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	77	SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_CS_X	UART0_TXD	I2C3_BCK	2.5	
CN4[L] CN4[L]	CN4[L]	79	1.8V	_	_	_	Power	0	1.8	0	_	0	_	0	_	-		_	_	_	_			
CN4[L] CN4[L]	CN4[L]	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	83	SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	85	SPR_SDIO_CMD	_	PIN_SDIO_CMD	82	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOA	P1r	GPIO	SDIO_CMD	SPI5_CS_X	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	87	SPR_SDIO_DATA0	_	PIN_SDIO_DATA0	83	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA0	SPI5_MOSI	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	89	SPR_SDIO_DATA1		PIN_SDIO_DATA1	84	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA1	SPI5_MISO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	91	SPR_SDIO_DATA2		PIN_SDIO_DATA2	85	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA2	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	93	SPR_SDIO_DATA3	_	PIN_SDIO_DATA3	86	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA3	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	95	GND	_	-	_	Power	_	_	_	_	_	-	-	_	-	_	_	_	_	-	_		
CN4[L] CN4[L]	CN4[L]	97	SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5	
CN4[L] CN4[L]	CN4[L]	99	GND	_		_	Power	_	_	_	_	_	-	-	_	_	_	_	_	_	_	_		
CN4[R] CN4[R]		2	5V	_	_	_	Power	1/0	5	0	_	0	-	0	_	_	_	_	_	_	_	_	6	
CN4[R] CN4[R]			5V	_	_	_	Power	1/0	5	0	_	0	_	0	_	-	_	_	_	_	_	_	6	
CN4[R] CN4[R]			ACP_SPAP	_	_	_	Analog	0	3.3	-	Hi-Z	_	Hi-Z		Hi-Z	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		ACP_SPAN	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]			ACP_SPBN	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]			ACP_SPBP	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z		Hi-Z	CXD5247GF	_	_	_	_	_	_		
CN4[R] CN4[R]			AGND_DRV	_	_	_	Power	_	-	_	_	_	——————————————————————————————————————	_	_	-		_	_	_	_	_		
CN4[R] CN4[R]	CN4[R]		SPR_SWDIO	_	_	_	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG		_	_	_	_	_	2.5	
CN4[R] CN4[R]			SPR_SWDCLK		_	_	Digital	1, 0	1.8	1	Hi-Z	ı	Hi-Z		Hi-Z	CXD5602GG		_	_	_	_	_	2.5	+
CN4[R] CN4[R]		_	ACP_CLK_DMIC		_	_	Digital		1.8	<u>'</u>	Hi-Z	· ·	Hi-Z	0	Hi-Z	CXD5002GG CXD5247GF		_	_	_	_	_	۷.5	+
CN4[R] CN4[R]			XRS_PWON		_	_	Power	1/0	3.3	1/0	Low	1/0	Low	1/0	Low	CXD5247GF CXD5247GF		_	_		_	_	7	
	CIV4[R]		ANO_F WON	_		_	rower	1/ U	٥.٥	1/ U	LUW	1/ U	LUVV	1/ 0	LU VV	0/10/2/4/QL			_				1	1
ON14557																						CPII WDT		
CN4[R] CN4[R]	CN4[R]	24	SPR_GNSS_1PPS_OUT	D44	PIN_GNSS_1PPS_OUT	6	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	GNSS_1PPS_OUT	P14	GPIO	GNSS_1PPS_OUT	CPU_WDT	CPU_WDT	2.5	
						6		<u>'</u>		_		_										(Open Drain)		
CN4[R] CN4[R] CN4[R] CN4[R]			SPR_GNSS_1PPS_OUT SPR_SEN_IRQ_IN	D44	PIN_GNSS_1PPS_OUT PIN_SEN_IRQ_IN	6 37	Digital Digital	I/O I/O	1.8	_	Hi-Z Hi-Z	_	Hi-Z Hi-Z		Hi-Z Hi-Z	CXD5602GG CXD5602GG	GNSS_1PPS_OUT SEN_IRQ_IN	P14 P1e	GPIO SEN_IRQ_IN	GNSS_1PPS_OUT SEN_IRQ_IN	SEN_IRQ_IN		2.5 2.5	
		26				6 37 31		<u>'</u>		_ 		-		_							SEN_IRQ_IN HIF_IRQ_OUT	(Open Drain)		
CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R]	26	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT	D22 D02	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT	6 37 31	Digital Digital	1/0	1.8		Hi-Z Hi-Z	-	Hi-Z Hi-Z	-	Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ	P1e	SEN_IRQ_IN GPIO	SEN_IRQ_IN HIF_IRQ_OUT	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R]	26 28 30	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND	D22 D02	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT -	6 37 31 -	Digital Digital Power	1/0	1.8 1.8	-	Hi-Z Hi-Z	-	Hi-Z Hi-Z	- -	Hi-Z Hi-Z	CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ -	P1e	SEN_IRQ_IN GPIO -	SEN_IRQ_IN HIF_IRQ_OUT -	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT -	2.5	
CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R]	26 28 30	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT	D22 D02	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT	6 37 31 - -	Digital Digital	1/0	1.8		Hi-Z Hi-Z	- - - 0	Hi-Z Hi-Z	- -	Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ	P1e	SEN_IRQ_IN GPIO	SEN_IRQ_IN HIF_IRQ_OUT	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain)	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R]	26 28 30 32	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND	D22 D02	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT -	6 37 31 - - 5	Digital Digital Power	1/0	1.8 1.8		Hi-Z Hi-Z	- - - 0	Hi-Z Hi-Z	- - - 0	Hi-Z Hi-Z	CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ -	P1e	SEN_IRQ_IN GPIO -	SEN_IRQ_IN HIF_IRQ_OUT -	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - - PMU_WDT	2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	26 28 30 32 34	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK	D22 D02 D40	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK	6 37 31 - - 5	Digital Digital Power Digital Digital	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8		Hi-Z Hi-Z – Low	- - - 0	Hi-Z Hi-Z High Hi-Z	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG - CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - PMU_WDT (Open Drain)	2.5 2.5 2.5	
CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	26 28 30 32 34 36	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2	D22 D02	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0	6 37 31 - - 5 -	Digital Digital Power Digital Digital Analog	1/0 1/0 - 0	1.8 1.8 - 1.8		Hi-Z Hi-Z – Low	- - - 0	Hi-Z Hi-Z – High	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ	P1e P02 —	SEN_IRQ_IN GPIO	SEN_IRQ_IN HIF_IRQ_OUT	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) -	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - - PMU_WDT	2.5 2.5 2.5 1.05	
CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	26 28 30 32 34 36 38	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3	D22 D02 D40 A0 A1	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1	6 37 31 - - 5 - -	Digital Power Digital Digital Analog Analog	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8 0.7 0.7		Hi-Z Hi-Z – Low	- - - 0 - 	Hi-Z Hi-Z High Hi-Z	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05	
CN4[R] CN4[R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	26 28 30 32 34 36 38 40	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4	D22 D02 D40 A0 A1 A2	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2	6 37 31 - - 5 - - -	Digital Digital Power Digital Digital Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8 0.7 0.7 0.7		Hi-Z Hi-Z – Low	- - - 0	Hi-Z Hi-Z High Hi-Z	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5	D22 D02 D40 A0 A1 A2 A3	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3	6 37 31 - - 5 - - -	Digital Digital Power Digital Digital Analog Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8 0.7 0.7 0.7 0.7		Hi-Z Hi-Z – Low	- - - 0 - - -	Hi-Z Hi-Z High Hi-Z -	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT - PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0	D22 D02 D40 A0 A1 A2 A3 A4	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0	6 37 31 - - 5 - - - -	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4		Hi-Z Hi-Z – Low	- - - 0 - - - -	Hi-Z Hi-Z High Hi-Z -	- - - 0	Hi-Z Hi-Z – High	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1	D22 D02 D40 A0 A1 A2 A3	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT - PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3	6 37 31 5	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8 0.7 0.7 0.7 0.7		Hi-Z Hi-Z – Low	- - - 0 - - - - -	Hi-Z Hi-Z High Hi-Z	- - - 0	Hi-Z Hi-Z – High	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ - AP_CLK - - - - - - - - - - - - -	P1e P02 —	SEN_IRQ_IN GPIO - GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 1.05	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND	D22 D02 D40 A0 A1 A2 A3 A4	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 -	6 37 31 5	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4		Hi-Z Hi-Z – Low	- - - 0 - - - - - -	Hi-Z Hi-Z High Hi-Z	- - - 0	Hi-Z Hi-Z – High	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 —	SEN_IRQ_IN GPIO - GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1	D22 D02 D40 A0 A1 A2 A3 A4 A5	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1	6 37 31 5 39	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Analog	1/0 1/0 - 0	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4		Hi-Z Hi-Z – Low	- - - 0 - - - - - -	Hi-Z Hi-Z High Hi-Z - - - - - - - - - - - -	O - O - I I I I I I I I I I I I I I	Hi-Z Hi-Z – High	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 P13	SEN_IRQ_IN GPIO - GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND	D22 D02 D40 A0 A1 A2 A3 A4 A5	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 -	6 37 31 5 39 42	Digital Digital Power Digital Digital Analog	1/0 1/0 - 0	1.8 1.8 - 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 -		Hi-Z Hi-Z Low Hi-Z - - - - - - - - - - - -	- - - 0 - - - - - -	Hi-Z Hi-Z High Hi-Z	O - O - I I I I I I I I I I I I I I	Hi-Z High Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK	P1e P02 P13	SEN_IRQ_IN GPIO - GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X	6 37 31 5 39 42 41	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Digital	I/O	1.8 1.8 - 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8		Hi-Z Hi-Z Low Hi-Z - - - - - - - Hi-Z	- - - 0	Hi-Z Hi-Z High Hi-Z Hin-Z Hi-Z	- O O O O O O O O O O O O O O O O O O O	Hi-Z High Hi-Z Hi-Z Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X	P1e P02 P13	SEN_IRQ_IN GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI	6 37 31 5 39 42 41 43	Digital Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8		Hi-Z Hi-Z Low Hi-Z - - - - - - Hi-Z Hi-Z Hi-Z	- - - 0	Hi-Z Hi-Z High Hi-Z - High - - - - - Hi-Z Hi-Z Hi-Z	O O O O O O O O O O O O O O O O O O	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3	P1e P02 P13 P1g P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8		Hi-Z Hi-Z Low Hi-Z - - - - - - Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3	P1e P02 P13 P1g P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_SCK SPR_SPI3_CS0_X	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Digital	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X	P1e P02 P13 P1g P1i P1i P1i P1f	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_SCK SPR_SPI3_CS0_X	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Digital	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X	P1e P02 P13 P1g P1i P1i P1i P1f	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Digital	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- O O O O O O O O O O O O O O O O O O O	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1f	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Analog	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3		Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0 - - - - - - -	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z	-	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT -	P1e P02 P13 P1g P1i P1i P1i P1f	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT -	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X	D22 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Digital Digital Power Digital Digital Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Analog	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	- - - 0	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	-	Hi-Z Hi-Z High Hi-Z - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - - - - - - - - - - - -	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Digital Digital Digital Digital Digital Digital Digital Digital Power Digital Poigital Poigital Analog Analog	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 - 1.8 - - - - - - - - - - - - -		Hi-Z Hi-Z - Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	-	Hi-Z Hi-Z High Hi-Z - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - - - - - - - - - - - -	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain)	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GP05	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 D39 D39 D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0 -	41	Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 1.8		Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi	-	Hi-Z High Hi-Z - High Hi-Z - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - - Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO -	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 - D39 -	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3		Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	-	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPIO0	P1e P02 P13 P1g P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT - AP_CLK - AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain) GPIO - GPS_EXTLD	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 - D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Digital Power Digital Digital Digital Digital Analog Digital	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3		Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi	-	Hi-Z High Hi-Z - High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT PMU_WDT (Open Drain) GPIO - GPS_EXTLD	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 - D39 D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3	O O O I/O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi	1/ 0	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	-	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 CS0_X RTC_IRQ_OUT - HIF_GPIO0	P1e P02 P13 P1g P1i P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPI	SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X ACP_GPO5 ACP_GPO5 ACP_GPO7 ACP_VSYS ACP_VSYS	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT PIN_AP_CLK LPADC0 LPADC1 LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPIO0	41	Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Power Power	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3	O O O I/O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z High Hi-Z — — — — — — — — — Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Low —	-	Hi-Z Hi-Z High Hi-Z - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low -	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X RTC_IRQ_OUT HIF_GPIO0	P1e P02 P13 P1g P1i P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT RTC_IRQ_OUT (Open Drain) - GPIO	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN5 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MSO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS GND	D22 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 D39 D30 D32 D41 - D39 - D39 D39 D39 D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT	41	Digital Power Digital Digital Digital Analog Analog Analog Analog Analog Analog Analog Digital Power Digital Power Power Power	I/O	1.8 1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3	O O I/O	Hi-Z Hi-Z Low Hi-Z Hi-Z Hi-Z Hi-Z Hi	1/ 0	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Low	-	Hi-Z Hi-Z High Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low	CXD5602GG CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPIOO	P1e P02 P13 P1g P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT RTC_IRQ_OUT (Open Drain) - GPIO	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート
CN4[R] CN4[R]	CN4[R]	26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80 82	SPR_SEN_IRQ_IN SPR_HIF_IRQ_OUT GND SPR_RST_X SPR_AP_CLK SPR_SEN_AIN2 SPR_SEN_AIN3 SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS GND SPR_SDIO_CD	D22 D02 D02 D02 D40 A0 A1 A2 A3 A4 A5 D07 D31 D29 D30 D32 D41 - D39 - D39	PIN_SEN_IRQ_IN PIN_HIF_IRQ_OUT	41 43 38 4 ——————————————————————————————————	Digital Power Digital Digital Digital Digital Analog Analog Analog Analog Analog Analog Digital Power Digital Power Digital Power Power	I/O	1.8 1.8 1.8 1.8 1.8 0.7 0.7 0.7 0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4 3.6-4.4 3.6-4.4 1.8	O O I/O I/O I/O	Hi-Z Hi-Z Low Hi-Z - - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z - Hi-Z	I/O - -	Hi-Z Hi-Z High Hi-Z — — — — — — — — Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Low Low Low Low — —	-	Hi-Z Hi-Z High Hi-Z - - - - - Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z C Hi-Z Hi-Z	CXD5602GG	SEN_IRQ_IN HIFIRQ AP_CLK SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3	P1e P02 P13 P1g P1i P1i P1i P1i P1i P1i	SEN_IRQ_IN GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO	SEN_IRQ_IN HIF_IRQ_OUT AP_CLK SPI3_CS1_X SPI3_MOSI SPI3_SCK SPI3_MISO SPI3_CS0_X RTC_IRQ_OUT - GPIO	SEN_IRQ_IN HIF_IRQ_OUT (Open Drain) PMU_WDT RTC_IRQ_OUT (Open Drain) - GPIO	(Open Drain) SEN_IRQ_IN GNSS_1PPS_OUT	2.5 2.5 2.5 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	ヘッドフォンミュート Lowでミュート

CN4[R]	CN4[R]	CNIV[D] 0	6	CDD 13C3 CC1	_	PIN_SPI0_MOSI	10	Digital	I/O 1.8	_	Liαh	1/0		ı	∐iah	CXD5602GG	SPI0B	P17	GPIO	ISCS BCK	SDIO MOSI		2.5		Γ
		CN4[R] 8		SPR_I2C2_SCL			19	Digital	· · · · · · · · · · · · · · · · · · ·		High	1/0		0				PII		I2C2_BCK	SPI0_MOSI		2.5		
CN4[R]	CN4[R]	CN4[R] 8	_	ACP_GPO0		_	_	Digital	0 3.6-4.4		Hi-Z	0	Hi-Z	0	Hi-Z	CXD5247GF	_	_	_	_	_	_			Lie Lande I
CN4[R]	CN4[R]	CN4[R] 9	0	ACP_GPO1	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_		_	_	_		オーディオ3.3V出力	Highで出力
CN4[R]	CN4[R]	CN4[R] 9	2	ACP_GPO2	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_		LTE用電源出力	 Highで出力
0111[11]	514 1[14]	0111[11]		7101 _011 02				Digital				J	2011	J		0/(0/02/1/0/1								(LTE拡張ボードのみ)	тывт с щ / 3
																								メインボード側からの	
CN4[R]	CN4[R]	CN4[R] 9	4	ACP_GPO3	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	Ο	Low	CXD5247GF	_	_	_	_	_	_		LTE用電源出力	Highで出力
																								(LTE拡張ボードのみ)	
CN4[R]	CN4[R]	CN4[R] 9	6	SWOCLK	_	_	_	Digital	0 1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5	, 2000	
CN4[R]	CN4[R]	CN4[R] 9		SWO		_	_	Digital	0 1.8	_	Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
		<u> </u>							0 1.0			+			111-2	CAD3002GG							2.3		
CN4[R]	CN4[R]	CN4[R] 10	00	GND		_	_	Power	_ _	_	_	_	_	_		_	_	_	_	_	_	-			
	JP3		L	NC	_	_	_	_		_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP3	2	2	VDD_LVS	_	-	_	Power	O 5/3.3	0	_	0	_	Ο	_	_	_	_	_	_	_	_			
	JP3	3	3	XRS_PWON	_	_	_	Digital	1/0 3.3	1/0	_	1/0	_	1/0	_	_	_	_	_	_	_	_	7		
	JP3	4	1	3.3V	_	_	_	Power	O 3.3	0	_	Ο	_	Ο	_	_	_	_	_	_	_	-			
	JP3	Ę	5	MAIN_POWER	_	_	_	Power	I/O 5	1/0	_	1/0	_	1/0	_	_	_	_	_	_	_	_	6		
	JP3		ŝ	GND	_	_	_	Power	_ _	_	_	_	_	_	_	-	_	_	_	_	_	-			
	JP3	 	7	GND	_	_	_	Power	_ _	_	_	_	_	_	_	_	_	_	_	_	_	_			
	JP3	8	3	5V_IN_PIN		_	_	_	_	_	_	_	_	_		_	_	_	_	_	_	_			
	JP4		1			LPADC0		Anala-	· ~5	1	_	1	_			CADEROSCO							7 5		
			_	SPR_SEN_AIN2	A0		_	Analog								CXD5602GG	_	_	SEN_AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2	7.5		
	JP4	2		SPR_SEN_AIN3	A1	LPADC1	_	Analog	l ∼5		_	1	_		_	CXD5602GG	_	_	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4	3	3	SPR_SEN_AIN4	A2	LPADC2	_	Analog	I ∼5		_		_			CXD5602GG	_	_	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4	4	1	SPR_SEN_AIN5	A3	LPADC3	_	Analog	I ∼5	I	-		_		_	CXD5602GG	_	_	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4	Ę	5	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I ∼5		_				_	CXD5602GG	_	_	SEN_AIN0	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4	6	ŝ	SPR_SEN_AIN1	A5	HPADC1		Analog	I ∼5		_	ı	_			CXD5602GG	_		SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2	1	1	I2C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0 5/3.3	_	High		High		High	CXD5602GG	12C0	P1j	GPIO	I2C0_BCK	_	_	7		
	JP2	2	2	I2C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0 5/3.3	_	High	1	High	ı	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	_	7		
	JP2	3	3	AREF	_	_	_	_	O 5/3.3	0	High	0	High	0	High	_	_	_	_	_	_	_			
	JP2		1	GND	_	_	_	_		_	_	_	_	_		_	_	_	_	_	_	_			
	JP2	-	5	SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	_	GPIO	7		
	JP2		2			PIN_SPI4_MISO	7/		1/0 5/3.3		Hi-Z	+	Hi-Z		Hi-Z	CXD5602GG	SPI4		GPIO	SPI4_MISO	_	GPIO GPIO	7		
		-	7	SPI4_MISO	D12		74	Digital						_				P10					7		
	JP2		/	SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MOSI	_	GPI0			
	JP2	8	3	SPI4_CS_X	D10	PIN_SPI4_CS_X	/1	Digital	1/0 5/3.3		Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_CS_X	_	GPIO			
	JP2	9	9	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3	+	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	_	7		
	JP2	1	0	SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO_RTS	GPIO	7		
	JP13	1	l	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		
	JP13		2	PWM0	D06	PIN_PWM0	46	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	7		
	JP13	3	3	PWM1	D05	PIN_PWM1	47	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	-	7		
	JP13		1	SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	7		
	JP13	5	5	PWM3	D03	PIN_PWM3	49	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	7		
						_															HIF IRO OUT				
	JP13	(ŝ	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	7		
	JP13	 	7	UART_TX	D01	DINI LIADTO TV	67	Digital	1/0 5/3.3	_	Hi-Z		⊔: 7	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_TX		GPIO	7		
)		D01	PIN_UART2_TX	01	Digital		+	-	_	Hi-Z										7		
	JP13	0010)	UART_RX	D00	PIN_UART2_RX	00	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RX	_	GPIO	1		
		CN9 1	L	MAIN_POWER				Power	O 4~5	U	High	U	High	U	High	_	_	_	_	_	-	_			
		CN9 2	_	GND				Power	_ _	_	High	-	High		High	-	_	_	_	_	_	_			
		CN9 3	3	SPR_SEN_AIN1	A5	HPADC1	_	Analog	I ~5		_		_		Hi-Z	CXD5602GG	_	_	_	_	_	_	8.9		
		CN9 4	1	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I ~5	I	_		_		Hi-Z	CXD5602GG	_	_	_	_	_	_	8.9		
		CN9 5	5	3.3V				Power	O 3.3	0	_	0	_	0		_	_		_	_	_	_			
		CN9 6	5	GND				Power		_	_	-	_	-	_	_	_	_	_	_	_	-			
		ONIO	,	IIIE 180 011T	D00	DIM THE 150 COST	01	5	1/0 5/05				=			OVDECCCC	IIIE 250 017	D00	0010	IIIE 150 01 T	HIF_IRQ_OUT	ONICO 1550 000	7		
		CN9	/	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	1		
		CN9 8	3	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	_	_	7		
		CN9 S		PWM0	D06	PIN_PWM0	46	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	7		
		CN9 1		SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital		_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MISO	_	_	7		
		CN9 1	1	PWM1	D30	PIN_PWM1	47	+			Hi-Z		Hi-Z		Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	7		
		+	2				-	Digital		+	+												7		
		CN9 1	_	SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	SPI3	P1i	GPIO GPIO	SPI3_MOSI	- 1201 DOK	_	<u> </u>		
		CN9 1	-	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO GPIO	PWM2	I2C1_BCK	_			
		CN9 1		SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	-	_	7		
		CN9 1		PWM3	D03	PIN_PWM3	49	Digital	1/0 5/3.3	+	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	7		
		CN9 1	6	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		

日付	内容							
2020.11.13	第一稿							
2021.4.14	ACP_GPO1~7のArduinoおよびSDK起動後の初期値を訂正。							
2021.4.14	XRST(SPR_RST_X)の各初期値を訂正。							
2021.12.20	CXD5247GFに接続されるピンの電圧範囲を3.6-4.4Vに訂正。							
2021.12.20	MCLKの電源投入後およびAruduino起動後の初期値を訂正。							