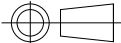


<div>3</div> X						GENERAL TOLERANCE		DATE OF ISSUE
<div>2</div> X								DOCUMENT TYPE
<div>1</div> X								CIRCUIT DIAGRAM
								TOLERANCING
HISTORY X COUNT	SUFFIX /REPL.	RELEASE NO.		REVISION	SIGN.	MATERIAL(COLOR)	FINISH(COLOR)	2018.7.10
		DATE						
LEGAL OWNER								
RESPONSIBLE DEPARTMENT				SIZE A3	UNIT mm	ORIGINAL MODEL SPRESENSE	DESCRIPTION (E) Main Board (CXD5602PWBMAIN1)	
PLANNED BY		CHECKED BY	APPROVED BY	SCALE :	TENTATIVE MODEL	DESCRIPTION (J)		
DRAWN BY				PRJ.METHOD 	TENTATIVE PART NO.	PART NO.		SHEET/PAGES 1 / 8

A

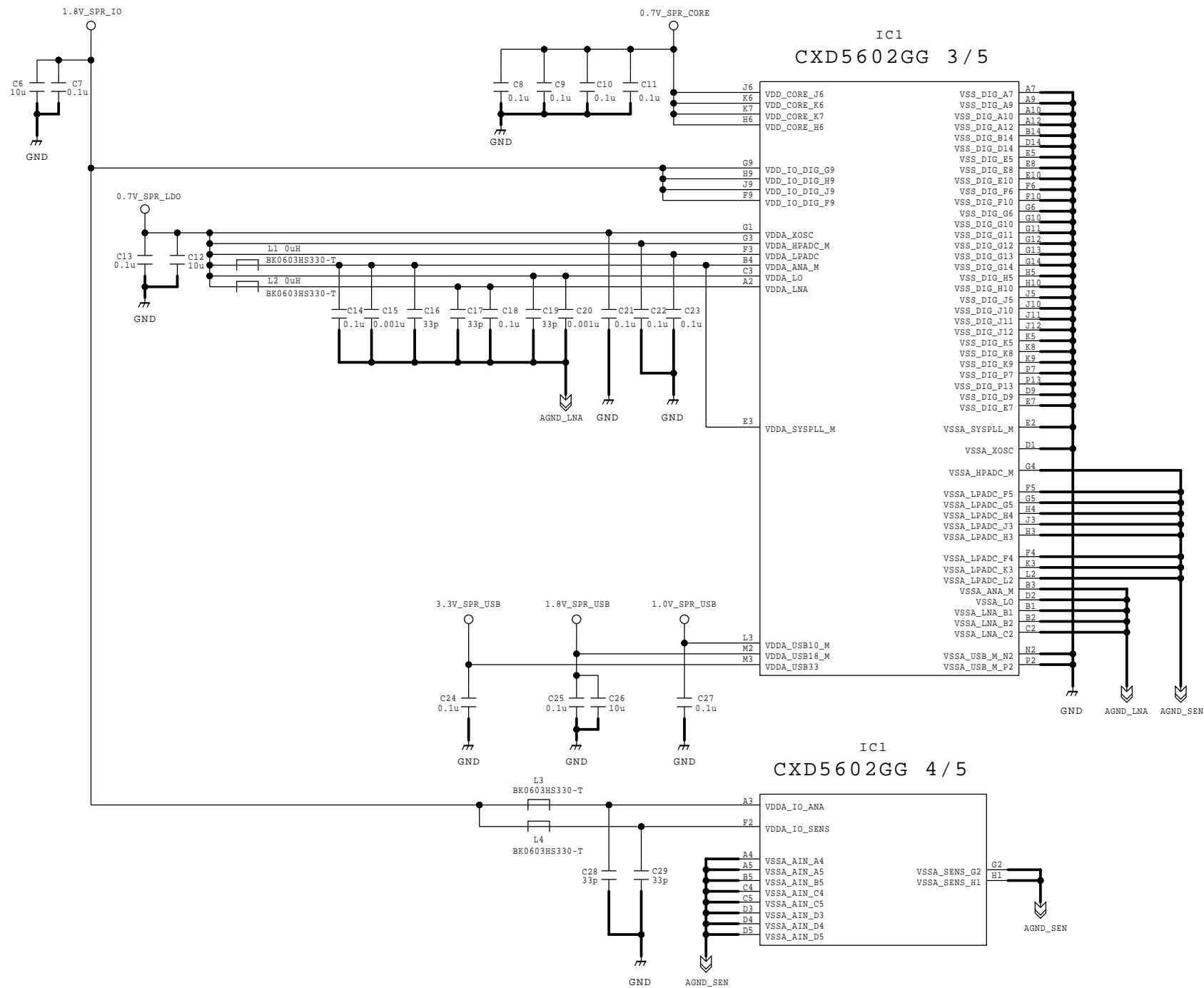
B

C

D

E

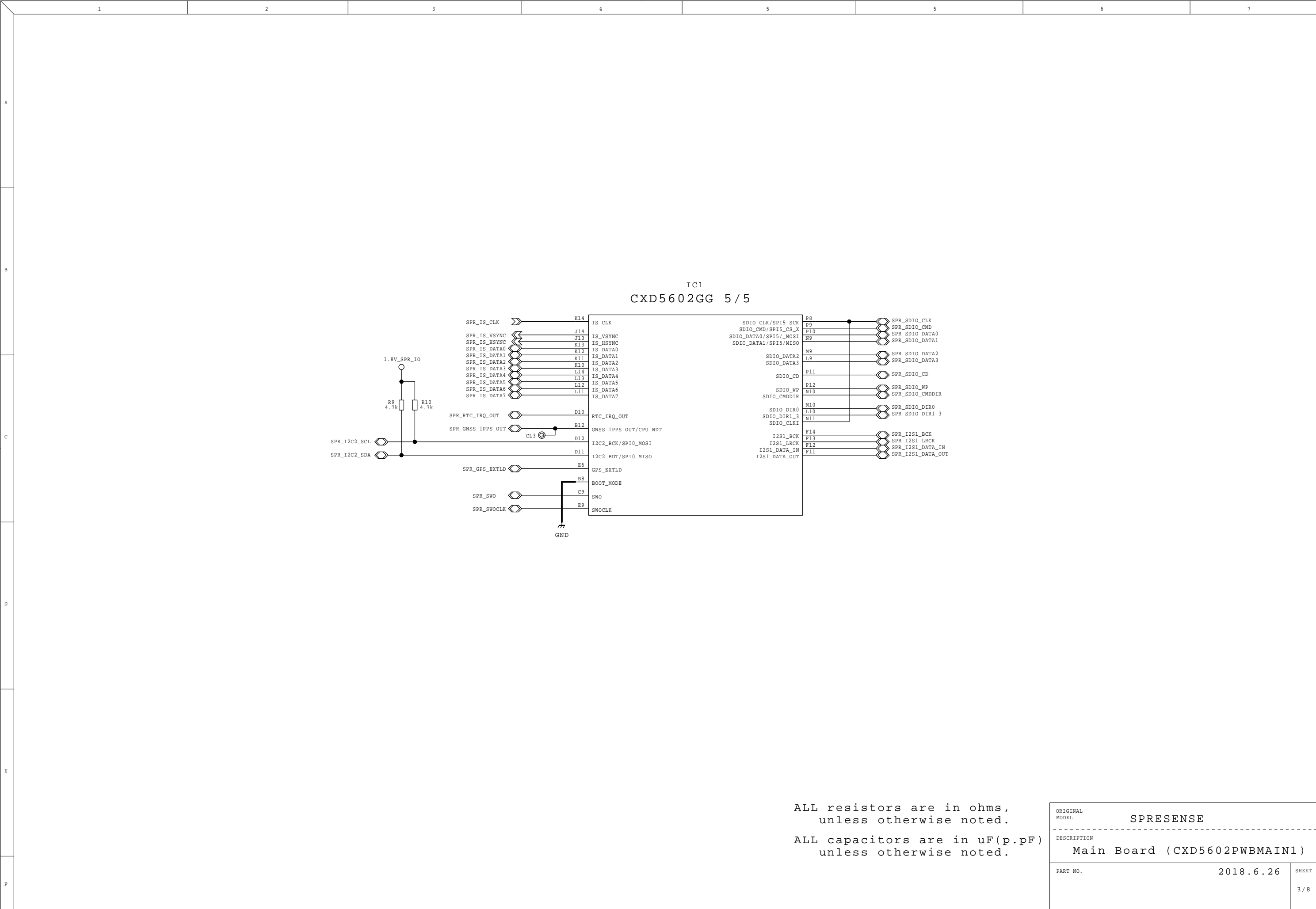
F



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

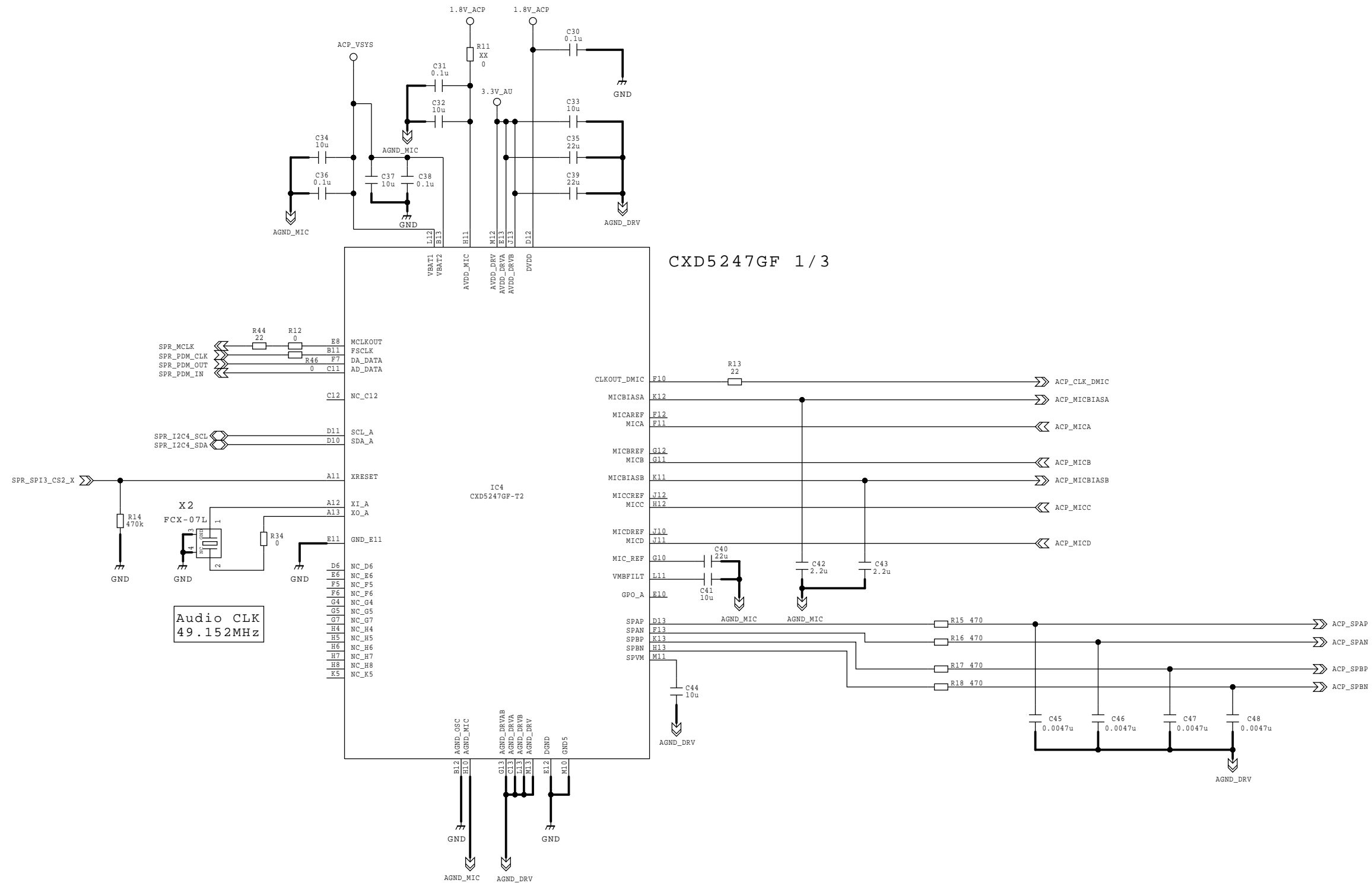
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET 2 / 8



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

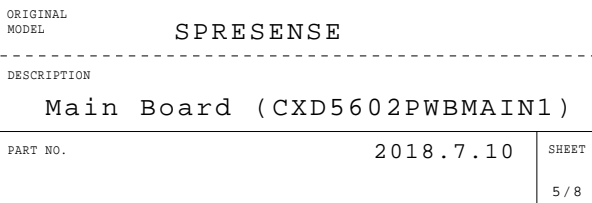
ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.6.26	SHEET 3 / 8

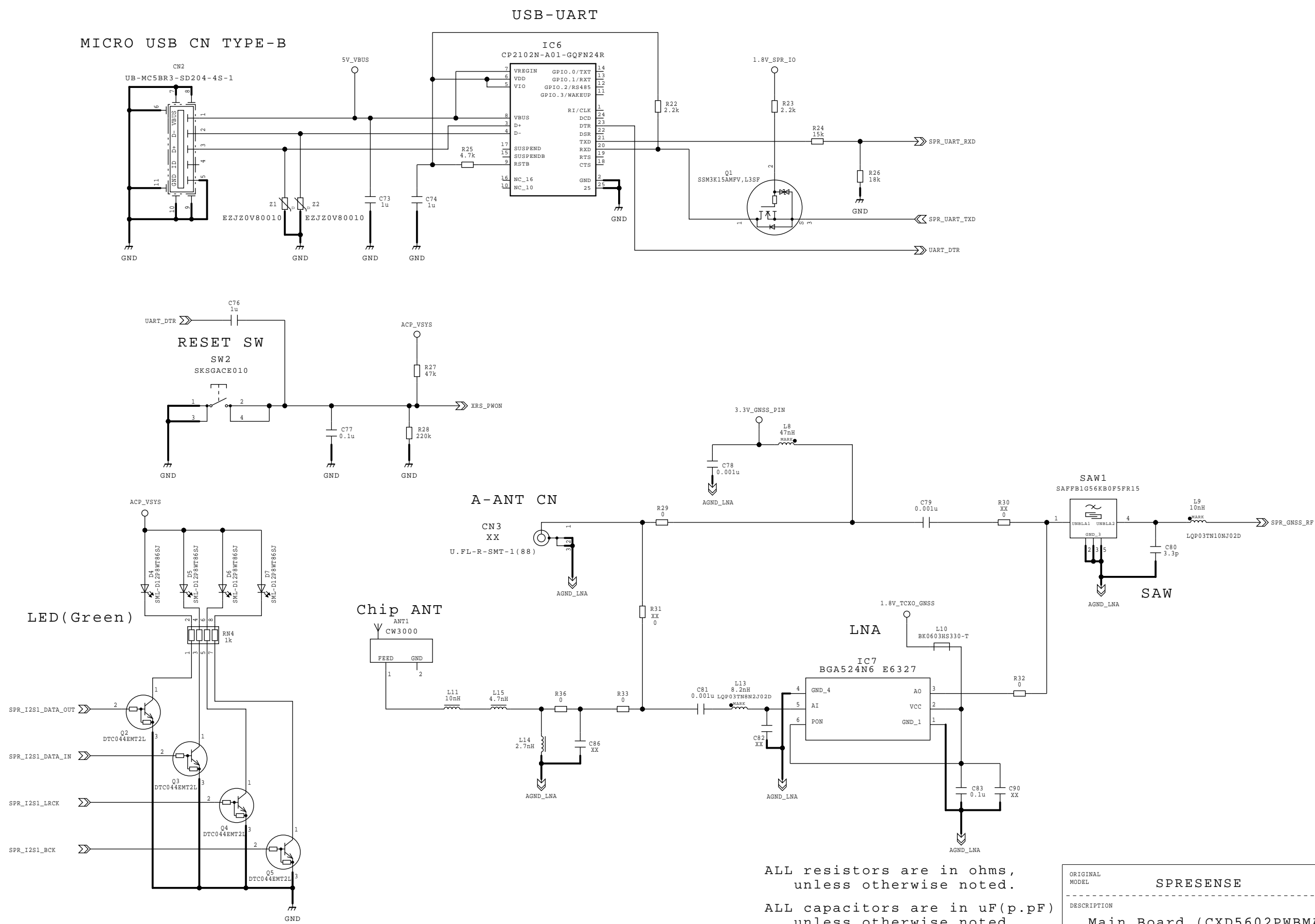


ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET 4 / 8

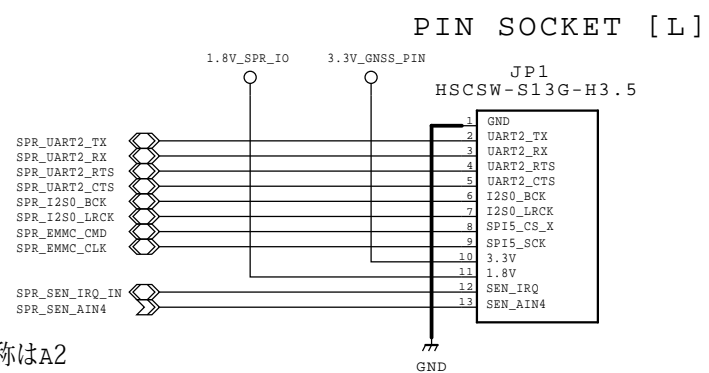




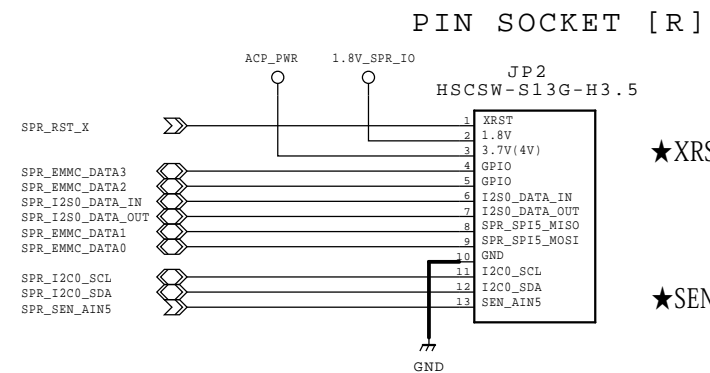
ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	Main Board (CXD5602PWBMAIN1)	
PART NO.	2018.7.10	SHEET 6 / 8

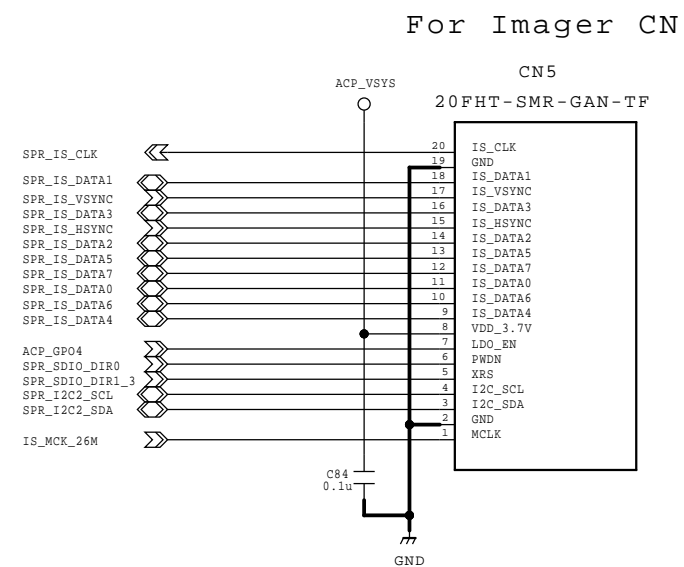
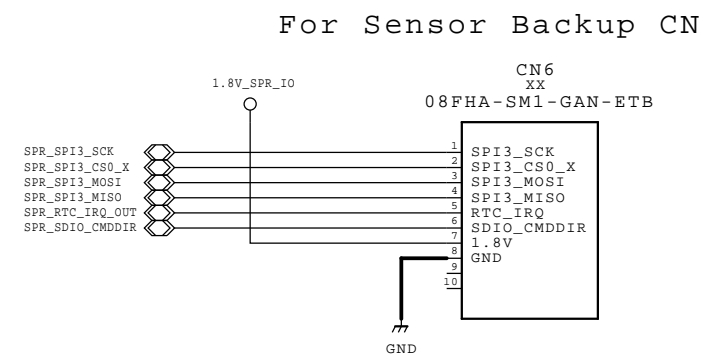


★SEN_AIN4のArduino IDE上の呼称はA2



★XRST(SPR_RST_X)はOutput(電圧レベル1.8v)

★SEN_AIN5のArduino IDE上の呼称はA3



ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p.pF)
unless otherwise noted.

ORIGINAL MODEL			SPRESENSE		

DESCRIPTION					
Main Board (CXD5602PWBMAIN1)					
PART NO.		2018.7.9		SHEET	
				8 / 8	