	Ref	Pin	Name on schematic	Name on	Name on SDK	Pin No. on	Typo	dir Voltage After turn	ing on the power Afto	ter starting A	duino Aft	er starting SDK	Connection	Pin grou	ıp		CXD5602G	G pin function		Maximum absolute	Features on the	explanation
Main	Ext Cam	LTE No.		ArduinoIDE		SDK	Type	dir	Init val dir	init	al dir	init val	Connection	Mode name	doc.	mode0	mode1	mode2	mode3	rating Voltage(V)	extension board	ехріапаціоп
JP1 JP1		1 2	GND UART2_TX	 D01	PIN_UART2_TXD	- 67	Power Digital		Hi-Z		 i-Z -	- <u> </u>	CXD5602GG	– UART2	- P1n	- GPIO	UART2_TXD	_	- GPIO	2.5		
JP1		3	UART2_RX	D01	PIN_UART2_RXD	68	Digital	1/0 1.8 -	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RXD	_	GPIO	2.5		
JP1		4	UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0 1.8 -	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RTS	_	GPIO	2.5		
JP1		5	UART2_CTS	D27	PIN_UART2_CTS	69	Digital	I/O 1.8 –	Hi-Z	– H	i-Z –	- Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_CTS	-	GPIO	2.5		
JP1		6	I2S0_BCK	D26	PIN_I2S0_BCK	93	Digital	1/0 1.8 -	Hi-Z		i-Z –	Hi-Z	CXD5602GG	12S0	P1v	GPIO GPIO	I2S0_BCK	_	GPIO GPIO	2.5		
JP1 JP1		8	I2S0_LRCK SPI5_CS_X	D25 D24	PIN_I2S0_LRCK PIN_SPI5_CS_X	94 76	Digital Digital	I/O	Hi-Z		i-Z –	- Hi-Z - Hi-Z	CXD5602GG CXD5602GG	I2S0 EMMCA	P1v P1p	GPIO GPIO	I2S0_LRCK EMMC_CMD	SPI5_CS_X	GPIO GPIO	2.5		
JP1		9	SPI5_SCK	D23	PIN_SPI5_SCK	75	Digital	I/O 1.8 –	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5		
JP1		10	3.3V	_	-	_	Power	O 3.3 O	- (0	- O) –	_	_	-	_	-	_	_			
JP1		11	1.8V	_	-	_	Power	0 1.8 0	- (<u> </u>	– O		_	_	_	_	_	_	_			
JP1 JP1		12	SEN_IRQ	D22	PIN_SEN_IRQ_IN LPADC2	37	Digital	I/O 1.8	Hi-Z	. +	i-Z –	- Hi-Z	CXD5602GG CXD5602GG	SEN_IRQ_IN	P1e	GPIO –	SEN_IRQ_IN _	_		2.5		
JP1 JP2		13	SEN_AIN4 XRST	A2 	(SPR_RST_X)	_	Analog Digital	0 1.8 -	Low	<u>'</u>	gh O	High	CXD5602GG CXD5602GG	_				_		1.05		
JP2		2	1.8V	_	-	_	Power	0 1.8 0	- (_	- O		-	_	_	_	_	_	_			
JP2		3	3.7V(4.0V)	_	-	_	Power	I/O 3.6-4.4 O	- (0	- O) –	_	_	_	_	_	_	-	7		
JP2		4	GPIO	D21	PIN_EMMC_DATA3	80	Digital	I/O 1.8 –	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	EMMCB	P1q	GPIO	EMMC_DATA3	-	GPIO	2.5		
JP2		5	GPIO	D20	PIN_EMMC_DATA2	79	Digital	1/0 1.8 -	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	EMMCB	P1q	GPIO OPIO	EMMC_DATA	_	GPIO	2.5		
JP2 JP2		7	I2SO_DATA_IN I2SO_DATA_OUT	D19 D18	PIN_I2S0_DATA_IN PIN_I2S0_DATA_OUT	95	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z		i-Z –	- Hi-Z - Hi-Z	CXD5602GG CXD5602GG	12S0 12S0	P1v P1v	GPIO GPIO	I2S0_DATA_IN I2S0_DATA_OUT	_	GPIO GPIO	2.5		
JP2		8	SPR_SPI5_MISO	D17	PIN_SPI5_MISO	78	Digital	I/O 1.8 –	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	EMMCA	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO	2.5		
JP2		9	SPR_SPI5_MOSI	D16	PIN_SPI5_MOSI	77	Digital	I/O 1.8 –	Hi-Z		i-Z –			EMMCA	P1p	GPIO	EMMC_DATA0	SPI5_MOSI	GPIO	2.5		
JP2		10	GND		-		Power			_				-	_	-	-	-	_			
JP2		11	12C0_SCL	D15	PIN_I2C0_BCK	44	Digital	I/O 1.8 -	High		gh I	High		1200	P1j	GPIO	I2C0_BCK	_		2.5		
JP2 JP2		12	I2C0_SDA SEN_AIN5	D14 A3	PIN_I2C0_BDT LPADC3	45 _	Digital Analog	I/O 1.8	High –	. +	gh I	High _	CXD5602GG CXD5602GG	12C0 _	P1j —	GPIO –	I2C0_BDT _	_		2.5		
CN5	CN1	13	MCLK			_	Digital	0 1.8 -	- (<u>'</u>	i-Z O) Hi-Z	26MHz TCXO	_	_	_	_	_	_	2.5		
CN5	CN1	2	GND	_	_	_	Power			+		- –	_	_	_	_	_	_	_			
CN5	CN1	3	I2C_SDA	_	PIN_SPI0_MISO	20	Digital	I/O 1.8 –	High -	– Н	gh I	High	CXD5602GG	12C2	P17	GPIO	I2C2_BDT	SPI0_MISO	GPIO	2.5		
CN5	CN1	4	I2C_SCL	-	PIN_SPI0_MOSI	19	Digital	1/0 1.8 -	High ·		gh I	High	CXD5602GG	12C2	P17	GPIO	I2C2_BCK	SPI0_MOSI	GPIO	2.5		
CN5 CN5	CN1 CN1	5	XRS PWDN	D35	PIN_SDIO_DIR1_3	91	Digital	I/O 1.8 - I/O 1.8 -	Hi-Z		i-Z –	- Hi-Z - Hi-Z	CXD5602GG CXD5602GG	SDIOC SDIOC	P1t P1t	GPIO GPIO	SDIO_DIR1_3 SDIO_DIR0	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1	7	LDO_EN	— D34 —	PIN_SDIO_DIR0 (ACP_GPO4)	90	Digital Digital	0 3.6-4.4 0	Hi-Z (ow O) Low	CXD5002GG CXD5247GF	- SDIOC	— PIL	— GP10	- SDIO_DIR0	- -	— GP10 —	2.5		
CN5	CN1	8	VDD_3.7V	_	-	_	Power	O 3.6-4.4 O	- (0	- O		-	_	_	_	_	_	_			
CN5	CN1	9	IS_DATA4	_	PIN_IS_DATA4	63	Digital	I/O 1.8 –	Hi-Z	– H	i-Z I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA4	GPIO	GPIO	2.5		
CN5	CN1	10	IS_DATA6	_	PIN_IS_DATA6	65	Digital	I/O 1.8 –	Hi-Z		i-Z I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA6	GPIO	GPIO	2.5		
CN5	CN1	11	IS_DATA0	_	PIN_IS_DATA0	59	Digital	1/0 1.8 -	Hi-Z		-Z I		CXD5602GG	IS	P1m	GPIO CRIO	IS_DATA7	GPIO	GPIO GPIO	2.5		
CN5 CN5	CN1 CN1	12	IS_DATA7 IS_DATA5		PIN_IS_DATA7 PIN_IS_DATA5	64	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z		i-Z I		CXD5602GG CXD5602GG	IS IS	P1m P1m	GPIO GPIO	IS_DATA7 IS_DATA5	GPIO GPIO	GPIO GPIO	2.5		
CN5	CN1	14	IS_DATA2	_	PIN_IS_DATA2	61	Digital	I/O 1.8 –	Hi-Z		i-Z I		CXD5602GG	IS	P1m	GPIO	IS_DATA2	GPIO	GPIO	2.5		
CN5	CN1	15	IS_HSYNC	_	PIN_IS_HSYNC	58	Digital	I/O 1.8 –	Hi-Z	– H	i-Z I	_	CXD5602GG	IS	P1m	GPIO	IS_HSYNC	GPIO	GPIO	2.5		
CN5	CN1	16	IS_DATA3	_	PIN_IS_DATA3	62	Digital	I/O 1.8 –	Hi-Z		i-Z I	_	CXD5602GG	IS	P1m	GPIO	IS_DATA3	GPIO	GPIO	2.5		
CN5	CN1	17	IS_VSYNC	_	PIN_IS_VSYNC	57	Digital	1/0 1.8 -	Hi-Z		-Z I		CXD5602GG	IS	P1m	GPIO OPIO	IS_VSYNC	GPIO	GPIO	2.5		
CN5 CN5	CN1 CN1	18 19	IS_DATA1 GND		PIN_IS_DATA1 _	- 60	Digital Power	I/O 1.8	Hi-Z -		i-Z I 		CXD5602GG	IS –	P1m –	GPIO –	IS_DATA1 _	GPIO –	GPIO –	2.5		
CN5	CN1	20	IS_CLK	_	PIN_IS_CLK	56	Digital	I 1.8 I	_	1 .	- 1	_	CXD5602GG	IS	P1m	GPIO	IS_CLK	GPIO	GPIO	2.5		
	CN4[L]	CN4[L] 1	3.3V_AU	_	_	_	Power	O 3.3 I	-	1	- 1	_	_	-	_	_	_	_	_			
	CN4[L]	CN4[L] 3	3.3V_AU	_	-		Power	O 3.3 I	_	<u>'</u>	- 1		-	_		_	_	_	_			
	CN4[L]	CN4[L] 5	ACP_MICA			_	Analog	-	_	<u>.</u>	- l		CXD5247GF		_	_		_				
	CN4[L]	CN4[L] 7 CN4[L] 9	ACP_MICB ACP_MICC				Analog Analog	-	_	<u>'</u>	- I - I		CXD5247GF CXD5247GF	_				_				
	CN4[L]	CN4[L] 11	ACP_MICD		_	_	Analog	1 - 1	_	1 .	- <u>'</u>		CXD5247GF	_	_	_	_	_				
CN4[L]	CN4[L]	CN4[L] 13	ACP_MICBIASA	_	-	_	Analog	0 2 0	- (0	- O) –	CXD5247GF	_	_	_	-	-	_			
-	CN4[L]	CN4[L] 15	ACP_MICBIASB	_	_	_	Analog	0 2 0	- (- O) –	CXD5247GF	_	_	_	_	_	_			
	CN4[L]	CN4[L] 17 CN4[L] 19	AGND_MIC	 D15	PIN_I2C0_BCK		Power					——————————————————————————————————————	CXD5602GG	- I2C0	D1:	- GPIO	- I2C0_BCK	_		2.5		
	CN4[L]	CN4[L] 19 CN4[L] 21	SPR_I2C0_SCL SPR_I2C0_SDA	D15	PIN_I2C0_BCK PIN_I2C0_BDT	44	Digital Digital	I/O 1.8 –	High High		gh I	High High	CXD5602GG CXD5602GG	12C0 12C0	P1j P1j	GPIO GPIO	I2C0_BCK	_		2.5		
	CN4[L]	CN4[L] 23	SPR_SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	I/O 1.8 –	Hi-Z		i-Z –	- Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	_	GPIO	2.5		
	CN4[L]	CN4[L] 25	SPR_SPI4_MISO	D12	PIN_SPI4_MISO	74	Digital	I/O 1.8 -	Hi-Z	– H	-Z –	- Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MISO	-	GPIO	2.5		
	CN4[L]	CN4[L] 27	SPR_SPI4_MOSI	D11	PIN_SPI4_MOSI	73	Digital	I/O 1.8 -	Hi-Z		-Z –	Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_MOSI	_	GPIO	2.5		
+	CN4[L]	CN4[L] 29	SPR_SPI4_CS_X	D10	PIN_SPI4_CS_X	71 40	Digital	I/O 1.8 –	Hi-Z		-Z –	- Hi-Z	CXD5602GG	SPI4	P10	GPIO GPIO	SPI4_CS_X	- 12C1 BCK	GPIO _	2.5		
	CN4[L]	CN4[L] 31 CN4[L] 33	SPR_PWM2 SPR_I2S0_LRCK	D09 D25	PIN_PWM2 PIN_I2S0_LRCK	94	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z		i-Z –	- Hi-Z - Hi-Z	CXD5602GG CXD5602GG	PWMB I2S0	P1I P1v	GPIO GPIO	PWM2 I2S0_LRCK	I2C1_BCK -	GPIO	2.5		
	CN4[L]	CN4[L] 35	GND	-		_	Power						-	-	-	-		_	— — — — — — — — — — — — — — — — — — —	2.0		
	CN4[L]	CN4[L] 37	SPR_I2S0_DATA_OUT	T D18	PIN_I2S0_DATA_OUT	96	Digital	I/O 1.8 –	Hi-Z	– Н	i-Z –	- Hi-Z	CXD5602GG	12S0	P1v	GPIO	I2S0_DATA_OUT	_	GPIO	2.5		
—	CN4[L]	CN4[L] 39	SPR_PWM0	D06	PIN_PWM0	46	Digital	I/O 1.8 -	Hi-Z		i-Z –	Hi-Z	CXD5602GG	PWMA	P1k	GPI0	PWM0	-	_	2.5		
	CN4[L]	CN4[L] 41	SPR_PWM1	D05	PIN_PWM1	47	Digital	1/0 1.8 -	Hi-Z		-Z –	- Hi-Z	CXD5602GG	PWMA	P1k	GPIO GPIO	PWM1	GPIO _	- CDIO	2.5		
CN4[L]	CN4[L]	CN4[L] 43 CN4[L] 45	SPR_I2S0_DATA_IN SPR_PWM3	D19 D03	PIN_I2S0_DATA_IN PIN_PWM3	49	Digital Digital	I/O 1.8 - I/O 1.8 -	Hi-Z		i-Z –	- Hi-Z - Hi-Z	CXD5602GG CXD5602GG	I2S0 PWMB	P1v P1l	GPIO GPIO	I2S0_DATA_IN PWM3	- I2C1_BDT	GPIO –	2.5		
			SPR_I2S0_BCK			93									P1v		I2S0_BCK	-	GPIO	2.5		
	<u> </u>			1	•	i		1	<u> </u>	1		1	i	i		1	1	<u> </u>		i	1	

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CN4[L] CN4			49	SPR_UART2_TX	D01	PIN_UART2_TX	67	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_TX	_	GPIO GPIO	2.5		
CN4[L] CN4		CN4[L]	51	SPR_UART2_RX	D00	PIN_UART2_RX	68	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_RX	_	GPIO GPIO	2.5		
CN4[L] CN4			53	SPR_UART2_RTS	D28	PIN_UART2_RTS	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_RTS	_	GPIO GPIO	2.5		
CN4[L] CN4			55	SPR_UART2_CTS	D27	PIN_UART2_CTS	69	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	UART2	P1n	GPI0	UART2_CTS	-	GPIO GPIO	2.5		
CN4[L] CN4			57	SPR_EMMC_CLK	D23	PIN_EMMC_CLK	75	Digital	1/0	1.8		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO OPIO	EMMC_CLK	SPI5_SCK	GPIO	2.5		
CN4[L] CN4		 		SPR_EMMC_CMD	D24	PIN_EMMC_CMD	70	Digital	1/0	1.8	_	Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO CPIO	EMMC_CMD	SPI5_CS_X	GPIO CPIO	2.5		
CN4[L] CN4				SPR_EMMC_DATA0	D16	PIN_EMMC_DATA1	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO CPIO	EMMC_DATA0	SPI5_MOSI	GPIO CRIO	2.5		
CN4[L] CN4				SPR_EMMC_DATA1	D20	PIN_EMMC_DATA1	70	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC A	P1p	GPIO	EMMC_DATA1	SPI5_MISO	GPIO GPIO	2.5		
CN4[L] CN4[SPR_EMMC_DATA2	D20	PIN_EMMC_DATA2	79	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q	GPIO GPIO	EMMC_DATA2		GPIO GPIO	2.5		
CN4[L] CN4[CN4[L] CN4[L]		CN4[L]		SPR_EMMC_DATA3 GND		PIN_EMMC_DATA3	80	Digital	1/0	1.8	_	Hi-Z –		Hi-Z	_	Hi-Z	CXD5602GG	EMMC B	P1q –	- GP10	EMMC_DATA3 -		GP10 _	2.5		
CN4[L] CN4[CN4[L] CN4[L]		CN4[L]	69 71	SPR_SPI2_SCK	 D42	PIN_SPI2_SCK	28	Power Digital	1/0	1.8		Hi-Z		Hi-Z		Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_SCK	UART0_RXD	I2C3_BDT	2.5		
CN4[L] CN4[CN4[L]	73	SPR_SPI2_SCK	D08	PIN_SPI2_MISO	30	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO_RTS	GPIO	2.5		
CN4[L] CN4[CN4[L]	75	SPR_SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	2.5	+	
CN4[L] CN4		CN4[L]	77	SPR_SPI2_CS_X	D42	PIN_SPI2_CS_X	27	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2A	P00	GPIO	SPI2_CS_X	UARTO_TXD	I2C3_BCK	2.5		
CN4[L] CN4		CN4[L]	79	1.8V		- T 111_31 12_03_X		Power	0	1.8	0	——————————————————————————————————————	0	_	0	_		- -	-	— — — — — — — — — — — — — — — — — — —		- -	1203_BCIX	2.5		
CN4[L] CN4[CN4[L]	81	SPR_SDIO_CMDDIR	D33	PIN_SDIO_CMDDIR	89	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOC	P1t	GPIO	SDIO_CMDDIR	GPIO	GPIO	2.5		
CN4[L] CN4[83	SPR_SDIO_CLK	D38	PIN_SDIO_CLK	81	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1u	GPIO	SDIO_CLK	SPI5_SCK	GPIO	2.5		
CN4[L] CN4			85	SPR_SDIO_CMD		PIN_SDIO_CMD	82	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Hi-Z	CXD5602GG	SDIOA	P1r	GPIO	SDIO_CMD	SPI5_CS_X	GPIO	2.5		
CN4[L] CN4[SPR_SDIO_DATA0	_	PIN SDIO DATAO	83	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO DATAO	SPI5 MOSI	GPIO	2.5		
CN4[L] CN4[SPR_SDIO_DATA1	_	PIN_SDIO_DATA1	84	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA1	SPI5_MISO	GPIO	2.5		
CN4[L] CN4[CN4[L]	91	SPR_SDIO_DATA2	_	PIN_SDIO_DATA2	85	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA2	GPIO	GPIO	2.5	+	
CN4[L] CN4[93	SPR_SDIO_DATA3	_	PIN_SDIO_DATA3	86	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	0	Low	CXD5602GG	SDIOA	P1r	GPIO	SDIO_DATA3	GPIO	GPIO	2.5	+	
CN4[L] CN4[+	95	GND	_	-		Power			_	_	_	_	_		-	_	_	_	-	——————————————————————————————————————	_		+	
CN4[L] CN4		+	97	SPR_SDIO_WP	D37	PIN_SDIO_WP	88	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SDIOB	P1s	GPIO	SDIO_WP	GPIO	GPIO	2.5	+	
CN4[L] CN4			99	GND	_		_	Power		_	_	_	_	-	_	_	-	-	_	-	- SDIO_WI	——————————————————————————————————————	— — — — — — — — — — — — — — — — — — —		+	
CN4[E] CN4[CN4[R]	2	5V	_	_	_	Power	1/0	5	0	_	0	_	0	_	_	_	_	_	_	_	_	6	+	
CN4[R] CN4[CN4[R]	4	5V	_	_	_	Power	1/0	5	0	_	0	_	0	_	_	_	_	_	_	_	_	6	+	
CN4[R] CN4[CN4[R]	6	ACP_SPAP	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5247GF	_	_	_	_	_	_			
CN4[R] CN4[CN4[R]	8	ACP_SPAN	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5247GF	_	_	_	_	_	_		+	
CN4[R] CN4[10	ACP_SPBN	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5247GF	_	_	_	_	_	_			
CN4[R] CN4[12	ACP_SPBP	_	_	_	Analog	0	3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5247GF	_	_	_	_	_	_			
CN4[R] CN4[-	14	AGND_DRV	_	_	_	Power	_	_	_	_	_	_	_	_	-	_	_	_	_	_	_			
CN4[R] CN4[+	16	SPR_SWDIO	_	_	_	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
CN4[R] CN4[CN4[R]	18	SPR_SWDCLK	_	_	_	Digital	1	1.8	ı	Hi-Z	1	Hi-Z	1	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
CN4[R] CN4[CN4[R]	20	ACP_CLK_DMIC	_	_	_	Digital	0	1.8	0	Hi-Z	0	Hi-Z	0	Hi-Z	CXD5247GF	_	_	_	_	_	_			
CN4[R] CN4[22	XRS_PWON	_	_	_	Power	1/0	3.3	1/0	Low	1/0	Low	1/0	Low	CXD5247GF	_	_	_	_	_	_	7		
											., -		., -		., -								CPU_WDT			
CN4[R] CN4[[R]	CN4[R]	24 S	SPR_GNSS_1PPS_OUT	D44	PIN_GNSS_1PPS_OUT	6	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	GNSS_1PPS_OUT	P14	GPIO	GNSS_1PPS_OUT	CPU_WDT	(Open Drain)	2.5		
CN4[R] CN4[[R]	CN4[R]	26	SPR_SEN_IRQ_IN	D22	PIN_SEN_IRQ_IN	37	Digital	1/0	1.8	_	Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	SEN_IRQ_IN	P1e	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	SEN_IRQ_IN	2.5		
			00		5.00		0.1		. / 0	1.0		–		=		–			500			HIE IRO OUT				
CN4[R] CN4[[R]	CN4[R]	28	SPR_HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	HIFIRQ	P02	GPIO	HIF_IRQ_OUT	(Open Drain)	GNSS_1PPS_OUT	2.5		
CN4[R] CN4[[R]	CN4[R]	30	GND	_	_	_	Power	-	_	-	_	_	_	_	_	_	_	_	_	_	_	-			
CN4[R] CN4[[R]	CN4[R]	32	SPR_RST_X	_	_	_	Digital	0	1.8	_	Low	0	High	0	High	CXD5602GG	_	_	_	_	_	_			
		ON14[D]	0.4		D.40			D	1/0	1.0		7					07/220000	4 D. O. 14	D10	0.010	A.D. O.L.K		PMU_WDT	0.5		
CN4[R] CN4[[R]	CN4[R]	34	SPR_AP_CLK	D40	PIN_AP_CLK	5	Digital	1/0	1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	AP_CLK	P13		AP_CLK			2.5		
CN4[R] CN4[[R]	CN4[R]	36	SPR_SEN_AIN2	A0	LPADC0	_	Analog	I	0.7	ı						OND OOLGG			GPIO		PMU_WDT	(Open Drain)	2.0		
CN4[R] CN4[[R]	CN4[R]	38	SPR_SEN_AIN3								_	1	_	1	_	CXD5602GG	_	_	— GP10 —	_		(Open Drain)	1.05	1	
CN4[R] CN4[0111_0211_/11110	A1	LPADC1		Analog	ı	0.7	i	_	1	-	 				_ _		_ _					
CN4[R] CN4[CN4[R]	40	SPR_SEN_AIN4	A1 A2	LPADC1 LPADC2	_	Analog Analog	l	0.7	l	- -		- - -		_ 	CXD5602GG		- - -		_ _ _			1.05		
CN4[R] CN4[[R]		40 42		A1 A2 A3			_		0.7 0.7 0.7		_ _ _ _		-		_ _ _ _	CXD5602GG CXD5602GG	- -	- - -		_	-		1.05 1.05		
		CN4[R]		SPR_SEN_AIN4		LPADC2	- - -	Analog	 			- - - -		-		- - - -	CXD5602GG CXD5602GG CXD5602GG	- - -	- - - -			- - -	- - -	1.05 1.05 1.05		
CN4[R] CN4[[R]	CN4[R] -	42	SPR_SEN_AIN4 SPR_SEN_AIN5	А3	LPADC2 LPADC3	- - - -	Analog Analog		0.7		- - - -		- - -		- - - -	CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - -	- - - - -	- - - -	_ _ _	- - - -	- - - -	1.05 1.05 1.05 1.05		
	[R] [R]	CN4[R]	42 44	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0	A3 A4	LPADC2 LPADC3 HPADC0	- - - -	Analog Analog Analog		0.7		- - - -		_ _ _ _			CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - - -	- - - - -	- - - -	- - -	- - - -	- - - -	1.05 1.05 1.05 1.05 2.5		
CN4[R] CN4[[R] [R] [R]	CN4[R]	42 44 46	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1	A3 A4 A5	LPADC2 LPADC3 HPADC0 HPADC1	- - - - - - 39	Analog Analog Analog Analog		0.7 1.4 1.4		- - - - - - Hi-Z	 	- - - -		- - - - - - Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - - - -	- - - - - - P1g	- - - - -	- - - -	- - - - -	- - - - -	1.05 1.05 1.05 1.05 2.5		
CN4[R] CN4[CN4[R] CN4[R]	[R] [R] [R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	42 44 46 48	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND	A3 A4 A5	LPADC2 LPADC3 HPADC0 HPADC1 -	- - - - - 39	Analog Analog Analog Analog Power		0.7 1.4 1.4 —		- - - - - - Hi-Z		- - - - -		- - - -	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - - - - -		- - - - - -	- - - - -	- - - - - -	- - - - - -	1.05 1.05 1.05 1.05 2.5 2.5		
CN4[R] CN4[CN4[R] CN4[R] CN4[R] CN4[R]	[R] [R] [R] [R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	42 44 46 48 50	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X	A3 A4 A5 - D07	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X	- - - - 39 42 41	Analog Analog Analog Analog Power Digital		0.7 1.4 1.4 - 1.8				- - - - - Hi-Z		- - - - - - Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - - - - - - SPI3_CS1_X	P1g	- - - - - - - GPIO	- - - - - - SPI3_CS1_X	- - - - - - -		1.05 1.05 1.05 1.05 2.5 2.5		
CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[[R] [R] [R] [R] [R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	42 44 46 48 50 52	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI	A3 A4 A5 - D07 D31	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI	- - - - - 39 42 41 43	Analog Analog Analog Analog Power Digital Digital		0.7 1.4 1.4 - 1.8 1.8		Hi-Z		- - - - - Hi-Z		- - - - - - Hi-Z	CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG CXD5602GG	- - - - - - - SPI3_CS1_X SPI3	P1g P1i	- - - - - - - GPIO GPIO		- - - - - - - -		1.05 1.05 1.05 1.05 2.5 2.5 2.5		
CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[[R] [R] [R] [R] [R] [R]	CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R] CN4[R]	42 44 46 48 50 52	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK	A3 A4 A5 - D07 D31 D29	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK	41	Analog Analog Analog Analog Power Digital Digital Digital		0.7 1.4 1.4 - 1.8 1.8		Hi-Z Hi-Z		Hi-Z Hi-Z Hi-Z		- - - - - Hi-Z Hi-Z	CXD5602GG	- - - - - - - SPI3_CS1_X SPI3	P1g P1i P1i	- - - - - - - GPIO GPIO GPIO		- - - - - - - - -		1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5		
CN4[R] CN4[[R] [R] [R] [R] [R] [R] [R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X	A3 A4 A5 - D07 D31 D29 D30 D32	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Analog Analog Analog Analog Power Digital Digital Digital Digital Digital	I/O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8	- - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - - Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1i P1f			- - - - - - - - - -		1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5		
CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[CN4[R] CN4[[R] [R] [R] [R] [R] [R] [R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO	A3 A4 A5 - D07 D31 D29 D30	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO	41	Analog Analog Analog Analog Power Digital Digital Digital Digital		0.7 1.4 1.4 - 1.8 1.8 1.8		Hi-Z Hi-Z Hi-Z		Hi-Z Hi-Z Hi-Z H		- - - - - Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1i	- - - - - - - GPIO GPIO GPIO		- - - - - - - - - -		1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5		
CN4[R] CN4[[R] [R] [R] [R] [R] [R] [R] [R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X	A3 A4 A5 - D07 D31 D29 D30 D32	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X	41	Analog Analog Analog Analog Power Digital Digital Digital Digital Digital	I/O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8	- - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - - Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1i P1f			- - - - - - - - - - - RTC_IRQ_OUT		1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5		
CN4[R] CN4[[R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT	A3 A4 A5 - D07 D31 D29 D30 D32	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Analog Analog Analog Analog Analog Power Digital Digital Digital Digital Digital Digital Digital	I/O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8	- - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - - Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1i P1f					1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5		
CN4[R] CN4[[R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM	A3 A4 A5 - D07 D31 D29 D30 D32 D41 -	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT -	41	Analog Analog Analog Analog Analog Power Digital Digital Digital Digital Digital Analog Analog	I/O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3	- - - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - - Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1i P1f					1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.		
CN4[R] CN4[[R]	CN4[R]	42 44 46 48 50 52 54 56 58	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP	A3 A4 A5 - D07 D31 D29 D30 D32 D41	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Analog Analog Analog Analog Analog Power Digital Digital Digital Digital Digital Analog Analog Analog	I/O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3	- - - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG		P1g P1i P1i P1f P12 -	GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.		
CN4[R] CN4[[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD	A3 A4 A5 - D07 D31 D29 D30 D32 D41 - D39	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Analog Analog Analog Analog Analog Power Digital	I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8	- - - - - - - -	Hi-Z Hi-Z Hi-Z Hi-Z				- - - - - Hi-Z Hi-Z Hi-Z Hi-Z	CXD5602GG	SPI3_CS1_X SPI3 SPI3 SPI3 SPI3 SPI3 SPI3 SPI3_CS0_X RTC_IRQ_OUT - HIF_GPI00	P1g P1i P1i P1f P12 -	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.		
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_MISO SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND	A3 A4 A5 - D07 D31 D29 D30 D32 D41 - D39 -	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0 -	41	Analog Analog Analog Analog Analog Power Digital Digital Digital Digital Digital Digital Digital Power Analog Analog Digital Power	I/O I/O I/O I/O I/O I/O I/O O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 1.8 -	- - - - - - - - - 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z -			- -	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-	CXD5602GG		P1g P1i P1i P1f P12 -	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5	A3 A4 A5 — D07 D31 D29 D30 D32 D41 — D39 — —	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Analog Analog Analog Analog Analog Power Digital	/0	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4	- - - - - - - - - 0 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z			_ _ _ _ 0		CXD5602GG		P1g P1i P1i P1f P12 -	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6	A3 A4 A5 — D07 D31 D29 D30 D32 D41 — D39 — — —	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPIO0	41	Analog Analog Analog Analog Analog Power Digital	I/O I/O I/O I/O I/O I/O O O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4	- - - - - - - - 0 0	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z			- - - 0 0		CXD5602GG		P1g P1i P1i P1f P12 P03	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R]	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MSO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7	A3 A4 A5 — D07 D31 D29 D30 D32 D41 — D39 — — — — —	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT PIN_HIF_GPIO0	41	Analog Analog Analog Analog Analog Power Digital Analog Analog Digital Power Digital Digital Digital	I/O I/O I/O I/O I/O I/O O O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4	O O I/O	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z			- - - 0 0		CXD5602GG		P1g P1i P1i P1f P12	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R]	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MSO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS	A3 A4 A5 — D07 D31 D29 D30 D32 D41 — — D39 — — — — — —	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT - PIN_HIF_GPIO0	41	Analog Analog Analog Analog Analog Power Digital Analog Analog Digital Power Digital Digital Power Digital Digital Power	I/O I/O I/O I/O I/O I/O O O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4 3.6-4.4	O O I/O	Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	1/ 0		0 0 0 0 1/0		CXD5602GG		P1g P1i P1i P1f P12	GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R]	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MOSI SPR_SPI3_SCK SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_RTC_IRQ_OUT SPR_USB_DM SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS	A3 A4 A5 — D07 D31 D29 D30 D32 D41 — — D39 — — — — — — — —	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41	Analog Analog Analog Analog Analog Power Digital Power Digital Digital Power Digital Digital Power Digital Digital Power Power	I/O I/O I/O I/O I/O I/O O O I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4 3.6-4.4 3.6-4.4		Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z	1/ 0		0 0 0 0 1/0		CXD5602GG		P1g P1i P1i P1f P12	GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.	Headphone mute	Mute at Low
CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4[R] CN4[CN4[R] CN4	[R]	CN4[R]	42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80 82	SPR_SEN_AIN4 SPR_SEN_AIN5 SPR_SEN_AIN0 SPR_SEN_AIN1 GND SPR_SPI3_CS1_X SPR_SPI3_MOSI SPR_SPI3_MSO SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_SPI3_CS0_X SPR_USB_DM SPR_USB_DP SPR_USB_DP SPR_GPS_EXTLD GND ACP_GPO5 ACP_GPO6 ACP_GPO7 ACP_VSYS ACP_VSYS GND SPR_SDIO_CD	A3 A4 A5 - D07 D31 D29 D30 D32 D41 D39	LPADC2 LPADC3 HPADC0 HPADC1 - PIN_SPI3_CS1_X PIN_SPI3_MOSI PIN_SPI3_SCK PIN_SPI3_MISO PIN_SPI3_CS0_X PIN_RTC_IRQ_OUT	41 43 38 4 - - 32 - - - - - - - 87	Analog Analog Analog Analog Analog Power Digital Digital Digital Digital Digital Digital Digital Digital Digital Power Digital Power Digital Power Power Power	I/O	0.7 1.4 1.4 - 1.8 1.8 1.8 1.8 1.8 3.3 3.3 1.8 - 3.6-4.4 3.6-4.4 3.6-4.4 3.6-4.4 1.8		Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z Hi-Z - Hi-Z Hi-Z	I/O - -		-		CXD5602GG		P1g P1i P1i P1f P1c	GPIO GPIO GPIO GPIO GPIO GPIO GPIO GPIO				1.05 1.05 1.05 1.05 1.05 2.5 2.5 2.5 2.5 2.5 2.5 2.5 2.5 7 7 7	Headphone mute	Mute at Low

CN4[R]	CN4[R]	CN4[R]	86	SPR_I2C2_SCL		PIN_SPI0_MOSI	19	Digital	I/O 1.8	Ι_	High	1/0	_ [, [High	CXD5602GG	SPI0B	P17	GPIO	I2C2_BCK	SPI0_MOSI		2.5		
CN4[R]	CN4[R]	+	88	ACP_GP00		FIIN_3FI0_IVIO3I	_	Digital	0 3.6-4.4		Hi-Z	0	Hi-Z	0	Hi-Z	CXD5002GG CXD5247GF	- 3F10B	F11	GF10 _	1202_BCR	- 3F10_IVIO31	_	2.3		
CN4[R]	CN4[R]						_		0 3.6-4.4			0					_		_	_	_	_		Audio 3.3V output	Output at High
CN4[K]	CN4[K]	CN4[R]	90	ACP_GPO1		_	_	Digital	0 3.0-4.4	U	Hi-Z	0	Low	0	Low	CXD5247GF	_	_		_	_	_		·	Output at High
CN4[R]	CN4[R]	CN4[R]	92	ACP_GPO2	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	Ο	Low	CXD5247GF	_	_	_	_	-	-		Power output for LTE	Output at High
																								(Only LTE extension)	
ON 4[D]	0114[D]	ON A [D]	0.4	400.0000				D							ı	07/0504205								LTE power output from	
CN4[R]	CN4[R]	CN4[R]	94	ACP_GPO3	_	_	_	Digital	O 3.6-4.4	0	Hi-Z	0	Low	0	Low	CXD5247GF	_	_	_	_	_	_		the main board	Output at High
																								(Only LTE extension)	
CN4[R]	CN4[R]		96	SWOCLK	_	_	-	Digital	O 1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	_	2.5		
CN4[R]	CN4[R]		98	SWO	_	_	_	Digital	O 1.8	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	_	_	_	_	_	-	2.5		
CN4[R]	CN4[R]	CN4[R] 1	L00	GND	_	_	_	Power		_	_	_	_	_	_	_	_	_	_	_	_	-			
	JP3		1	NC	_	_	_	_		_	_	_	-	_	_	_	_	_	_	_	_	_			
	JP3		2	VDD_LVS	_	_	_	Power	O 5/3.3	0	_	Ο	_	0	_	_	_	_	_	_	_	_			
	JP3		3	XRS_PWON	_	_	_	Digital	1/0 3.3	1/0	_	1/0	_	1/0	_	_	_	_	_	_	_	_	7		
	JP3		4	3.3V	_	_	_	Power	O 3.3	Ο	-	Ο	_	Ο	_	_	_	_	_	_	_	-			
	JP3		5	MAIN_POWER	_	_	_	Power	1/0 5	1/0	_	1/0	-	1/0	_	_	_	_	_	_	_	-	6		
	JP3		6	GND	_	_	_	Power		_	-	-	-	-	_	_	_	_	_	_	_	-			
	JP3		7	GND	_	_	_	Power		_	-	-	-	-	_	-	_	_	_	_	_	_			
	JP3		8	5V_IN_PIN	_	_	_	_		_	_	-	_	-	_	_	_	_	_	_	_	_			
	JP4		1	SPR_SEN_AIN2	A0	LPADC0	_	Analog	I ~5	ı	_	1	_	1	_	CXD5602GG	_	_	SEN_AIN2	SEN_AIN2	SEN_AIN2	SEN_AIN2	7.5		
	JP4		2	SPR_SEN_AIN3	A1	LPADC1	_	Analog	I ~5	I	_		_		_	CXD5602GG	_	_	SEN_AIN3	SEN_AIN3	SEN_AIN3	SEN_AIN3	7.5		
	JP4	 	3	SPR_SEN_AIN4	A2	LPADC2	_	Analog	I ~5		_		_		_	CXD5602GG	_	_	SEN_AIN4	SEN_AIN4	SEN_AIN4	SEN_AIN4	7.5		
	JP4		4	SPR_SEN_AIN5	A3	LPADC3	_	Analog	1 ~5		_	-+	_	$\neg \uparrow$	_	CXD5602GG	_	_	SEN_AIN5	SEN_AIN5	SEN_AIN5	SEN_AIN5	7.5		
	JP4		5	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I ~5	<u> </u>	_		_		_	CXD5602GG	_	_	SEN_AINO	SEN_AIN0	SEN_AIN0	SEN_AIN0	8.9		
	JP4		6	SPR_SEN_AIN1	A5	HPADC1	_	Analog	1 ~5	<u> </u>	_	+	_		_	CXD5602GG	_	_	SEN_AIN1	SEN_AIN1	SEN_AIN1	SEN_AIN1	8.9		
	JP2		1	12C0_SCL	D15	PIN_I2C0_BCK	44	Digital	1/0 5/3.3	_	High	<u> </u>	High	i	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BCK	-	-	7		
	JP2		2	12C0_SDA	D14	PIN_I2C0_BDT	45	Digital	1/0 5/3.3	_	High	- 	High	<u> </u>	High	CXD5602GG	12C0	P1j	GPIO	I2C0_BDT	_	_			
	JP2		3	AREF		-	_		O 5/3.3	0	High	0	High	0	High		-	_	_		_	_	<u>'</u>		
	JP2		1	GND			_	_		_	-	_	-	_		_	_	_	_	_	_	_			
	JP2		5	SPI4_SCK	D13	PIN_SPI4_SCK	72	Digital	1/0 5/3.3	_	Hi-Z		Hi-Z		Hi-Z	CXD5602GG	SPI4	P1o	GPIO	SPI4_SCK	_	GPIO	7		
	JP2		6	SPI4_MISO		PIN_SPI4_MISO	7/	_	1/0 5/3.3		Hi-Z		Hi-Z	_	Hi-Z	CXD5602GG	SP14		GPIO	SPI4_3CK SPI4_MISO	_	GPIO	7		
	JP2 JP2		7		D12		72	Digital	1/0 5/3.3		Hi-Z		Hi-Z		Hi-Z		SP14 SP14	P1o	GPIO GPIO	SPI4_MISU SPI4_MOSI	_	GPIO GPIO	7		
			0	SPI4_MOSI	D11	PIN_SPI4_MOSI	71	Digital				_				CXD5602GG	SP14 SP14	P1o					7		
	JP2		0	SPI4_CS_X	D10	PIN_SPI4_CS_X	71	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG		P10	GPIO GPIO	SPI4_CS_X	- 1001 DOK	GPIO			
	JP2		9	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z		Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	- ODIO			
	JP2		10	SPI2_MISO	D08	PIN_SPI2_MISO	30	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MISO	UARTO_RTS	GPIO			
	JP13		1	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_			
	JP13		2	PWM0	D06	PIN_PWM0	46	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	-	_			
	JP13		3	PWM1	D05	PIN_PWM1	47	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	7		
	JP13		4	SPI2_MOSI	D04	PIN_SPI2_MOSI	29	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI2B	P01	GPIO	SPI2_MOSI	UARTO_CTS	GPIO	7		
	JP13		5	PWM3	D03	PIN_PWM3	49	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	-	7		
	JP13		6	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT	GNSS_1PPS_OUT	7		
																				_	(Open Drain)		•		
	JP13		7	UART_TX	D01	PIN_UART2_TX	67	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_TX	_	GPIO	7		
	JP13		8	UART_RX	D00	PIN_UART2_RX	68	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	UART2	P1n	GPIO	UART2_RX	_	GPIO	7		
		CN9	1	MAIN_POWER				Power	O 4~5	0	High	0	High	0	High	_	_	_	_	_	_	_			
		CN9	2	GND				Power		_	High	_	High	-	High	_	_	_	_	_	_	_			
		CN9	3	SPR_SEN_AIN1	A5	HPADC1	_	Analog	I ~5	ı	_		_		Hi-Z	CXD5602GG	_	_	_	_	_	_	8.9		
		CN9	4	SPR_SEN_AIN0	A4	HPADC0	_	Analog	I ~5	I	_		_	ı	Hi-Z	CXD5602GG	_	_	_	_	-	_	8.9		
		CN9	5	3.3V				Power	O 3.3	0	_	0	_	0	_	_	_	_	_	_	_	_			
		CN9	6	GND				Power		_	_	_	_	_	_	_	_	_	_	_	_	-			
		CN9	7	HIF_IRQ_OUT	D02	PIN_HIF_IRQ_OUT	31	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	HIF_IRQ_OUT	P02	GPIO	HIF_IRQ_OUT	HIF_IRQ_OUT	GNSS_1PPS_OUT	7		
		CIVA	1		DUZ		31	Digital	1/O 5/3.3	<u></u>	111-∠		ı II-Z		I II-∠ 	CAD3002GG		F UZ	GFIU		(Open Drain)				
		CN9	8	SPI3_SCK	D29	PIN_SPI3_SCK	41	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_SCK	_	_	7		
		CN9	9	PWM0	D06	PIN_PWM0	46	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM0	_	_	7		
		CN9	10	SPI3_MISO	D30	PIN_SPI3_MISO	43	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MISO	_	_	7		
		CN9	11	PWM1	D05	PIN_PWM1	47	Digital		_	Hi-Z	_	Hi-Z	-	Hi-Z	CXD5602GG	PWMA	P1k	GPIO	PWM1	GPIO	_	7		
		CN9	12	SPI3_MOSI	D31	PIN_SPI3_MOSI	42	Digital	1/0 5/3.3	_	Hi-Z	-	Hi-Z	-	Hi-Z	CXD5602GG	SPI3	P1i	GPIO	SPI3_MOSI	_	_	7		
		CN9	13	PWM2	D09	PIN_PWM2	48	Digital	1/0 5/3.3	_	Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM2	I2C1_BCK	-	7		
		+	14	SPI3_CS0_X	D32	PIN_SPI3_CS0_X	38	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS0_X	P1f	GPIO	SPI3_CS0_X	_	_	7		
		CN9	15	PWM3	D03	PIN_PWM3	49	Digital	1/0 5/3.3		Hi-Z	-	Hi-Z	_	Hi-Z	CXD5602GG	PWMB	P1I	GPIO	PWM3	I2C1_BDT	_	7		
			16	SPI3_CS1_X	D07	PIN_SPI3_CS1_X	39	Digital	1/0 5/3.3		Hi-Z	_	Hi-Z	_	Hi-Z	CXD5602GG	SPI3_CS1_X	P1g	GPIO	SPI3_CS1_X	_	_	7		
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Date	Contents
2020.11.13	First draft.
2021 4 1 4	Corrected the initial value of ACP_GPO1 to 7 after starting
2021.4.14	Arduino and SDK.
	Corrected each initial value of XRST (SPR_RST_X).
	Corrected the voltage range of the pins connected to the
2021.12.20	CXD5247GF to 3.6-4.4V.
2021.12.20	Corrected the initial values after turning on the power of
	MCLK and after starting Aruduino.