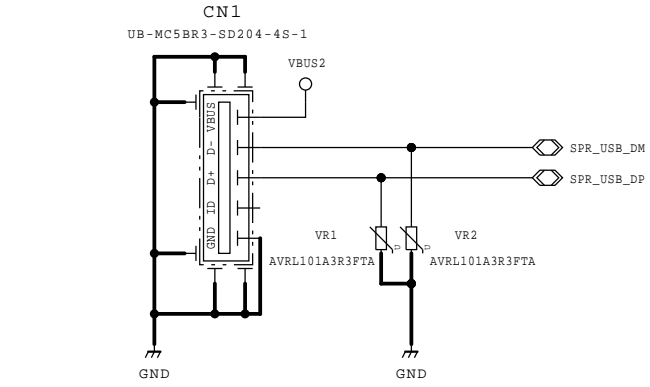
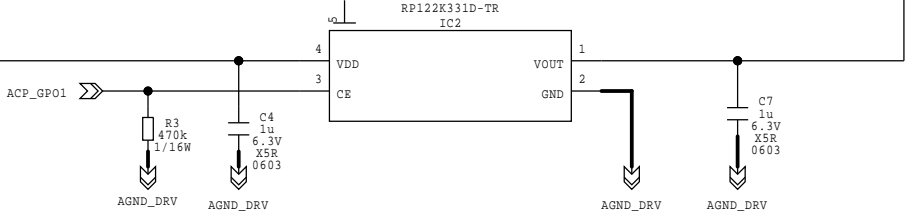
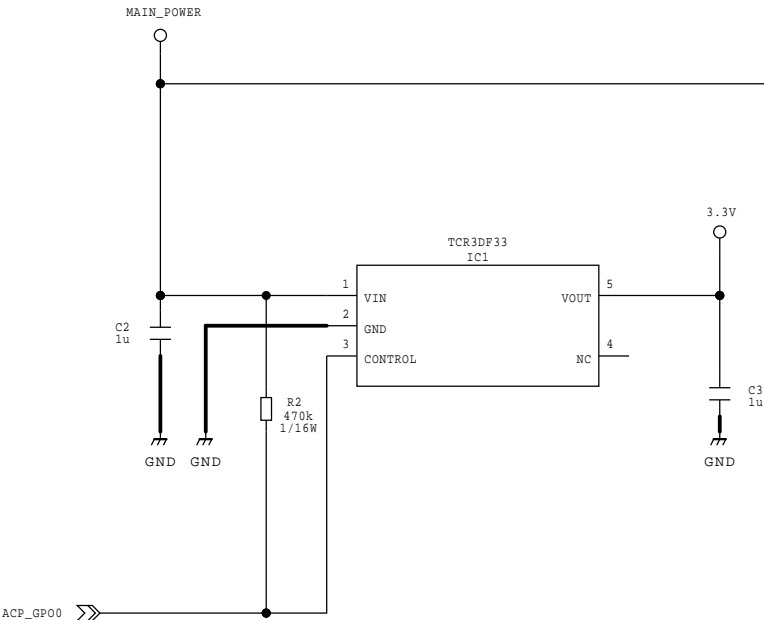
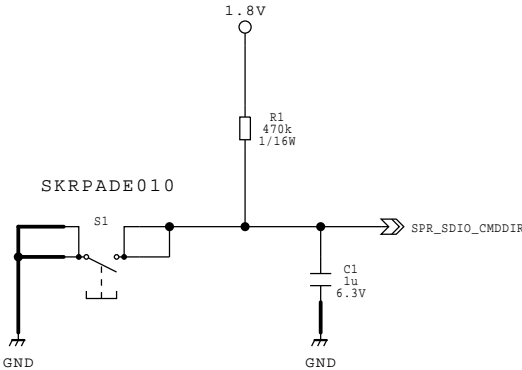
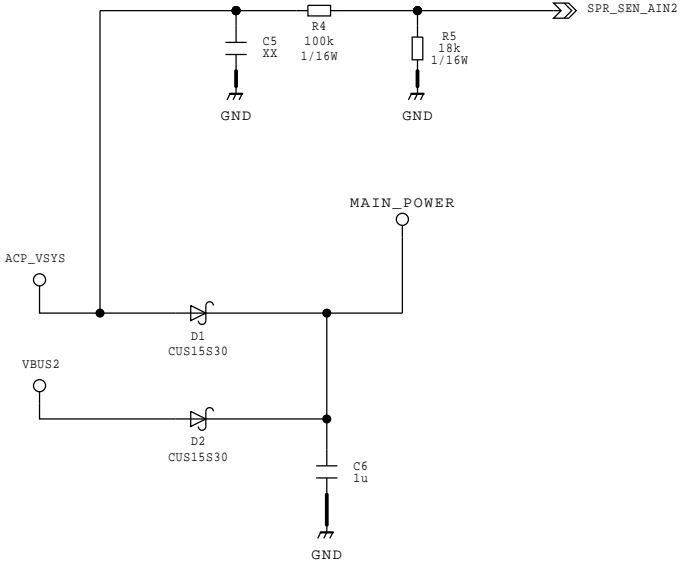
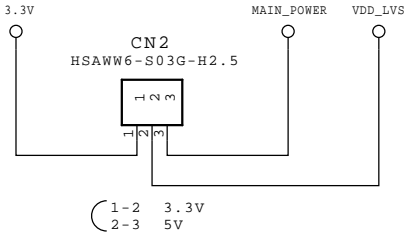


MICRO USB CN TYPE-B

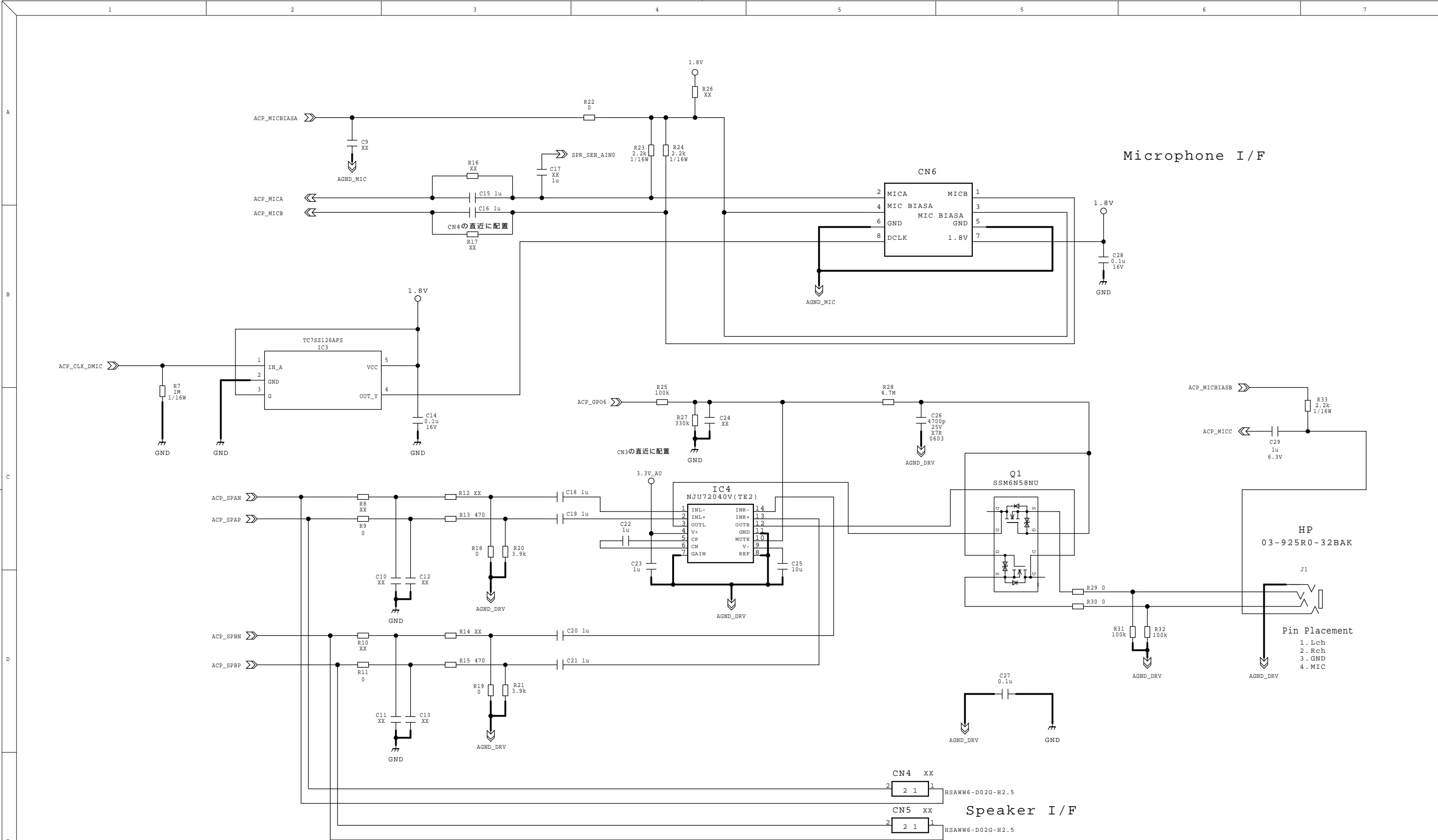


I/O Power SW JP



Note) "XX" stands for NM

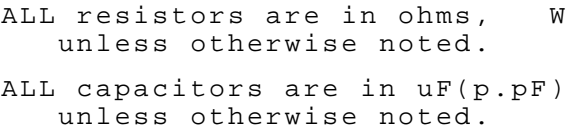
3 x				GENERAL TOLERANCE				DATE OF ISSUE	
2 x								DOCUMENT TYPE	
1 x								CIRCUIT DIAGRAM	
HISTORY X COUNT				RELEASE NO.				TOLERANCING	
				DATE				ISO 8015	
LEGAL OWNER								MATERIAL (COLOR)	
								FINISH (COLOR)	
								2019.12.23	
RESPONSIBLE DEPARTMENT						SIZE	UNIT	ORIGINAL MODEL	
						A3	mm	SPRESENSE	
PLANNED BY			CHECKED BY		APPROVED BY	SCALE		DESCRIPTION (E)	
						:		LTE Board (CXD5602PWBLM1)	
DRAWN BY						PRJ.METHOD		TENTATIVE MODEL	
						PRJ.METHOD		TENTATIVE PART NO.	
						PRJ.METHOD		PART NO.	
						PRJ.METHOD		SHEET/PAGES	
						PRJ.METHOD		1 / 7	

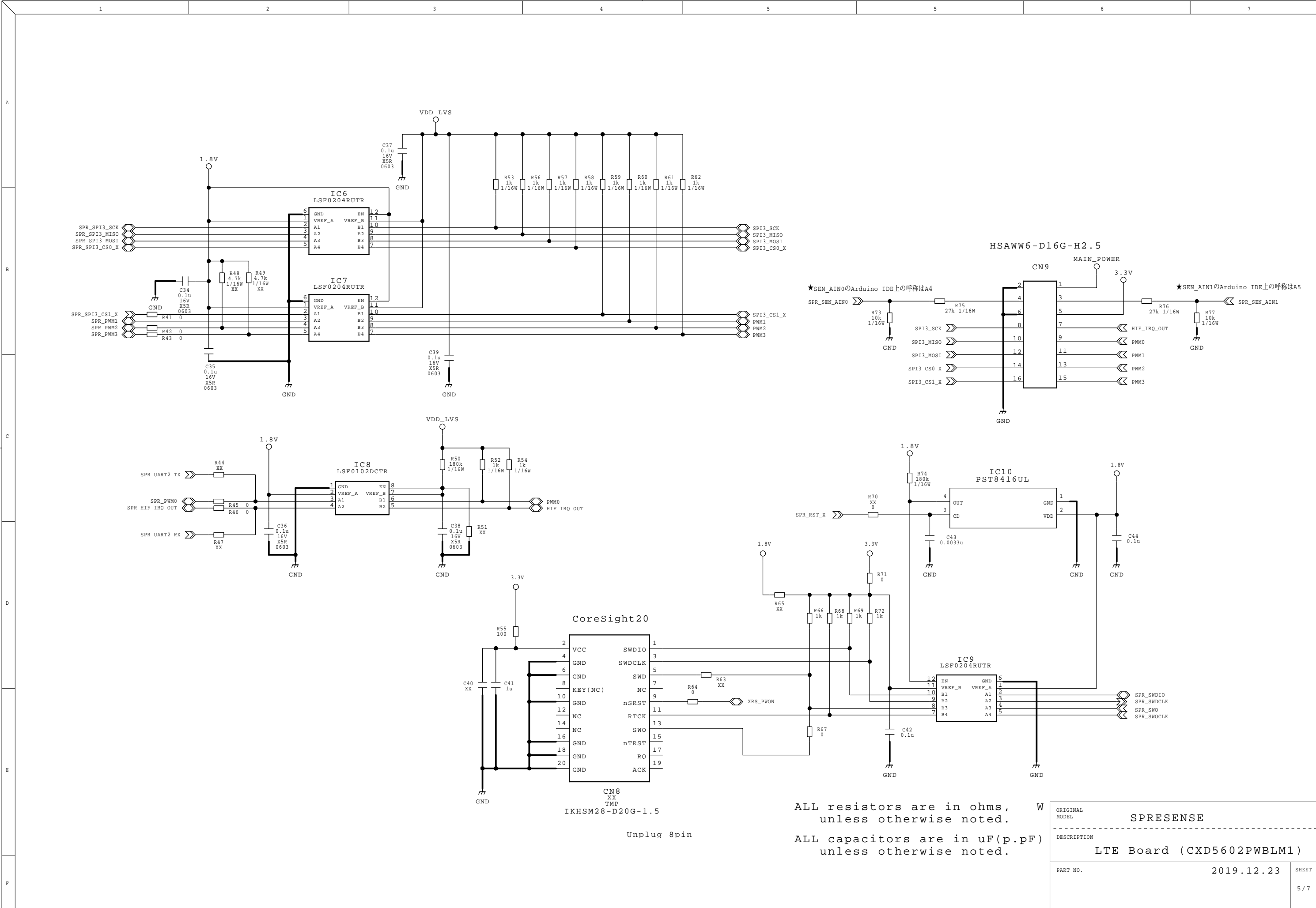


ALL resistors are in ohms, unless otherwise noted.

ALL capacitors are in uF(p.pF) unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.12.23	SHEET 3 / 7

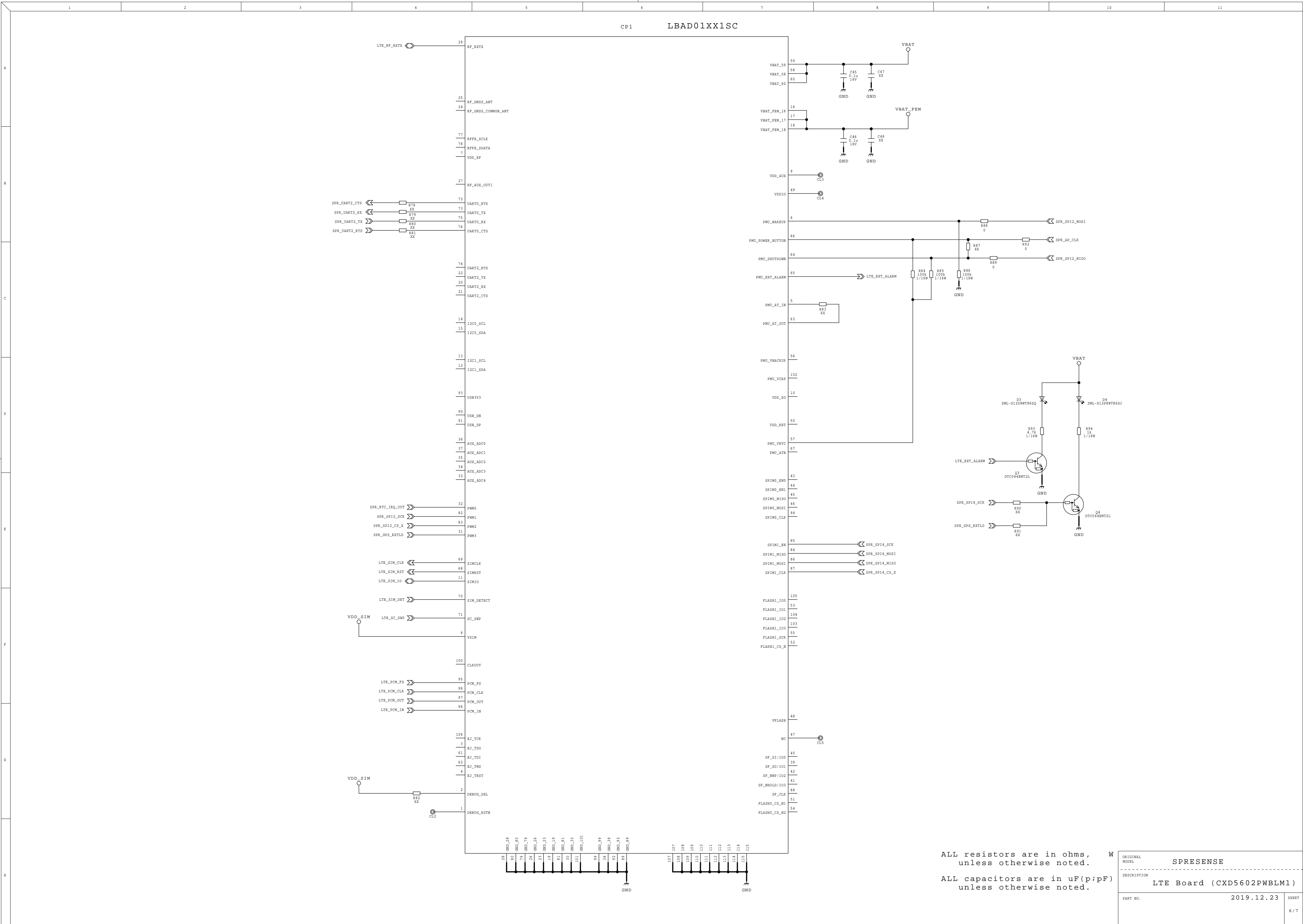
A 3

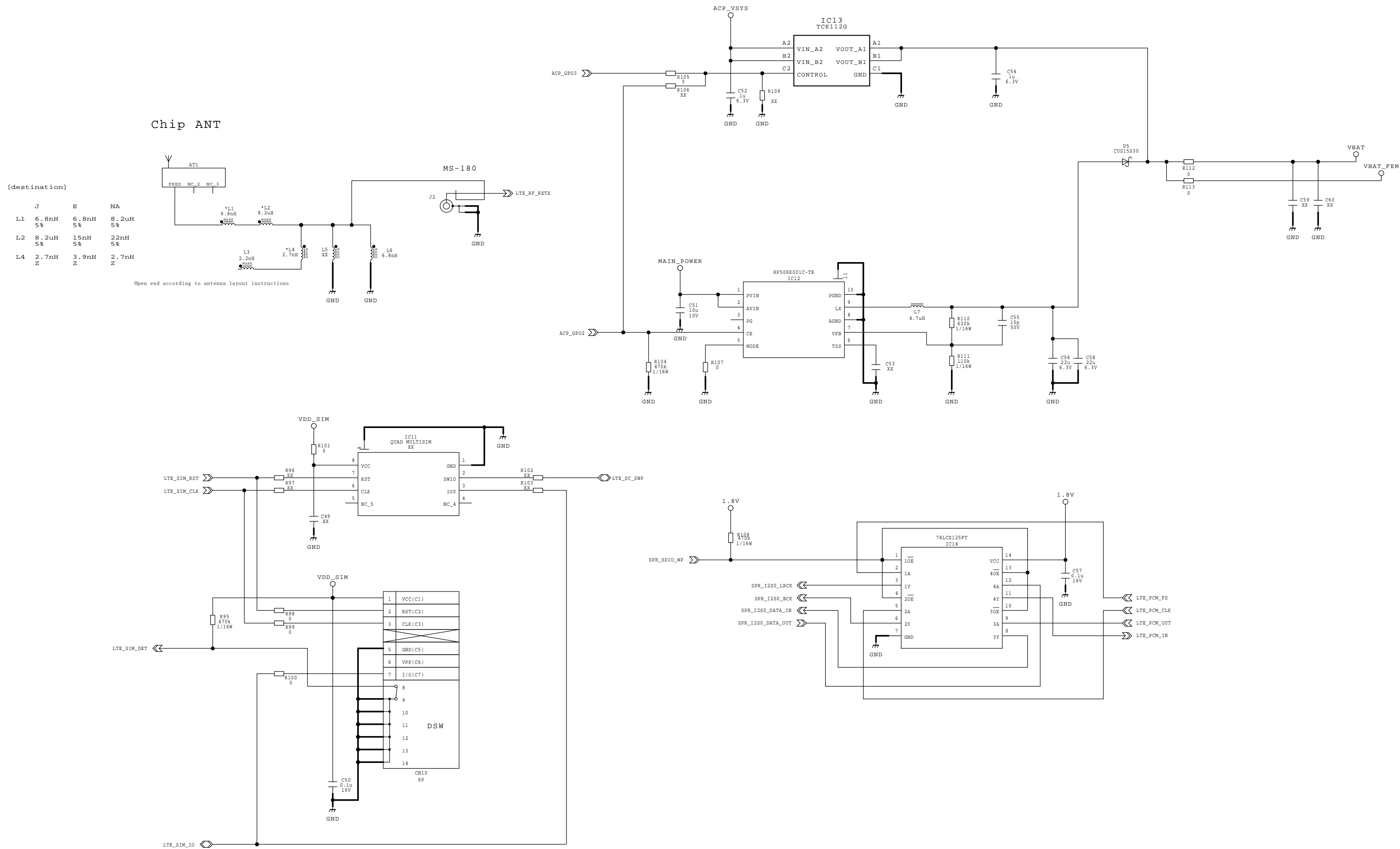


ALL resistors are in ohms, unless otherwise noted.

ALL capacitors are in uF(p.pF) unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.12.23	SHEET
		5 / 7





ALL resistors are in ohms,
unless otherwise noted.

ALL capacitors are in uF(p;pF)
unless otherwise noted.

ORIGINAL MODEL	SPRESENSE	
DESCRIPTION	LTE Board (CXD5602PWBLM1)	
PART NO.	2019.12.23	SHEET 7 / 7