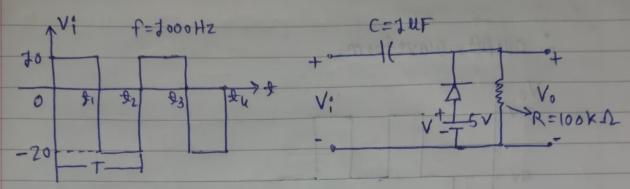
DELD Pructice Sheet 32 Zala Divy Balderbhai

AOUS U2065045 Caathi

(J)

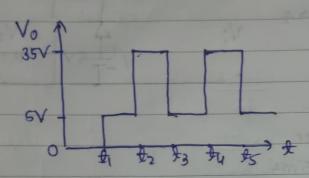


$$\rightarrow$$
 T= $\frac{1}{f} = \frac{1}{1000} =$

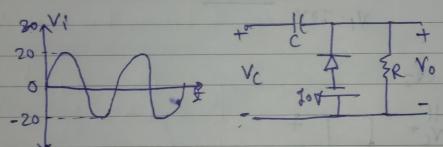
Again diode will be in RB. According KYL, 25+Ve-Vo=0 · V = 25+70=35V



. Output waveform,

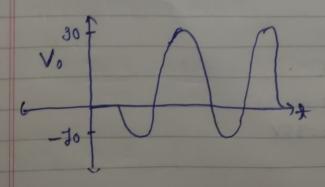


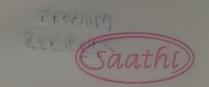
(2) Determine output waveform



- -> This circuit is the clumper with Ve get. of Jor.
- for 1st +ve hulf rycle, diode will be RB.
 - .. Vo= 0 as i=0 due to churging of capacitos.
- -) for 1st -ve hulf cycle, diode will be FB.
 - Vo=tor und capacitos will be charged to tor.
- -> Aftes 1 complete cycle cupucitos will provide de voltuge of lon
 - .. Vmux = Jo+20=30, Vmin = 10+(-20) = -10

output waveform,





(3)

(i) for +ve haif cycle,

4) Vi (20

Zenes diode - open ciscuited - K



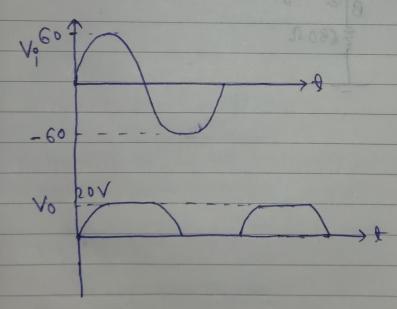
2) Vi 720

Zenes diode will provide const. vo Huge of 20v.

00 V0 = 20 V

(ii) for -ve half cycle,

→ Silicon diode cuill be open circuited and zenez will be in FB.



(U) 1=5490 A°

= 12400 eV

° F = 2.258eV

Caath

(5)
$$R_5=(3)$$
, $T_7=20 \text{ mA}$
 $V_9=7.6 \text{ V}$
 $V_5=70 \text{ V}$

$$R_{5} = V_{5} - V_{5}$$

Applying kVL,

$$V_A-V_B=1.8+0.7$$
 $V_B=V_A-2.5$
 $V_B=9.5V$

Applying KIL at node B,

$$70=7R$$
 $70=7R$
 $70=$