Computer Engineering Department, SVNIT, Surat

Course: Microprocessor and Interfacing Techniques (CS202)

Assignment

(1) Load the hexadecimal number 9DH and A9H in register D and E,respectively,and add the numbers. If the sum is greater than FFH,display 01H at output PORT0;otherwise, display the sum.

(2) Specify the register contents and the flag status(S,Z,CY) after the instruction ORA A is executed.

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MVI A,A7H
MVI B,54H
ADD B
ORA A
```

(3) Specify the contents of the registers and the flag status(S,Z,CY) as the following instructions are executed.

	A	B	C	D	S	Z	CY
MVI A,00H							
MVI B,F6H							
MOV C,A							
MOV D,B							
HLT							

(4) Sixteen bytes of data are stored in memory locations at XX50H to XX5FH.Transfer the entire block of data to new memory locations starting at XX70H.
Data(H) : 37,A2,F2,82,57,5A,7F,DA,E5,8B,A7,C2,B8,10,19,98

(5) The following block of data is stored in the memory locations from XX55H to XX5AH.Transfer the data to the locations XX80H to XX85H in the reverse order.
(eg: the data byte 22H should be stored at XX85H and 37H at XX80H)
Data(H) : 22,A5,B2,99,7F,37