

Sol-1 @ IE stands for Interrupt Enable, which is a flip flop. 8085 interrupt system is disabled whenever this flip flop is reset to 0 states. That is even if an external interrupt pin is activated, the corresponding internal signal is not activated.

e.g. EI : Enable interrupt
 MOV A, 98H : Load 8 bit pattern to enable
 SIM : sim instruction to enable
 RST 7.5, 6.5, 5.5

⑥ When executes instruction it will execute ISR according to priority of interrupt.

The priority of interrupt in decreasing order is

~~TRAP~~ TRAP > RST 7.5 > RST 6.5 > RST 5.5 > INTR

So among RST 5.5 and 7.5, RST 7.5 will be of high priority

1-b) Key check: CALL Keyboard
CPI 04H
JNZ
LXI H, 0020H ; Location RST 7.5
EI
CALL RST 7.5 ; call 0020H on
JMP Keycheck call H
POP H
POP D
POP B
POP PSW
RET
MVI A, FFH

RST 7: MVI D, F0H
Rotate : MOV A, D

1- ~~Q~~ (ii) LXI H, 2000H
 MOV A, M
 MOV C, A
 INX M
 MOV B, M
 MOV D, B
 ADD B
 SUI
 JNC LOOP
 MOV A, C
 RSTS ! MVI D, F0H
 Rotate MOV A, D
 RLC
 RLC