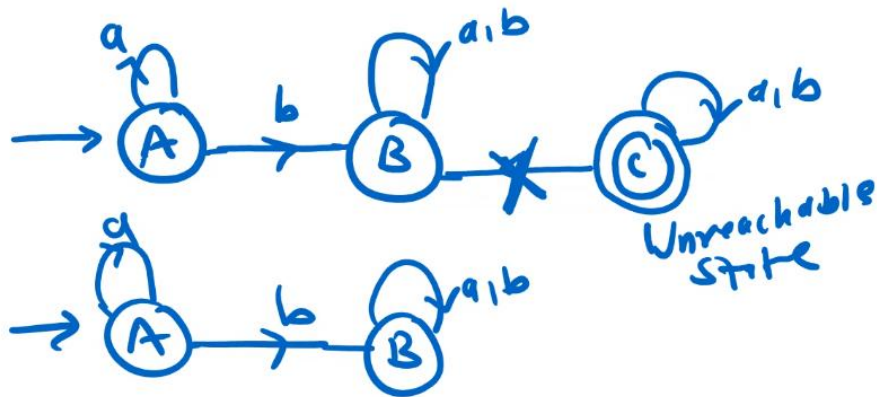


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↳ If the resulting machine is free from final state then it accepts empty language.



Are you talking? Your mic is off.
Click the mic to turn it on.

8:37 AM | Div-B | CS208 | AFL Theory Class | 11.02.2022 @ 8:3...

B095 AARTI OTARI SVNIT

B126 TANGUDU VIVEK S...

B114 LELLAPALLI VIKAS ...

B121 AJAY KANHERKAR ...

Dr. Balu L. Parne SVNIT

B096 SNEHARSH BELSA...

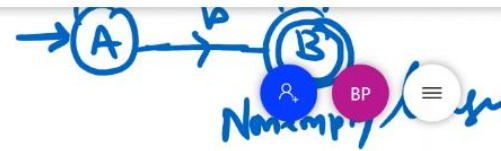
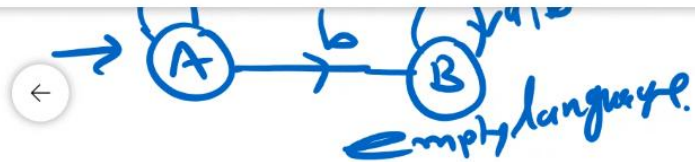
B077 MAHAVIR SVNIT

20 others

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⇒ Finite or Infinite nll :->

↳ Select the state which cannot be reachable from the initial state & delete them.

1



8:41 AM | Div-B | CS208 | AFL Theory Class | 11.02.2022 @ 8:3...



B095 AARTI OTARI SVNIT



B123 SHISHIR SVNIT



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B096 SNEHARSH BELSA...



B077 MAHAVIR SVNIT



B114 LELLAPALLI VIKAS ...



B126 TANGUDU VIVEK S...



23 others



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BP



⇒ Finite or Infinite nelly ⇒

↳ Select the state which cannot be reachable from the initial state & delete them. → (Unreachable state)

↳ select those state from which we cannot reach to the final state and delete them. (Dead state)



8:42 AM | Div-B | CS208 | AFL Theory Class | 11.02.2022 @ 8:3...



B095 AARTI OTARI SVNIT



B123 SHISHIR SVNIT



Dr. Balu L. Parne SVNIT



B096 SNEHARSH BELSA...



B077 MAHAVIR SVNIT



B083 Hemnani Mihir Jit...



B126 TANGUDU VIVEK S...



25 others



You

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↳ Select the state which cannot be reached from initial state & delete them. → (Unreachable state)

↳ select those state from which we cannot reach to the final state and delete them. (Dead state)

↳ If the resulting machine contains loops or cycles then the machine accepts the infinite language.

↳ If the resulting machine is free from cycles or loops then it accepts finite language.



8:44 AM | Div-B | CS208 | AFL Theory Class | 11.02.2022 @ 8:...



35

B095 AARTI OTARI SVNIT

B123 SHISHIR SVNIT

Dr. Balu L. Parne SVNIT

B096 SNEHARSH BELSA...

B077 MAHAVIR SVNIT

B083 Hemnani Mihir Jit...

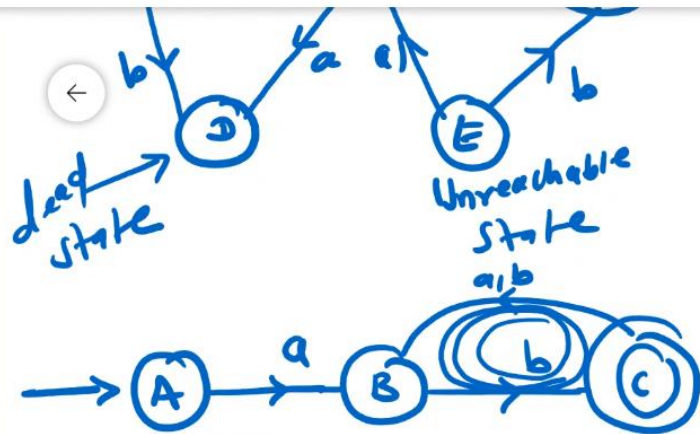
B126 TANGUDU VIVEK S...

26 others

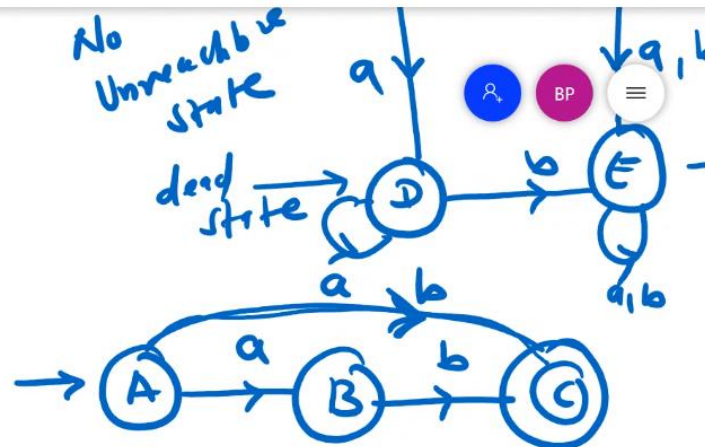
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↓
Infinite Language.



{ a b, a b }
⇒ Finite Language.



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B123 SHISHIR SVNIT

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B096 SNEHARSH BELSA...

B077 MAHAVIR SVNIT

B083 Hemnani Mihir Jit...

B109 CH ADITHYA SVNIT

28 others

You

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BP



Equivalence \Rightarrow

Two Finite state machines M_1 and M_2 are said to be equal if both of them accept the same set of string.

r



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B077 MAHAVIR SVNIT



B109 CH ADITHYA SVNIT



B123 SHISHIR SVNIT



B096 SNEHARSH BELSA...



B083 Hemnani Mihir Jit...



30 others



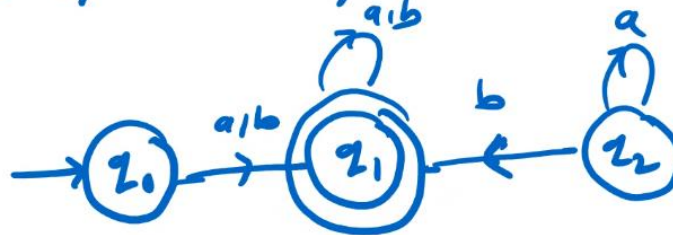
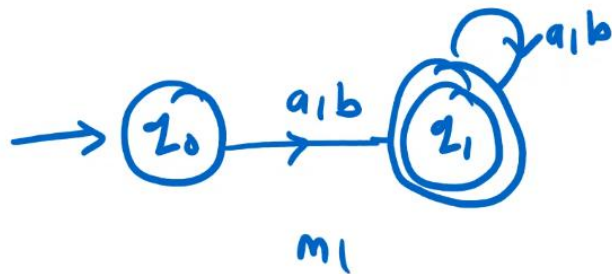
You

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Two finite state machines M_1 and M_2 are said to be equal if both of them are accept same set of string.

$$M_1 = M_2 \iff L(M_1) = L(M_2)$$



39

You



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B096 SNEHARSH BELSA...



B077 MAHAVIR SVNIT



B083 Hemnani Mihir Jit...



B109 CH ADITHYA SVNIT



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← number of states.

→ Two machines M_1 and M_2 are said to be isomorphic to each other if one can be obtained from other by replacing the states a, b



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39

You

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B096 SNEHARSH BELSA...

B077 MAHAVIR SVNIT

B083 Hemnani Mihir Jit...

B109 CH ADITHYA SVNIT

30 others

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→ Two machines M_1 and M_2 are said to be isomorphic to each other if one can be obtained from other by replacing the state a, b



→ The Isomorphic machine must be equal & the equal machine need not be isomorphic.



40

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BP



⇒⇒ Comparison Method is⇒
let M_1 and M_2 be two finite state machine

→ Construct the transition table that have pair of
states (q, q') $q \in M_1$ and $q' \in M_2$

→



9:00 AM | Div-B | CS208 | AFL Theory Class | 11.02.2022 @ 8:...



B132 ESWAR KALYAN RE...



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B077 MAHAVIR SVNIT



B083 Hemnani Mihir Jit...



B109 CH ADITHYA SVNIT



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Pair of initial states.

→ If the table contains any pair of the
Final & Nonfinal or Nonfinal and Final then stop
the construction of the table and declare that
two machines are not equal.



Windows taskbar showing various application icons, system tray icons, and the time/date: 9:02 AM 2/11/2022.

Zoom meeting controls: mute/unmute, video on/off, chat, hand raise, share screen, and end call.

Zoom meeting status: 41 participants, chat icon, and screen share icon.



B132 ESWAR KALYAN RE...



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B096 SNEHARSH BELSA...



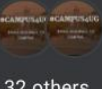
B077 MAHAVIR SVNIT



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32 others



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Final & Non-Final ...
the construction of the table and declare
two machines are not equal.

→ Continue the process of construction of table
for every new pair of the form $E-F$ &
 $NF-NF$ that appears on the input column and
terminate the process whenever no such type of pair
appears in the input column.



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You



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For every new pair of the form $F-F$ or
NF-NF that appears on the input column and
terminate the process whenever no such type of pair
appears in the input column.

→ If the transition table contains all the pairs
of the Final-Final, NonFinal/Nonfinal then the
two machines are equal.



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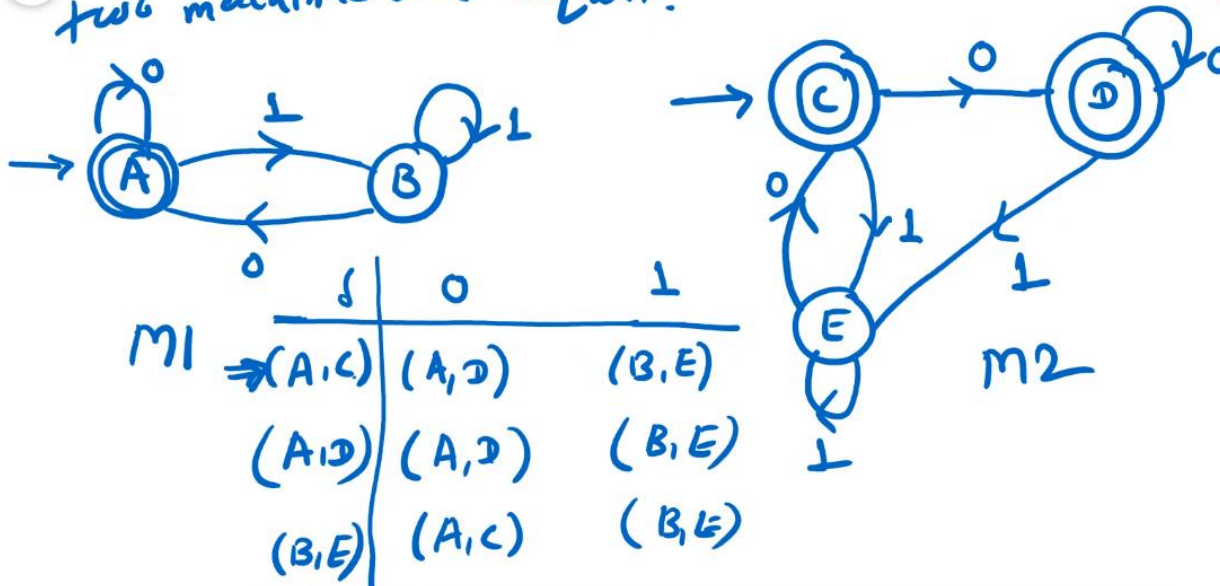


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of the Final-Final, NonFinal NonFinal
two machines are equal.



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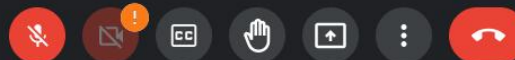
B109 CH ADITHYA SVNIT



33 others



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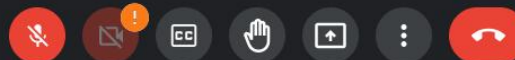
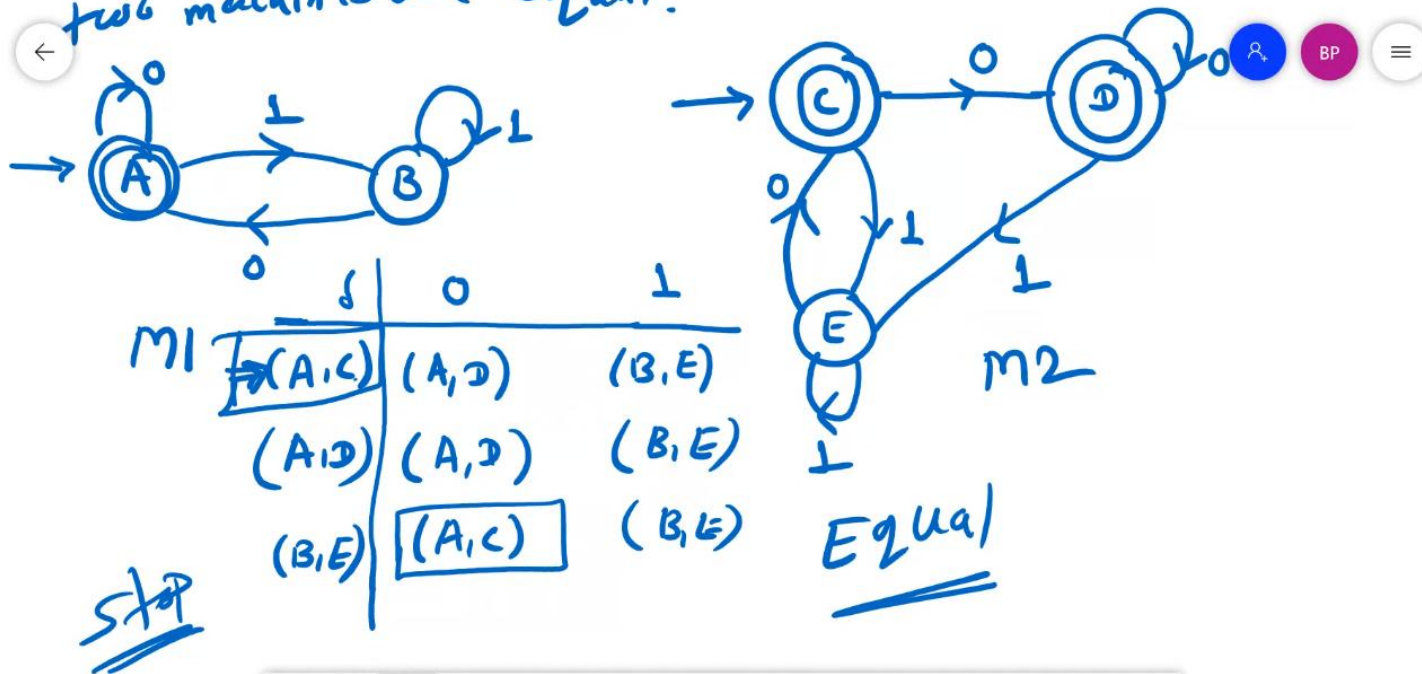




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two machines are equal.



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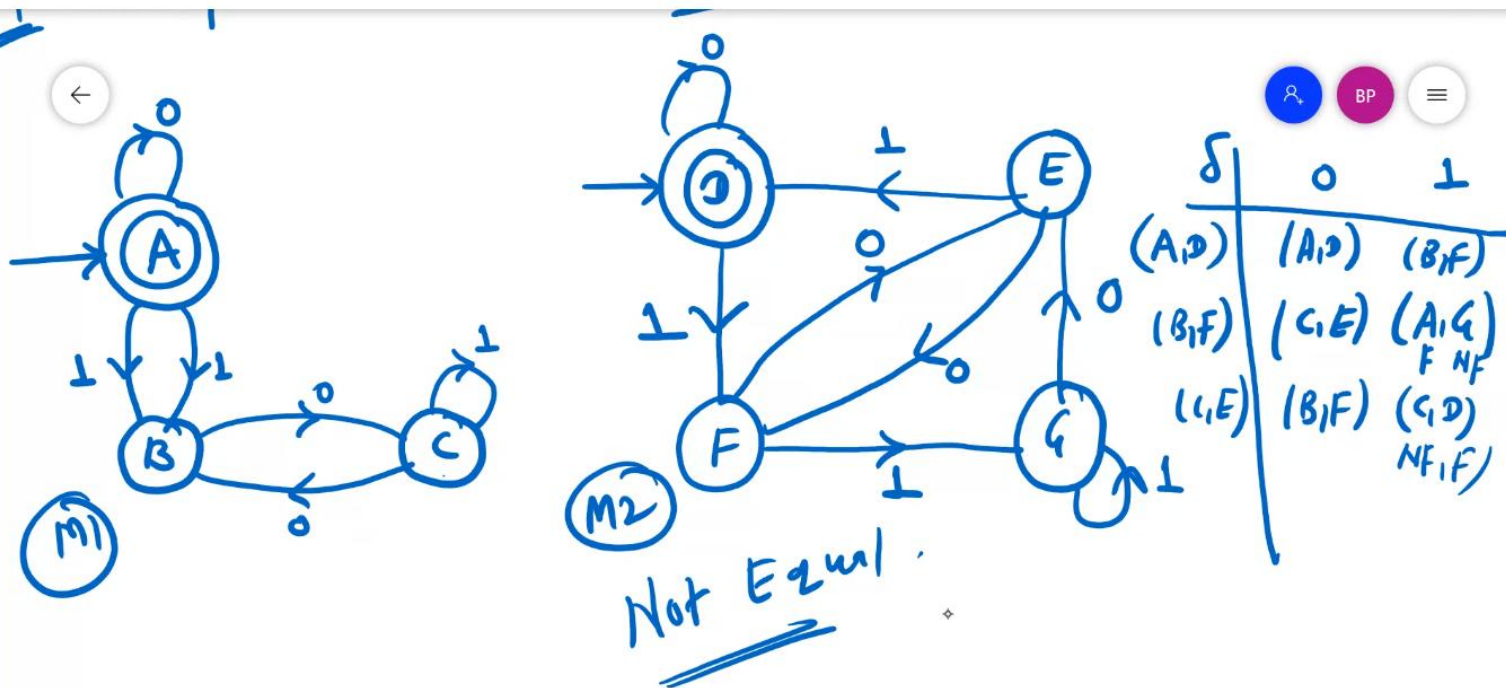
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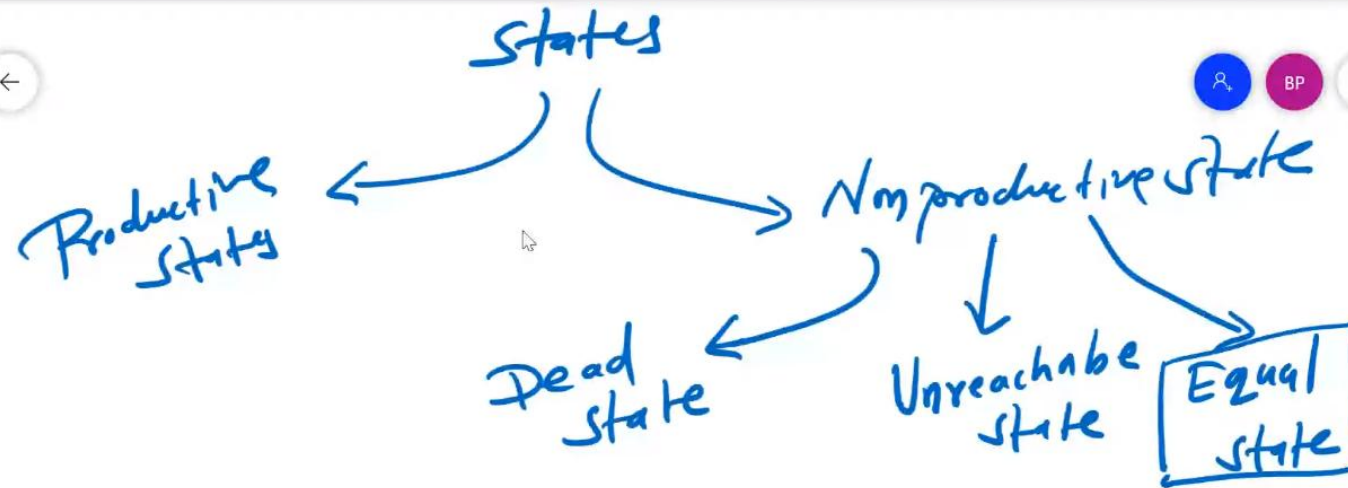


Participant list:

- B132 ESWAR KALYAN RE...
- B123 SHISHIR SVNIT
- Dr. Balu L. Parne SVNIT
- B096 SNEHARSH BELSA...
- B095 AARTI OTARI SVNIT
- B083 Hemnani Mihir Jit...
- B109 CH ADITHYA SVNIT
- 34 others

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9:17 AM
2/11/2022

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Equal states! \Rightarrow
Two states P, Q are said to be equal if both
 $\delta(P, n)$ and $\delta^*(Q, n)$ goes to Final or Non Final
State.



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43



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Equal States \Rightarrow

Two states P, Q are said to be equal if both $\delta(P, n)$ and $\delta^*(Q, n)$ goes to Final or Non Final State.

$$\therefore P = Q \Leftrightarrow \begin{array}{c} \delta(P, n) \quad \delta^*(Q, n) \\ \swarrow \quad \searrow \\ \text{NF} \end{array}$$

Eg



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Equal states are also called as ~~Final~~ states.

Unequal or Distinguishable states \Rightarrow

Two states P, Q are said to be unequal or distinguishable if $\delta(P, n)$ goes to final and $\delta(Q, n)$ goes to non-final or viceversa.

$$P \neq Q \Leftrightarrow \begin{array}{c} \uparrow F \\ \delta(P, n) \\ \downarrow NF \end{array} \quad \& \quad \begin{array}{c} \uparrow NF \\ \delta(Q, n) \\ \downarrow F \end{array}$$



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You

