

CS1026 – Digital Logic Design

Sequential Logic Analysis

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Today's Overview

- 1 What to do?
- 2 What not to do?
- 3 The exam structure
- 4 People to ask

What you should do I

Learn all the theorems and processes:

- No reference sheets given in Exam
- Just noting them down can access marks!

Exam Style

In lots of questions you use assumptions – Always note them down!

What you should do II

We give the gift of labs and tutorials:

- We don't have many different problems
- The exam will contain variations on these

Practice

Make your own questions and use Logisim to check the answer!

- Beware with Quine-McCluskey.. Branch and Bound case.

What to watch out for I

DLD, for some reason, has become two courses in one! However..

- DO NOT ignore the first part of the course
- It might seem easier now but it's worth 50%

What to watch out for II

Thus you really should split your time between the two:

- Double check you know all the basics (Boolean Algebra)
- The K-Map/Simplification stuff overlaps nicely

What to expect in the exam I

Again we have two parts over 2 hours:

- Section A – John's Stuff
- Section B – My stuff

Both equally weighted

- So share your time between the sections

What to expect in the exam II

I recommend doing the sections in order:

- Why?
 - Questions in Section 2 take longer
 - Also it gives you time to ace Section 1.. ;-)

Top tips

- You only answer 2 out 3 questions in each section
- Choose wisely!

Who should I ask? I

E-Mail me: morrisa5@tcd.ie

- I can help with anything in DLD
- Try to keep questions clear
 - So I give a clear answer

Just remember..

It takes time to read and reply to E-Mails

- Ask me early on
- Not on exam day

Who should I ask? II

You also E-mail the TAs or John Waldron

- John Waldron – john.waldron@scss.tcd.ie
- Shane Sheehan – sheehas1@tcd.ie
- Derek Keller – kellehdt@tcd.ie

Be aware however that these guys have other things to do!