

SiT5000 TCXO

```
graph LR; TCXO[SiT5000 TCXO] --> B1[LMK00105 Clk Buffer]; B1 --> B2[LMK00105 Clk Buffer]; B1 --> B3[LMK00105 Clk Buffer]; B2 --> B3; B3 --> Scope[Scope]; B1 --> TRX1[RF TRX]; B2 --> TRX2[RF TRX]; B3 --> TRX3[RF TRX];
```

The diagram illustrates a clock distribution system. It begins with a SiT5000 TCXO (orange box) at the top left. A vertical arrow points down to the first of three LMK00105 Clk Buffer blocks (purple boxes). These three buffers are arranged horizontally, connected by arrows from left to right. Each buffer has a vertical arrow pointing down to an RF TRX block (blue box). Finally, an arrow points from the rightmost buffer to a large green box labeled 'Scope'.

LMK00105
Clk Buffer

RF TRX

LMK00105
Clk Buffer

RF TRX

LMK00105
Clk Buffer

RF TRX

Scope