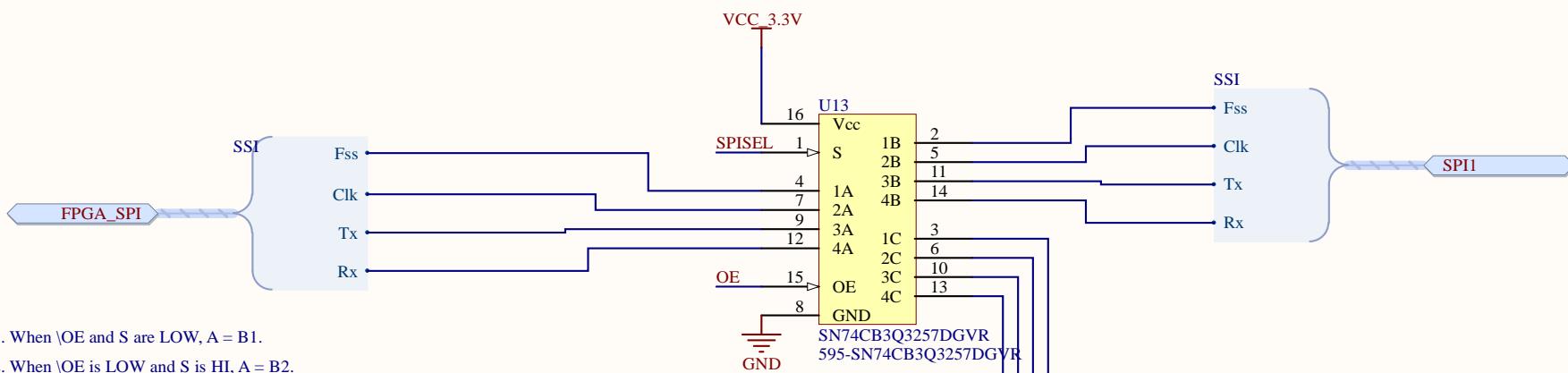


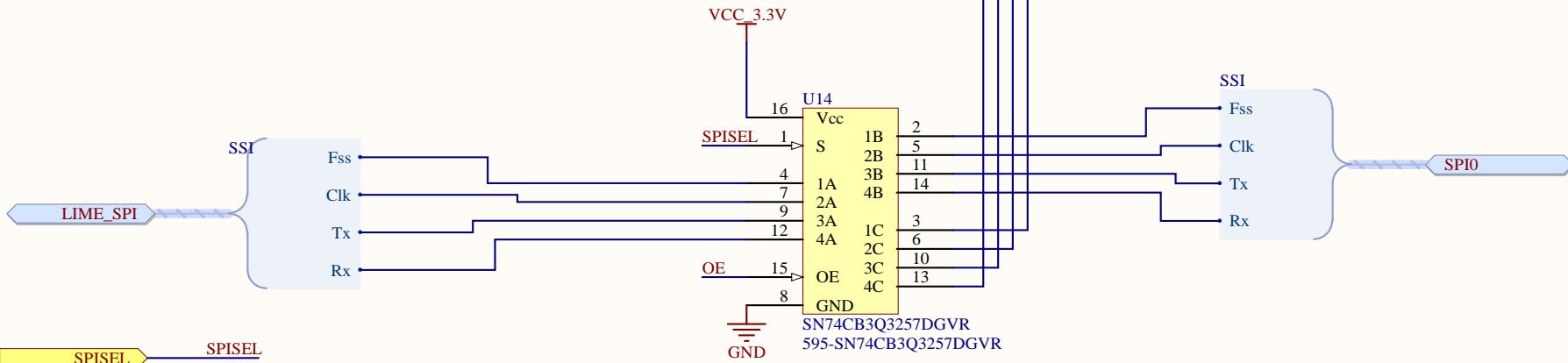
A



B

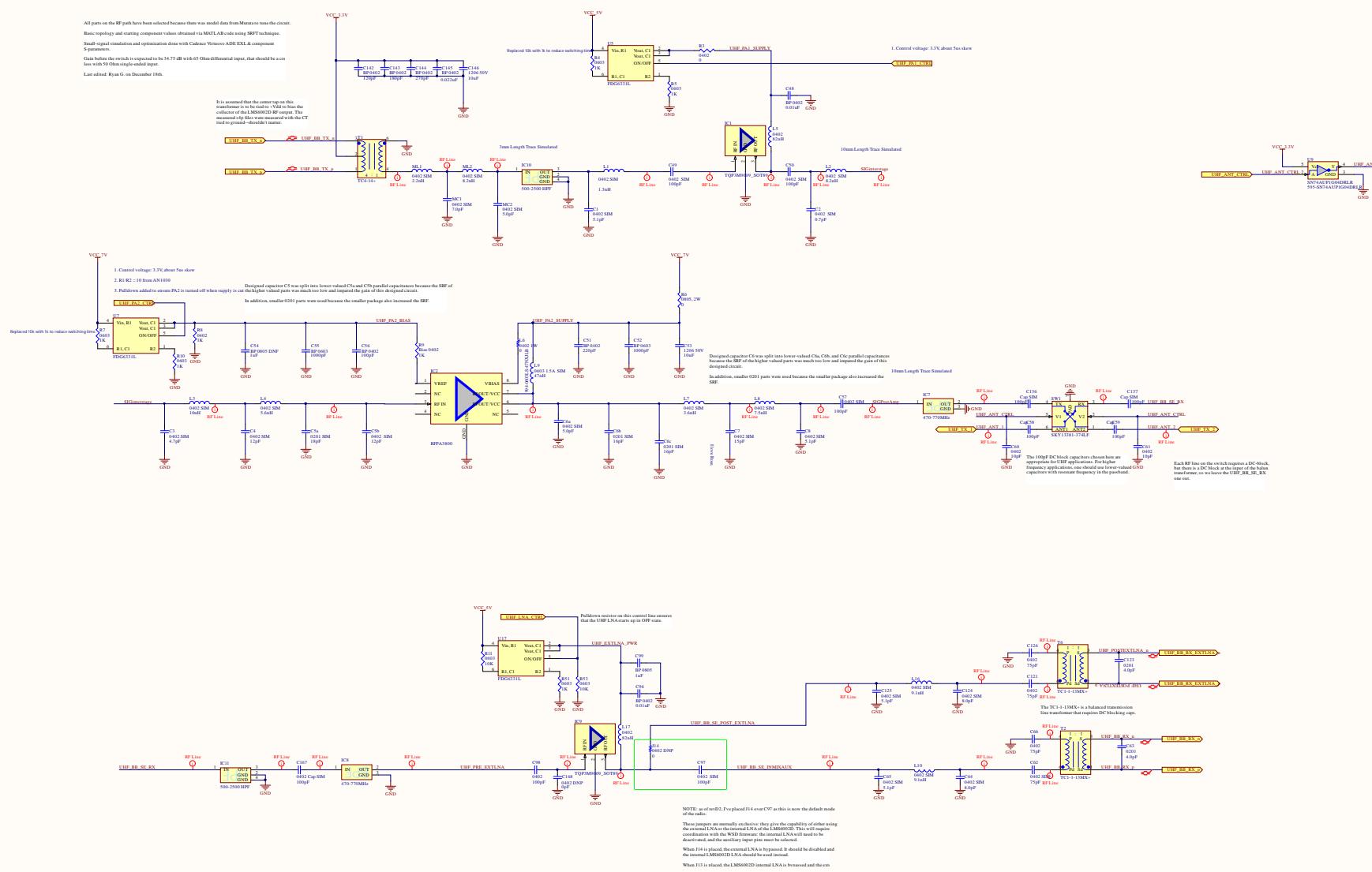
ENABLE → OE

C

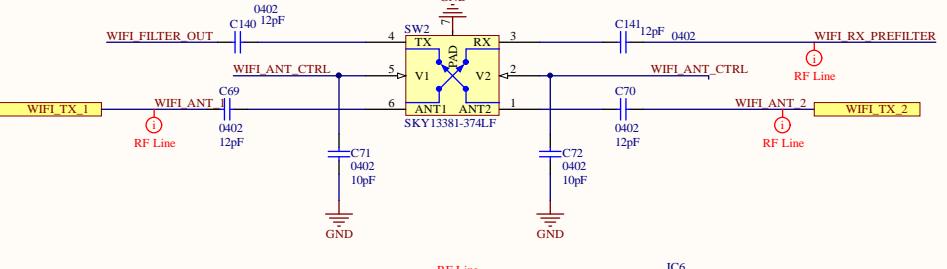
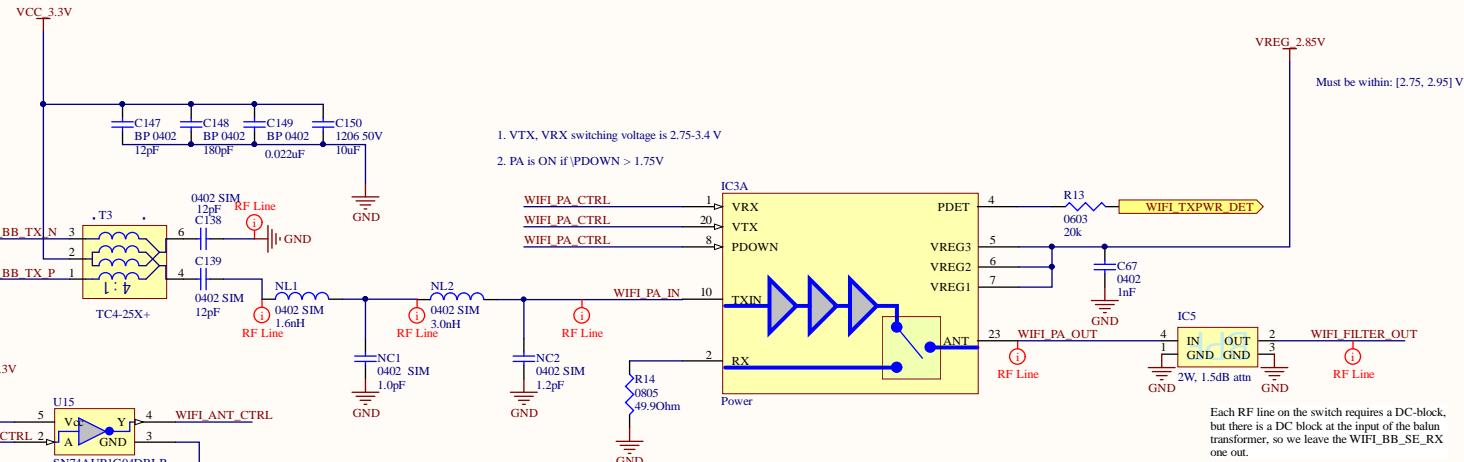


D

Serial Interface MUXes		
Size A	Number 6	Revision
Date:	11/16/2018	Sheet of
File:	D:\Dropbox\..\Whitespace_Daughtercard	Drawn & Doc



JFH 470-800 MHz Matching, PA Chain, TX/RX Switching  
 Title: 10 of 7 Revision: 15  
 Date: 11/16/2018 Sheet: 15 of 15  
 File: JF-Dropout\_Whitespace\_Diagram\_v1.0.dwg

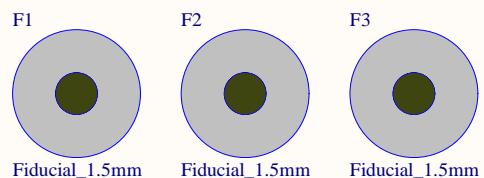
**Change Log**

- Swapped out inverting buffers for actual inverters
- Updated transformer schematic, and added proper DC-block caps
- Added additional DC-block caps to RF switch

WiFi 2.4GHz Matching, PA Chain, TX/RX Switching		
Size	Number	Revision
A3	10 of 8	
Date:	11/16/2018	Sheet of
File:	D:\Dropbox\Whitespace Daughtercard\	DiffSchematicDoc

A

Heatsink  
Tap & Screw  
Heatsink 3D Model  
HeatSinkUSA



A

B

B

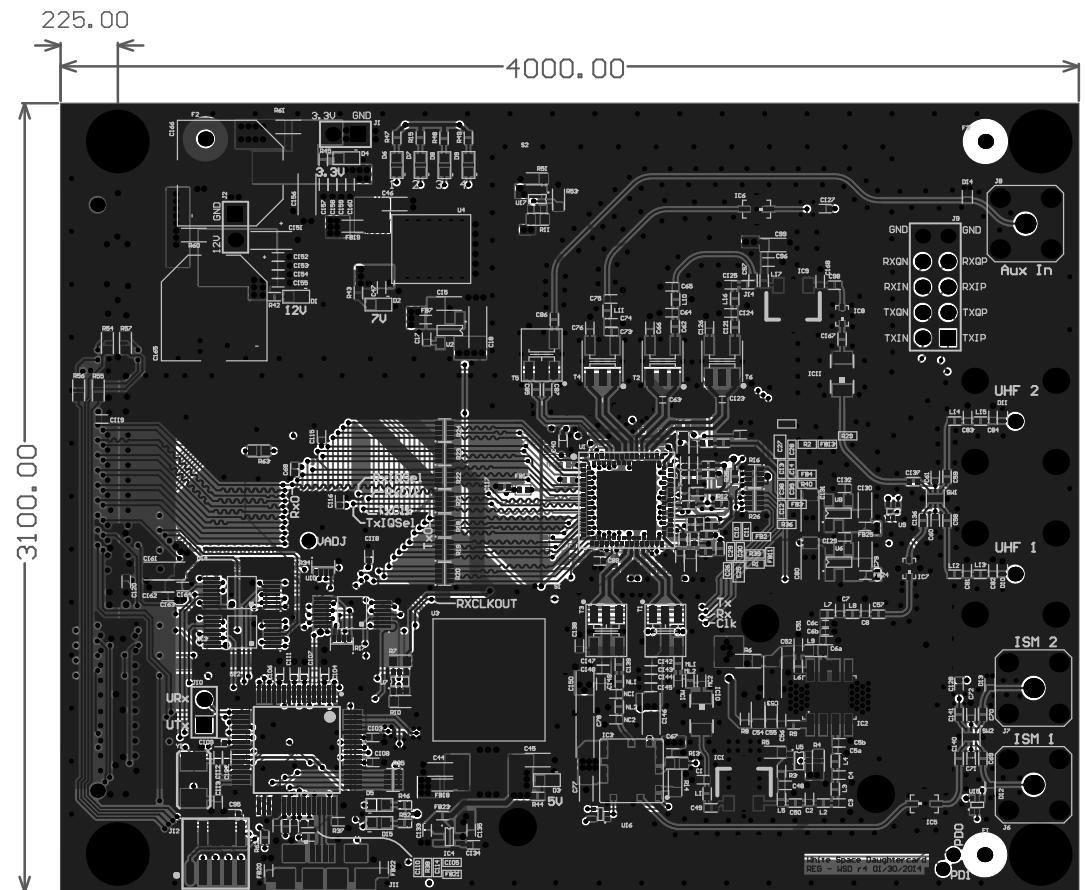
C

C

D

D

Misc. Project Notes		
1.0	10 of 10	Revision
Date:	11/16/2018	Sheet of
File:	D:\Dropbox\...\Whitespace_Daughtercard	089819.Doc



```
Layer Stack Up Detail for whitespace_DaughterCard_PcbDoc
Layer
Name
Top Layer
Ground Plane
MidLayer1
MidLayer2
Power Plane
Bottom Layer
```

===== WSD Rev 4 =====

- Isola FR402, Dk @ 1 MHz = 4.6
  - Controlled Dielectric: top & bottom signal planes are critical
  - See WSD\_r4\_Stackup.xls file for PCB Stackup
  - Lead-Free Solder ONLY
  - Contact Ryan Guerra: (315) 857-7693, ryan@volowireless.com
  - 1oz Copper Top & Bottom
  - Other layers: 0.5oz copper
  - OK to panel