

# RYAN GEBHARDT

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## EDUCATION

### The Pennsylvania State University

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Fall 2014-Present      Expected graduation date - May 2018  
Major GPA: 3.20  
• Dean's List - Fall 2017

## EXPERIENCE

### Project Vive, LLC.

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Summer 2017      *Electrical Engineering Intern*

- Designed an embedded system of a position-sensing watch from the ground up, to a working prototype.
- Collaborated with a cross-functional development team to design and manufacture new products.
- Recommended, selected and developed a new hardware platform for a fast-to-market, easily accessible device.

### Penn State Semiconductor Lab

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Sept 2016 - May 2017      *Research Assistant*

- Investigated the limits of modern semiconductor devices.
- Utilized Electron Paramagnetic Resonance spectrometers to discover band gap traps, and other imperfections, in semiconductor materials.
- Collaborated with research and development team members in a laboratory environment.

## SKILLS

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|--|-----------------------------------|
| • Embedded System Design                     | • Analog Amplifier Design         |
| • C/C++, Python, Matlab                      | • Digital Logic Circuit Design    |
| • NI Labview, CLAD certified                 | • Multisim, Ultiboard, Eagle      |
| • Electrical Troubleshooting                 | CAD                               |
| • Excellent Verbal and Written Communication | • Cadence schematic/layout design |

## STUDENT ORGANIZATIONS

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- Institute For Electrical and Electronic Engineers (IEEE)
- Engineers For A Sustainable World (ESW)
- 3D Printing Club