

# RYAN GEBHARDT

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## EDUCATION

### The Pennsylvania State University

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Fall 2014-Present Expected graduation date - May 2018

Major GPA: 3.20

- Dean's List - Fall 2017

## EXPERIENCE

### Project Vive, LLC.

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Summer 2017 *Electrical Engineering Intern*

- Designed an embedded system of a position-sensing watch from the ground up, to a working prototype.
- Collaborated with a cross-functional development team to design and manufacture new products.
- Recommended, selected and developed a new hardware platform for a fast-to-market, easily accessible device.

### Penn State Semiconductor Lab

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Sept 2016 - May 2017 *Research Assistant*

- Investigated the limits of modern semiconductor devices.
- Utilized Electron Paramagnetic Resonance spectrometers to discover band gap traps, and other imperfections, in semiconductor materials.
- Collaborated with research and development team members in a laboratory environment.

## PROJECTS

- 16-bit Carry Bypass Adder
- Audio Amplifier Circuit/PCB
- Position-Sensing Gyro-Watch
- Op Amp Circuit Design
- Embedded Signal Identifier
- Digital Theremin
- Autonomous Environment Sensing vehicle control

## SKILLS

- Embedded System Design
- C/C++, Python, Matlab
- NI Labview, CLAD certified
- Electrical Troubleshooting
- Excellent Verbal and Written Communication
- PCB Layout and Design
- Analog Amplifier Design
- Digital Logic Circuit Design
- Multisim, Ultiboard, Eagle CAD
- Cadence schematic/layout design
- Spectre/Pspice/Hspice