ASIC MODELING AND SYNTHESIS
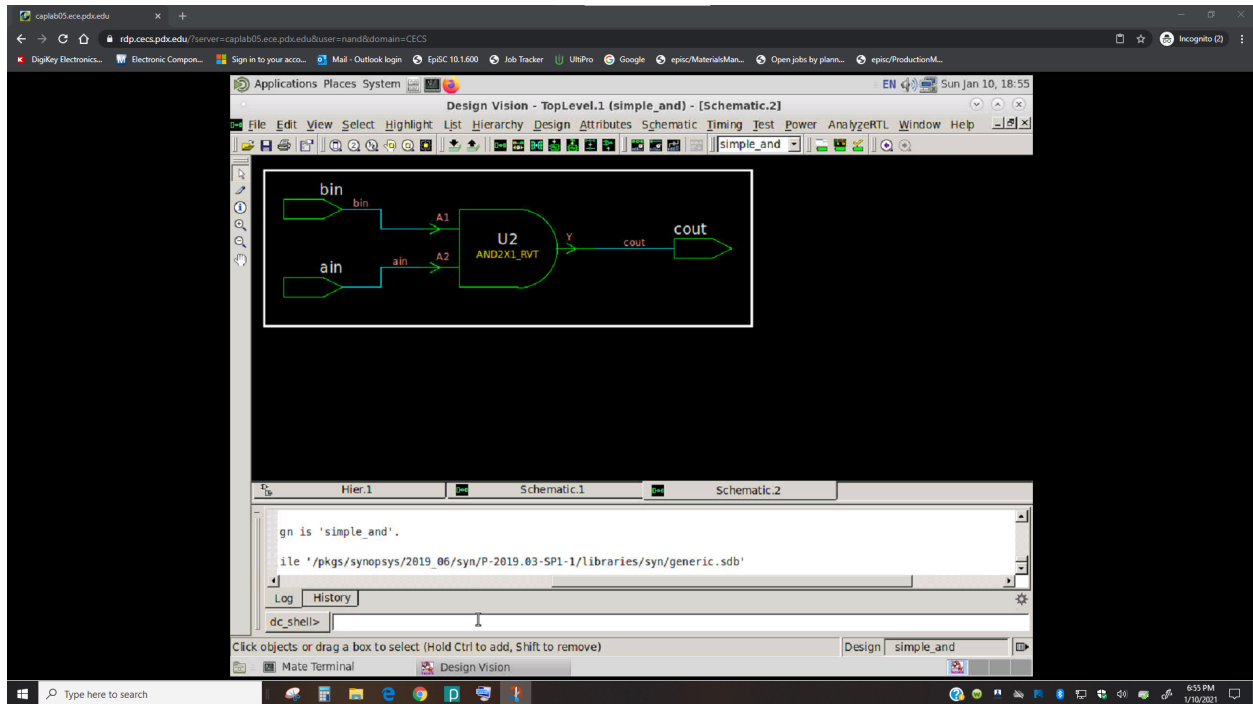# ECE - 4/581
Winter 2021

HW_Lab1

Ryan Nand
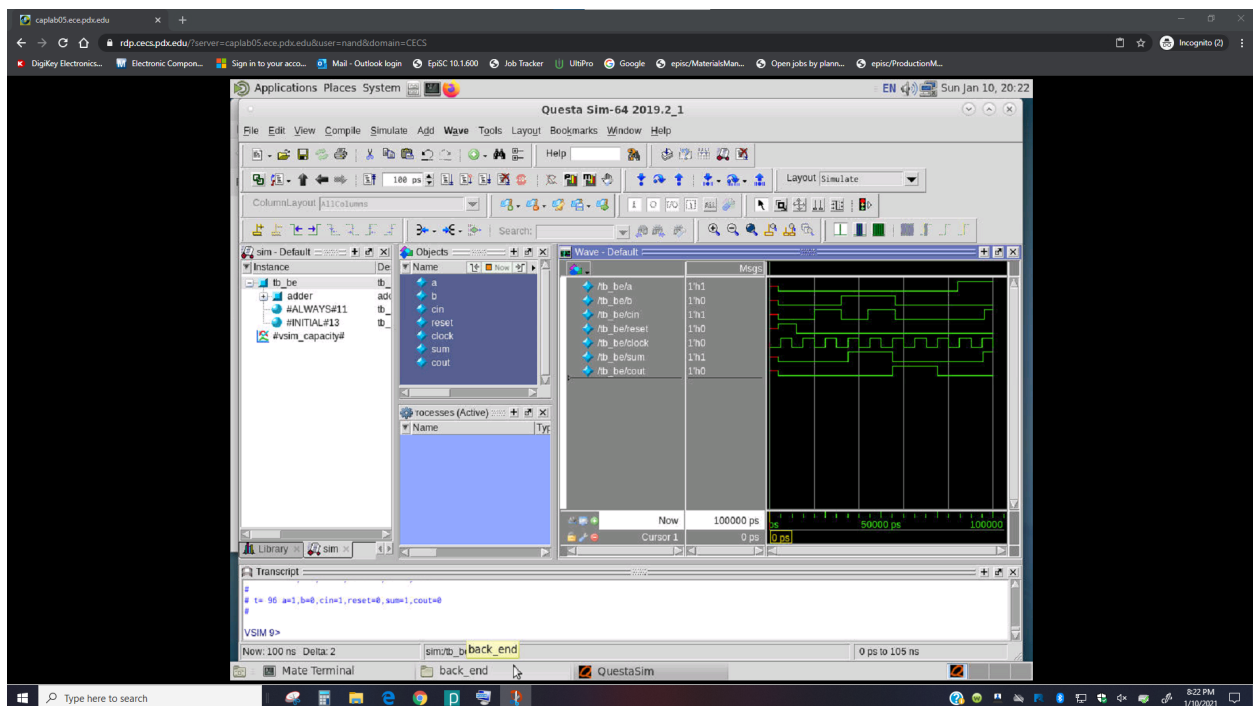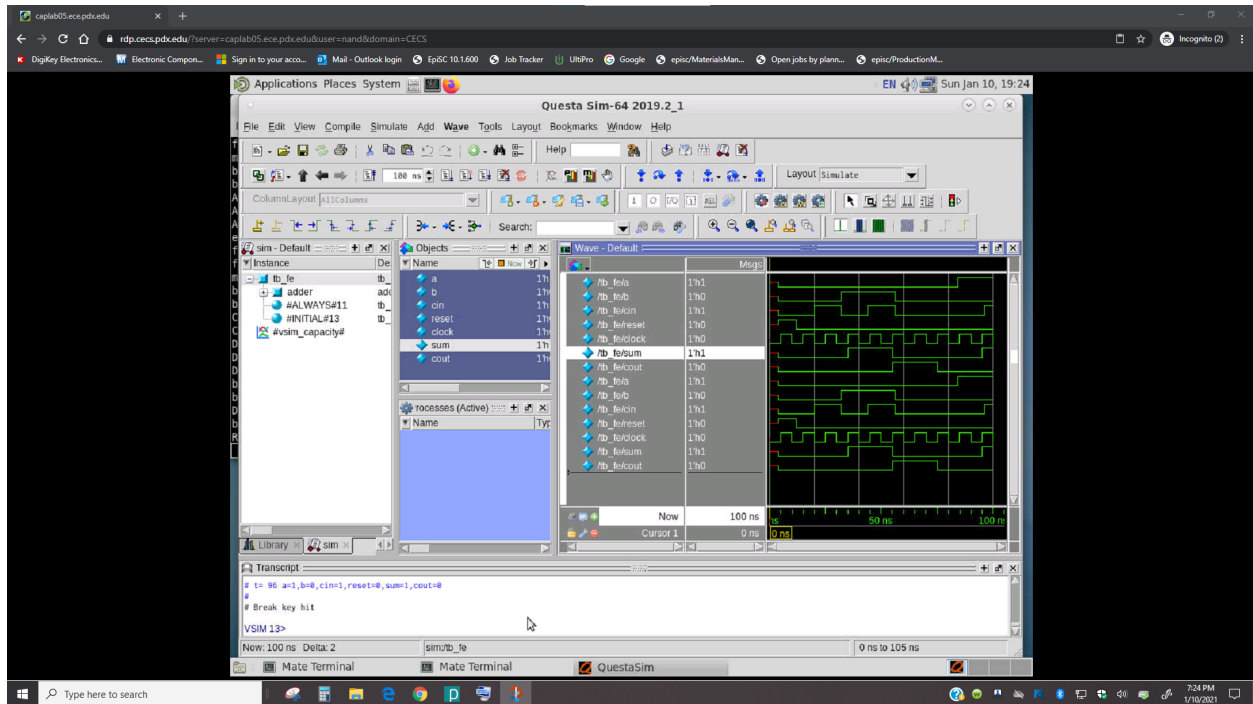1/10/2021

Synthesis using Design Compiler

Questa Sim FE and BE (I accidentally added two FE waveforms in one plot)

Cadence Conformal

Applications Places System

EN Sun Jan 10, 21:10

Computer

nand's Home

Trash

Encounter(R) Conformal(R) Logic Equivalence Checking

File Setup Report Run Tools Custom Preferences Window Help

cadence

Setup   LEC

Golden   Revised

adder   adder
  7 primitives   3 library cells
  dff_a(dff)   dff_a(dff_0)
  dff_b(dff)   dff_b(dff_4)
  dff_cin(dff)   dff_cin(dff_3)
  dff_cout(dff)   dff_cout(dff_2)
  dff_sum(dff)   dff_sum(dff_1)

```
Mapped points: SYSTEM class
-----------------------------------------------
Mapped points      PI    PO    DFF     Total
-----------------------------------------------
Golden             5     2     5       12
-----------------------------------------------
Revised            5     2     5       12
-----------------------------------------------

LEC> add compared points -all
// Command: add compared points -all
// 7 compared points added to compare list
LEC> compare
// Command: compare
-----------------------------------------------
Compared points          PO    DFF     Total
-----------------------------------------------
Equivalent               2     5       7
-----------------------------------------------

LEC>
```

Compare done!   100% completed

File Edit

// Check
bash-4.2$
bash-4.2$

Copyri

This prog
Cadence D
in a lice

// Warnin
m.

// Check
// Check
// Check
// Check
// Check
// Check
// Check

Mate Terminal   Encounter(R) Conform...

Type here to search

9:10 PM
1/10/2021