```
Last day of multiprocessing
./ gnitor example
 recall blackManbredSet (marriagedSignals); - tell OS "I will hardle these particular signals using SIGWAIT
state void reop Child Processes (set < pld-6> le processes) {
   while (true) {
        pid-t pid = wantpid (-1, NULL, WNOHANG);
        if (pld <= 0) break;
       processes erase (pid);
static void stop Playing (const set cpid-t>& processes) &
    for (Int pid processes) Icil (pid, SIGKILL);
    set Alam (0); // disable all future-scheduled alarms
synal blocks preserved over both fork & execup boundaries!! Remember to unblack Maniford Set (monitared synals);
Virtual Menony
   How nevery (RAM) is organized away of contiguous lytes
       physical hardware
       physical addressing: addres generated & developenced by the OS mapped to data at same address (the actual one)
        virtual addressing: to allow true multiprocessing
            all processes operate as if they own all of noin memory
            CPM+05 treat processes addresses as virtual, and translate to a physical addr before accessing RAM
                address mapping has to happen win the same clock cycle - super fast.
                     translation has to happen using bitwise and/or/left shift/right shift
            support for virtual memory has similar schene to file systems (slides are very informative here)
                uses "page" incheat of blocks - multiples of 4096 (meaning everything a multiple of 0x000 (ends of this))
                mapping scheme is simpler - OS stores page table of violation -> physical page mappings
   when the hard drive as physical memory
        & man nemos as a kind of cache
     be executables stored on disk & loaded to now memory when needed
     Lo OS will exist pages to the hard drive if they became inauther (in swap files)
```