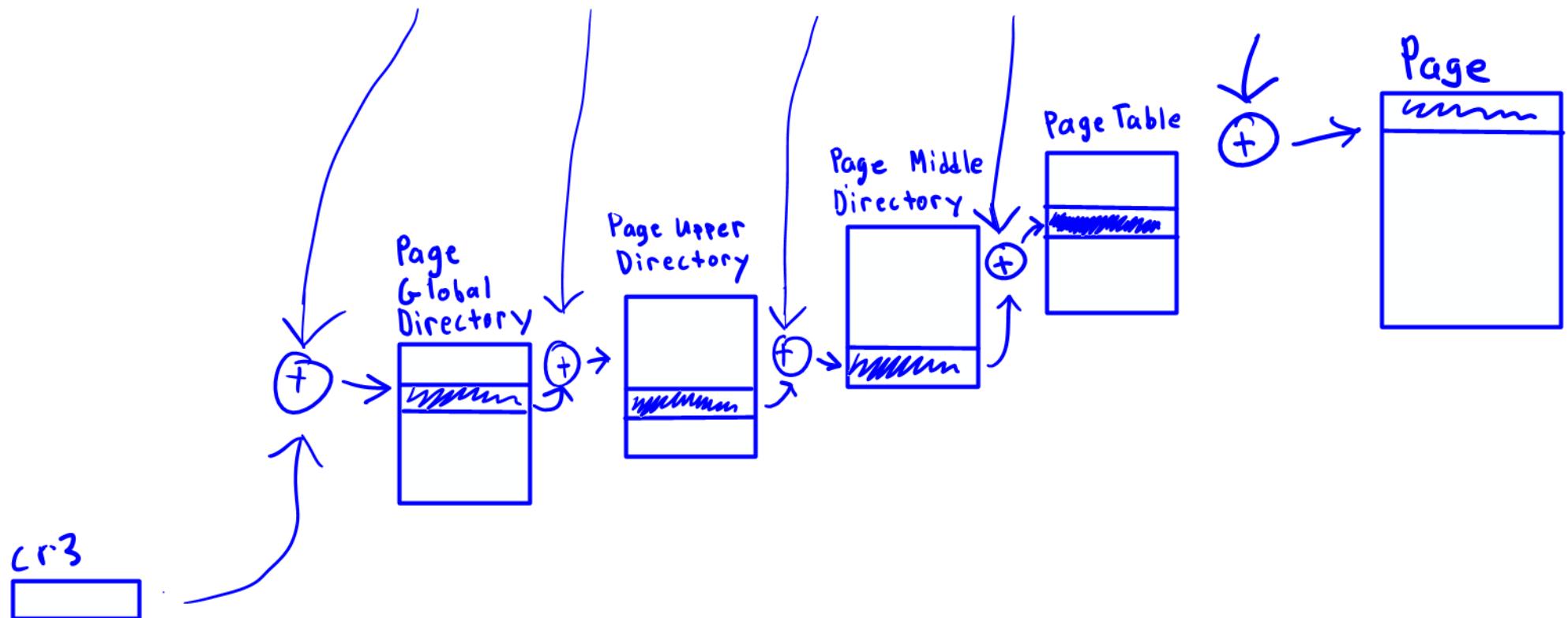
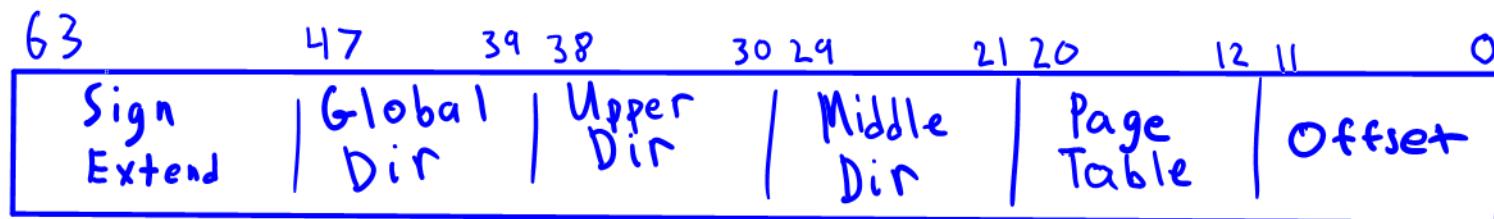


Linear Address ($\times 86-64$)



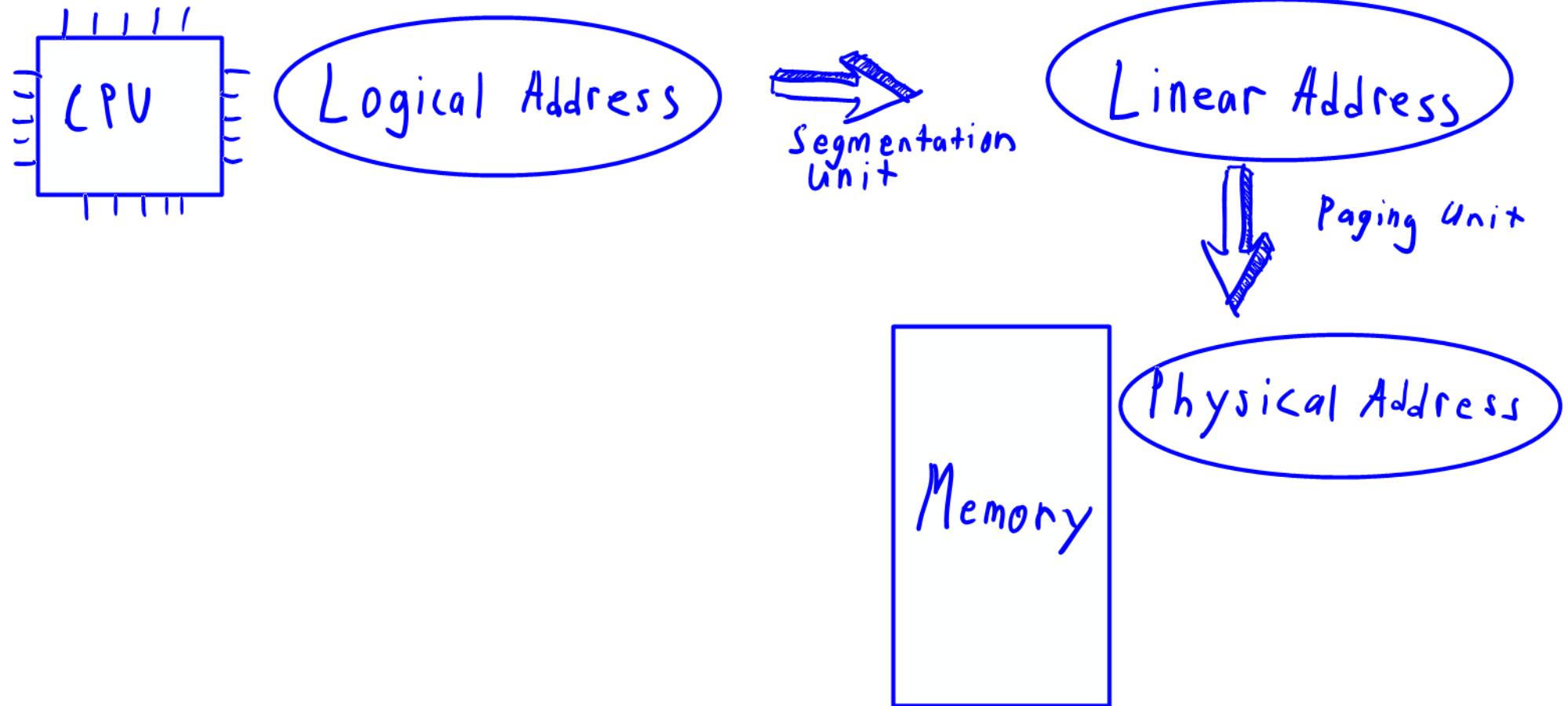
A Process's Linear
Address Space (Virtual Memory)

Page 0	Cached
Page 1	Unallocated
	Uncached
	Cached
	Cached
	Uncached
	Unallocated
	Uncached
	Cached
Page N-1	Uncached

Physical Pages (PP)
Cached in DRAM

PP 0
PP 1
PP M-1

Segment	Base	Limit	DPL
User Code	0x00000000	0xfffff	3
User Data	0x00000000	0xfffff	3
Kernel Code	0x00000000	0xfffff	0
Kernel Data	0x00000000	0xfffff	0
//			



Linear Address
Space

