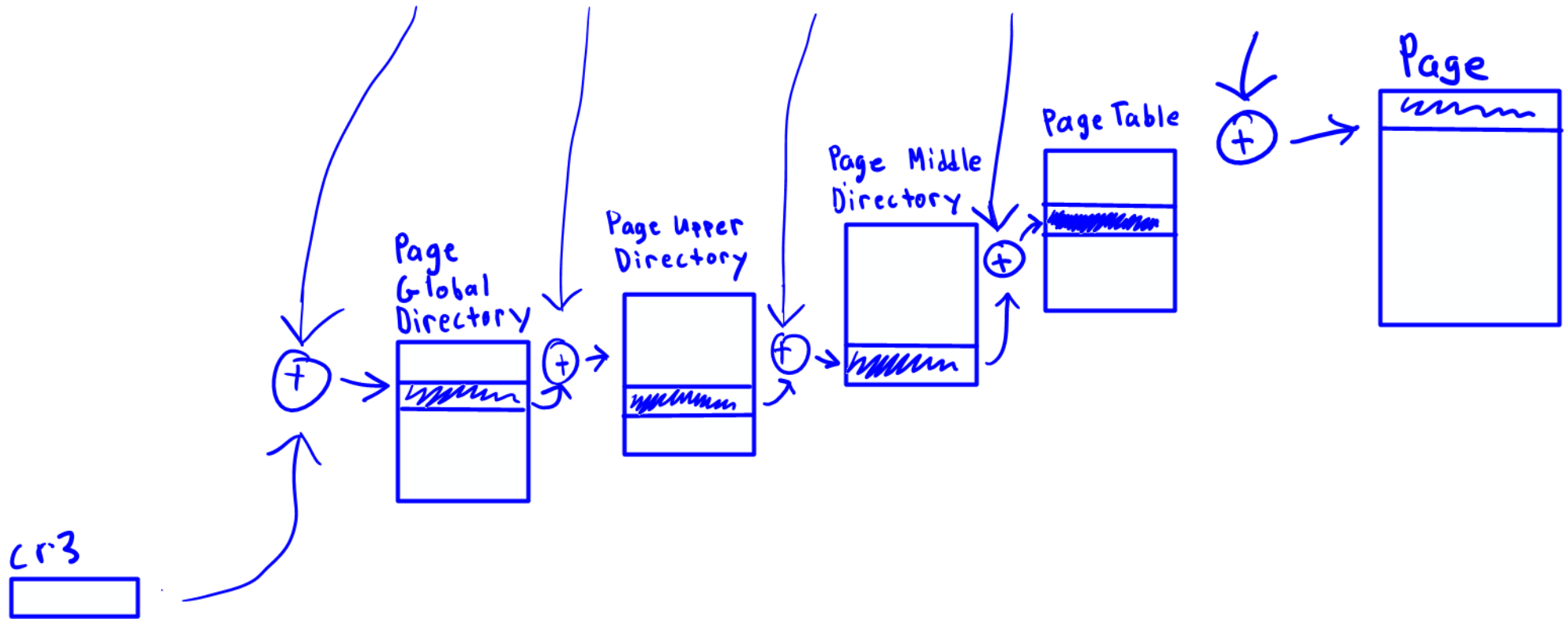
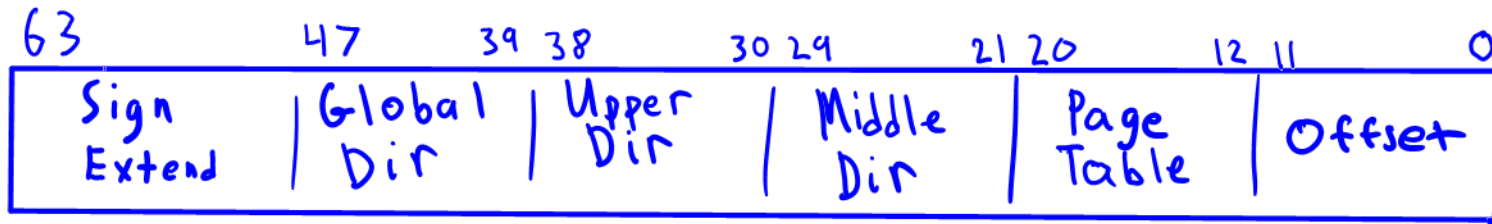
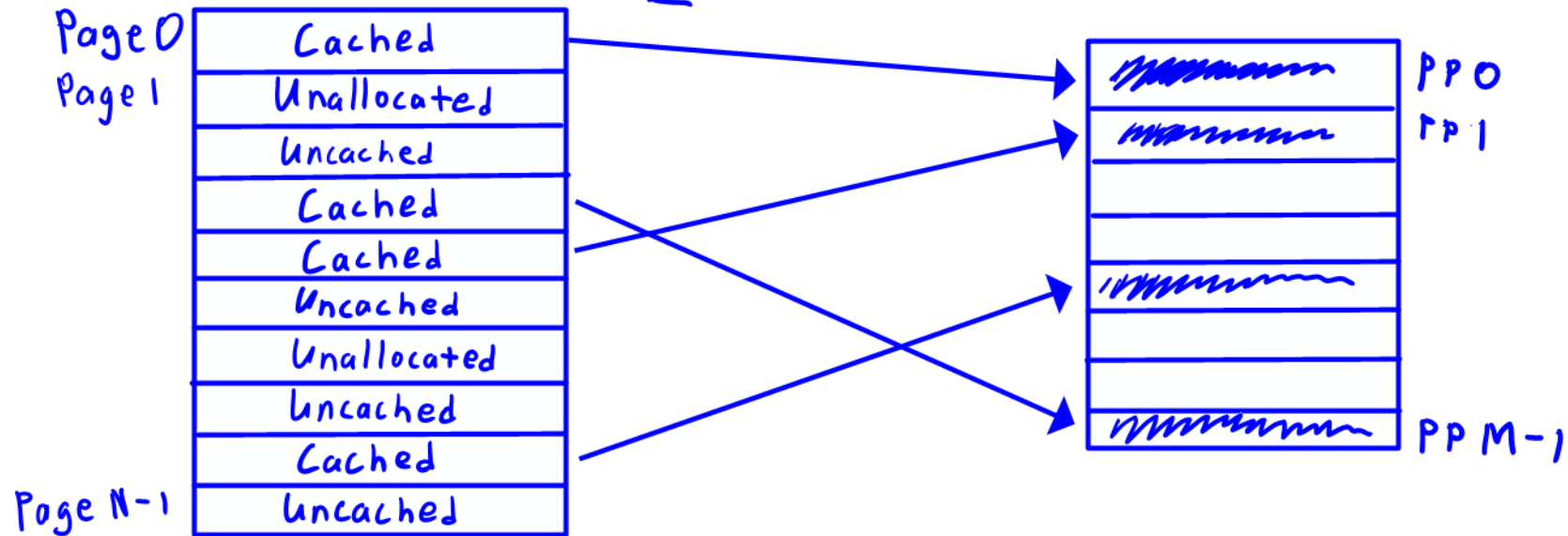


# Linear Address (x86-64)



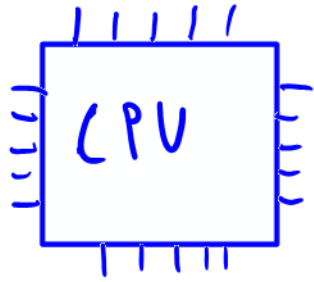
A Process's Linear  
Address Space (Virtual Memory)

Physical Pages (PP)  
Cached in DRAM



Segment	Base	Limit	DPL
User Code	0x00000000	0xffffffff	3
User Data	0x00000000	0xffffffff	3
Kernel Code	0x00000000	0xffffffff	0
Kernel Data	0x00000000	0xffffffff	0

//



Logical Address

Segmentation Unit

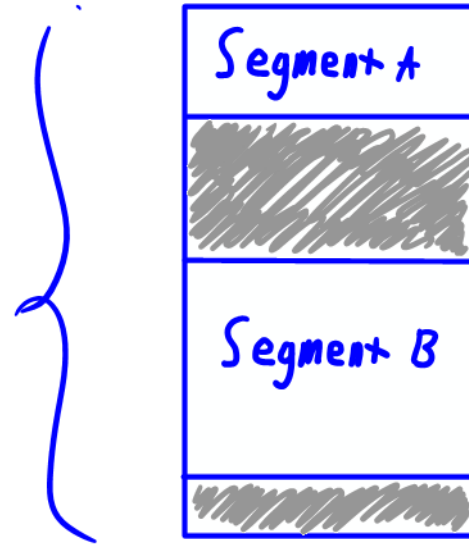
Linear Address

Paging Unit

Memory

Physical Address

Linear Address  
Space





Segment Selector

Logical Address