

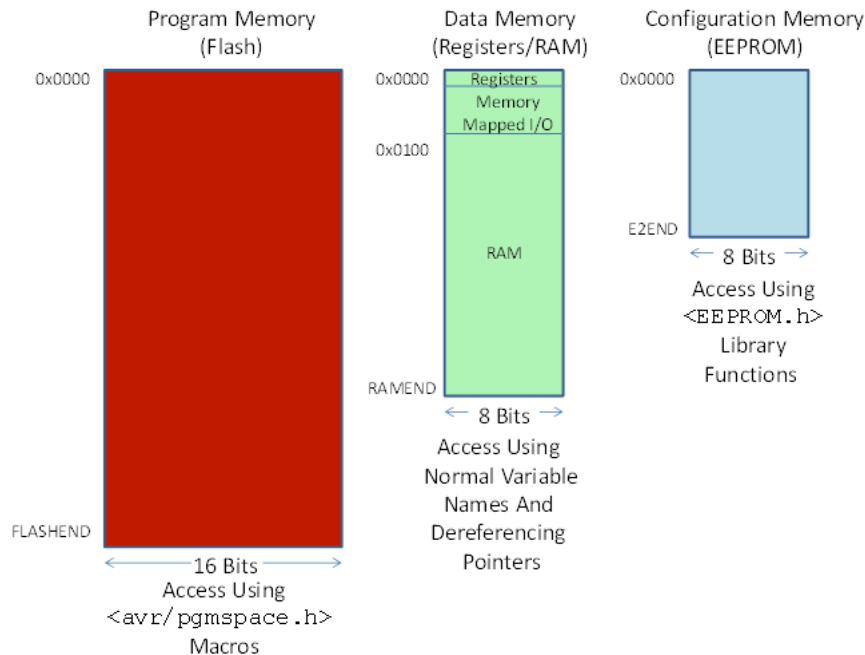
CS 341 – Lab 4

Memory Mapped I/O

In this lab, you will learn about memory mapped I/O on the Arduino UNO computer. The starter code can be found on GitHub under Lab 4.

Some Background Info

The ATMEGA processor on the Arduino UNO board implements a concept called memory mapped I/O. Instead of using a separate I/O address space and special assembly language instructions such as `in` and `out` to access I/O device registers like the Intel architecture does, the I/O device registers are mapped into memory space. In the ATMEGA Harvard architecture, I/O devices are mapped into the data memory space – not the program memory space. The 64 normal I/O device registers are located in data memory space from location 0x20 to 0x5f. There is also room reserved in the data memory space for 160 extended I/O registers from location 0x60 to 0xff. RAM starts at 0x100.



For each I/O pin on the Arduino (you can find pin out of the ATMEGA368 below), there is a mode bit in a port register that controls whether it is configured as an input or output. If the pin is configured as an input pin, a HIGH level on the pin will be read as a 1 and a LOW level will be read as a 0. If the pin is configured as an output pin, writing a 1 will generate a HIGH level and a 0 for a LOW level.

Arduino function						Arduino function
reset	(PCINT14/RESET)	PC6	1	28	PC5 (ADC5/SCL/PCINT13)	analog input 5
digital pin 0 (RX)	(PCINT16/RXD)	PD0	2	27	PC4 (ADC4/SDA/PCINT12)	analog input 4
digital pin 1 (TX)	(PCINT17/TXD)	PD1	3	26	PC3 (ADC3/PCINT11)	analog input 3
digital pin 2	(PCINT18/INT0)	PD2	4	25	PC2 (ADC2/PCINT10)	analog input 2
digital pin 3 (PWM)	(PCINT19/OC2B/INT1)	PD3	5	24	PC1 (ADC1/PCINT9)	analog input 1
digital pin 4	(PCINT20/XCK/T0)	PD4	6	23	PC0 (ADC0/PCINT8)	analog input 0
VCC	VCC		7	22	GND	GND
GND	GND		8	21	AREF	analog reference
crystal	(PCINT6/XTAL1/TOSC1)	PB6	9	20	AVCC	VCC
crystal	(PCINT7/XTAL2/TOSC2)	PB7	10	19	PB5 (SCK/PCINT5)	digital pin 13
digital pin 5 (PWM)	(PCINT21/OC0B/T1)	PD5	11	18	PB4 (MISO/PCINT4)	digital pin 12
digital pin 6 (PWM)	(PCINT22/OC0A/AIN0)	PD6	12	17	PB3 (MOSI/OC2A/PCINT3)	digital pin 11 (PWM)
digital pin 7	(PCINT23/AIN1)	PD7	13	16	PB2 (SS/OC1B/PCINT2)	digital pin 10 (PWM)
digital pin 8	(PCINT0/CLKO/ICP1)	PB0	14	15	PB1 (OC1A/PCINT1)	digital pin 9 (PWM)

```
int led = 13;          /* define the I/O pin # */
pinMode(led, OUTPUT); /* set the mode for the pin to be output */
digitalWrite(led, HIGH); /* Output a 1 */
```

If we want to read an input pin's value, we dereference the pointer to read the byte, and use a bitwise operation to mask the appropriate bit.

Program the I/O pins

Study those three versions of the state of data memory for the 16 locations designated above. See if you can determine which bits were changed by the library functions. In particular, find the address and data bit for the output state of pin 13. Verify your result with the Standard IO Registers map shown in <https://ucexperiment.wordpress.com/2016/03/11/arduino-inline-assembly-tutorial-5-2/>. You need to add 0x20 to the Standard IO Registers map shown below to get the right memory address.

Standard IO Registers (0-64):

1	Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	
2	...									
3	0x03	PINB	PINB7	PINB6	PINB5	PINB4	PINB3	PINB2	PINB1	P
4	0x04	DDRB	DDB7	DDB6	DDB5	DDB4	DDB3	DDB2	DDB1	D
5	0x05	PORTB	PORTB7	PORTB6	PORTB5	PORTB4	PORTB3	PORTB2	PORTB1	P
6	0x06	PINC	x	PINC6	PINC5	PINC4	PINC3	PINC2	PINC1	P
7	0x07	DDRC	x	DDC6	DDC5	DDC4	DDC3	DDC2	DDC1	D
8	0x08	PORTC	x	PORTC6	PORTC5	PORTC4	PORTC3	PORTC2	PORTC1	P
9	0x09	PIND	PIND7	PIND6	PIND5	PIND4	PIND3	PIND2	PIND1	P
10	0x0A	DDRD	DDD7	DDD6	DDD5	DDD4	DDD3	DDD2	DDD1	D
11	0x0B	PORTD	PORTD7	PORTD6	PORTD5	PORTD4	PORTD3	PORTD2	PORTD1	P
12	...									

Be sure to include the address in your report.

Now, add to your sketch some code in the loop function to setup a pointer to the location in memory you determined above. Dereference the pointer to read the byte, flip the state of the appropriate bit with a bit-wise exclusive-or using the mask for the bit that you determined above, dereference the pointer again to write the byte, and delay for about 1 second (1000 milliseconds). If you have done it correctly, the LED on the board should be flashing on and off at the rate you set with the delay call.

If you have time, figure out the address and bit mask for other digital output pins numbered 2 through 12. Demonstrate with an LED and resistor on your simulator (like in Lab 1) that you can use memory mapped I/O to flash an LED attached to one of those pins.

Write your lab report to explain what you did, how you did it, and what you learned about interfacing hardware to a microprocessor and its software (the “sketch”).