Question #1

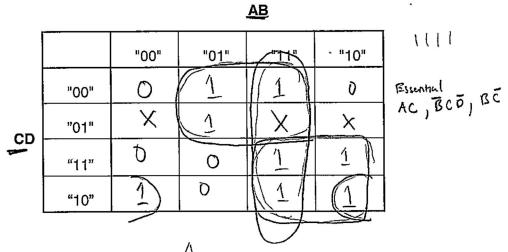
Consider the Boolean function defined by the truth table below where A, B, C, and D are inputs, and Y is the sole output.

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	1	0	0	1	-X	٩
	1	0	1	.0	1	10
	1	0	1	1	• 1	11
	1	1	0	0	ı	12
	1	1	0	1	X	15
	1	1	1	0	1.	14
	1	1	1	1	1	15

(a) Complete the following statements

$$\neg Y = \sum m(2,4,5,-\omega,11,12,14,15)$$

(b) Complete the Karnaugh, Map shown below for ~Y, circle the prime implicants.



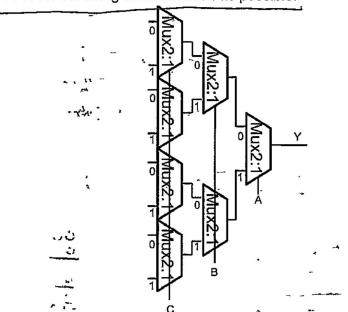
How many prime implicants are there? _

(c) Write the Boolean (sum-of-product) expression of just the essential prime implicants of (b) (if any).

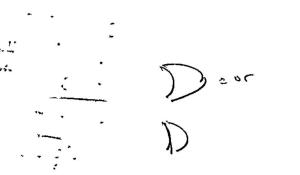
EssentialPrimeImplicants =
$$(A \land C) \lor (\overline{B} \land C \land \overline{D}) \lor (B \land \overline{C})$$

(d) Implement the function \dot{Y} (not ~Y) using a tree of 2 input multiplexers as shown.

The select signals are A, B, and C. The top input of the multiplexer is selected when the select-signal=0 and vice versa. Write the desired inputs on the figure below. You may use D or ~D as input but avoid using them as much as possible.



(e) Write Boolean expression for Y as a *minimal* product-of-sum. Then implement by using only NOR gates (each gate can have multiple inputs). Use the minimum number of NORs with fewest # of total inputs (minimize literals and terms). You may assume true and complement inputs are available.



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$$Y = \neg(\neg a \land d) \lor (\neg e \land \neg(c \land b))$$

(a) For the above Boolean function, convert the above expression into a minimal product-ofsum. = $(\alpha \lor \overline{\lambda}) \lor (\overline{\epsilon} \land (\overline{\epsilon} \lor \overline{b}))$

(b) If you need to represent the equation in (a) in a Fully **Conjunctive** Normal Form, how many terms, and write the expression.

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Number of sum terms: _	\$ \	

Y = (AVBVCVDVE) N (AVBVCVDVE)	9
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(A v Br c v D v E)	

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2×2×25 132 15

5 K5 2 25 = 12.5

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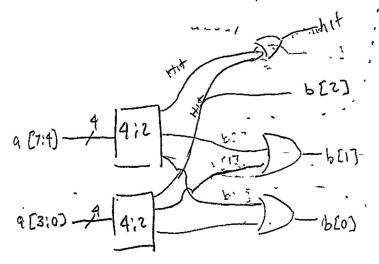
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Question #4

A 4-to-2 encoder (from one-hot to binary) is shown as a block below. The inputs are 4 one-hot inputs, a[3:0]. The output are 2 binary bits, b[1:0], indicating the position of the "hot" input bit. If the input has a hot bit, then hit=1'b1, otherwise, when all inputs are 0's, then hit=1'b0.



(a) Use instances of this 4-to-2 encoder block to design an 8-to-3 encoder. On-hot inputs are a[7:0], binary outputs are b[2:0], and a hit indicator. You may use additional OR, AND, or Inverters as needed but use as few as possible.

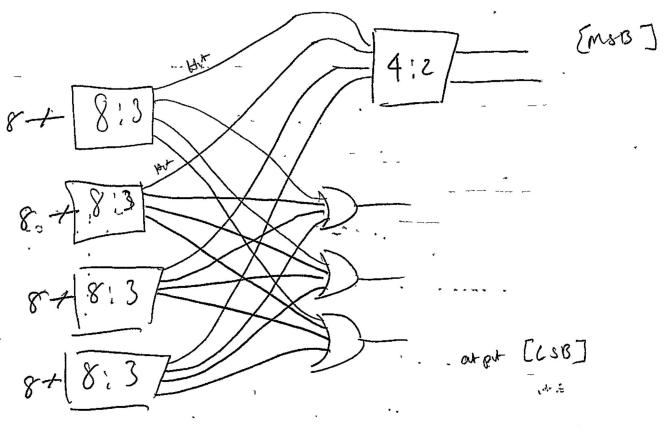


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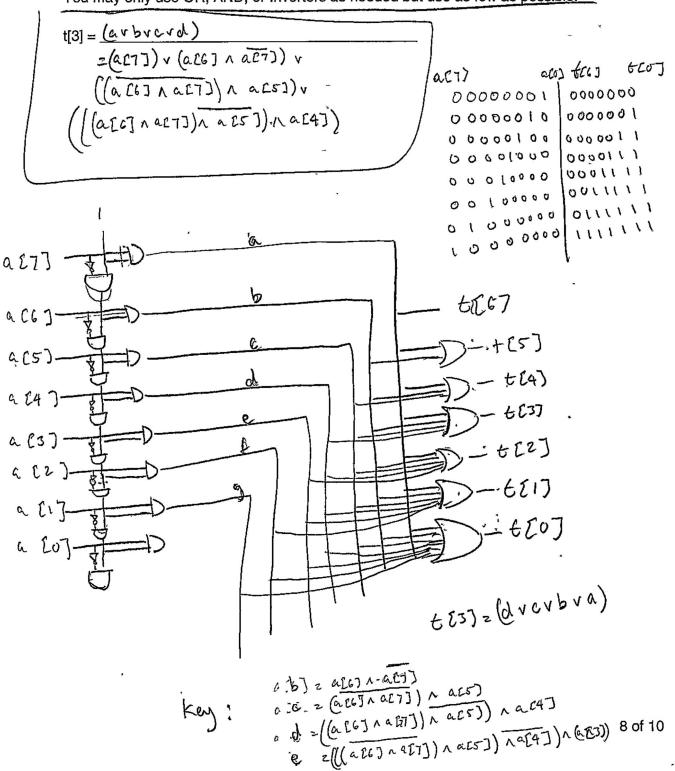
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(b) Use 8-to-3 encoders and 4-to-2 encoders as modules and design a 32-5 encoder with as few modules and additional logic as possible. Again, you may use additional OR, AND, or Inverters as needed.



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(c) Instead of an encoder that converts to binary, consider a converter that outputs thermometer code instead. For instance, an 8-bit one-hot input, a[7:0], would convert to 7-bit thermometer output, t[6:0]. When a[7:0] = 8'0100_0000, t[6:0] = 7'b011_1111, and when a[7:0] = 8'b0000_0001, t[6:0] = 7'b000_0000. First write the Boolean expression for t[3] as a function of the a[7:0] inputs. Then design, using a bit-cell approach, the logic for each bit position. Denote the inputs, outputs and signals passing between bit-positions clearly as well as the connections for the MSB and LSB positions. Note that since the output has one fewer bit than the input, one of the bit-slices may have logic that is not used to produce an output. You may only use OR, AND, or Inverters as needed but use as few as possible.



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