

## Register Address Map for Cyclone V HPS

Interface	Name	Start Address	End Address
<a href="#">hps2fpgaslaves</a>	FPGA Slaves Accessed Via HPS2FPGA AXI Bridge	0xC0000000	0xFBFFFFFF
<a href="#">stm</a>	STM Module	0xFC000000	0xFEFFFFFF
<a href="#">dap</a>	DAP Module	0xFF000000	0xFF1FFFFF
<a href="#">lwfpgaslaves</a>	FPGA Slaves Accessed Via Lightweight HPS2FPGA AXI Bridge	0xFF200000	0xFF3FFFFF
<a href="#">lwhps2fpgaregs</a>	LWHPS2FPGA AXI Bridge Module	0xFF400000	0xFF47FFFF
<a href="#">hps2fpgaregs</a>	HPS2FPGA AXI Bridge Module	0xFF500000	0xFF507FFF
<a href="#">fpga2hpsregs</a>	FPGA2HPS AXI Bridge Module	0xFF600000	0xFF67FFFF
<a href="#">emac</a>	EMAC Module	0xFF700000 0xFF702000	0xFF701FFF 0xFF703FFF
<a href="#">sdmmc</a>	SDMMC Module	0xFF704000	0xFF7043FF
<a href="#">qspiaregs</a>	QSPI Flash Controller Module Registers	0xFF705000	0xFF7050FF
<a href="#">fpgamgrregs</a>	FPGA Manager Module	0xFF706000	0xFF706FFF
<a href="#">acpidmap</a>	ACP ID Mapper Registers	0xFF707000	0xFF707FFF
<a href="#">gpio</a>	GPIO Module	0xFF708000 0xFF709000 0xFF70A000	0xFF70807F 0xFF70907F 0xFF70A07F
<a href="#">l3regs</a>	L3 (NIC-301) GPV Registers	0xFF800000	0xFF87FFFF

Interface	Name	Start Address	End Address
<a href="#">nanddata</a>	NAND Controller Module Data (AXI Slave)	0xFF900000	0xFF9FFFFFFF
<a href="#">qspiata</a>	QSPI Flash Module Data (AHB Slave)	0xFFA00000	0xFFAFFFFFFF
<a href="#">usb</a>	USB OTG Controller Module Registers	0xFFB00000 0xFFB40000	0xFFB3FFFF 0xFFB7FFFF
<a href="#">nandregs</a>	NAND Flash Controller Module Registers (AXI Slave)	0xFFB80000	0xFFB807FF
<a href="#">fpgamgrdata</a>	FPGA Manager Module Configuration Data	0xFFB90000	0xFFB90003
<a href="#">can</a>	CAN Controller Module	0xFFC00000 0xFFC01000	0xFFC001FF 0xFFC011FF
<a href="#">uart</a>	UART Module	0xFFC02000 0xFFC03000	0xFFC020FF 0xFFC030FF
<a href="#">i2c</a>	I2C Module	0xFFC04000 0xFFC05000 0xFFC06000 0xFFC07000	0xFFC040FF 0xFFC050FF 0xFFC060FF 0xFFC070FF
<a href="#">timer</a>	Timer Module	0xFFC08000 0xFFC09000 0xFFD00000 0xFFD01000	0xFFC080FF 0xFFC090FF 0xFFD000FF 0xFFD010FF
<a href="#">sdr</a>	SDRAM Controller	0xFFC20000	0xFFC3FFFF
<a href="#">l4wd</a>	L4 Watchdog Module	0xFFD02000 0xFFD03000	0xFFD020FF 0xFFD030FF

Interface	Name	Start Address	End Address
<a href="#">clkmgr</a>	Clock Manager Module	0xFFD04000	0xFFD041FF
<a href="#">rstmgr</a>	Reset Manager Module	0xFFD05000	0xFFD050FF
<a href="#">sysmgr</a>	System Manager Module	0xFFD08000	0xFFD0BFFF
<a href="#">dmanonsecure</a>	nonsecure DMA Module Address Space	0xFFE00000	0xFFE00FFF
<a href="#">dmasecure</a>	secure DMA Module Address Space	0xFFE01000	0xFFE01FFF
<a href="#">spis</a>	SPI Slave Module	0xFFE02000 0xFFE03000	0xFFE0207F 0xFFE0307F
<a href="#">spim</a>	SPI Master Module	0xFFFF0000 0xFFFF0100	0xFFFF00FF 0xFFFF01FF
<a href="#">scanmgr</a>	Scan Manager Module Registers	0xFFFF0200	0xFFFF021F
<a href="#">rom</a>	Boot ROM Module	0xFFFFD000	0xFFFFDFFF
<a href="#">mpu</a>	MPU Module Address Space	0xFFFE0000	0xFFFE0FFF
<a href="#">mpul2</a>	MPU L2 cache controller Module Address Space	0xFFFEF000	0xFFFEFFFF
<a href="#">ocram</a>	On-chip RAM Module	0xFFFF0000	0xFFFF0FFF

## HPS

Address map for the HHP HPS system-domain