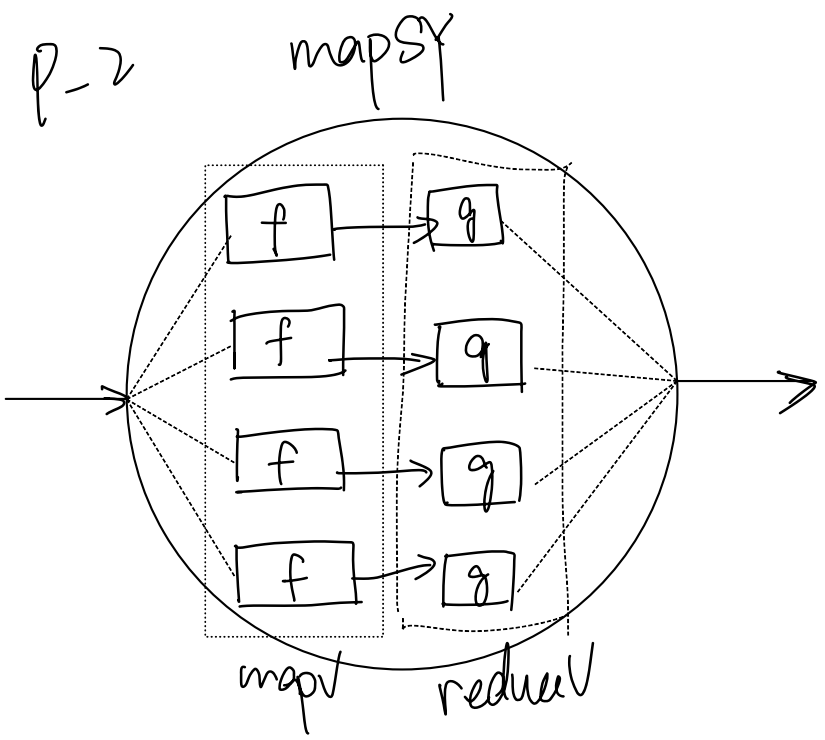
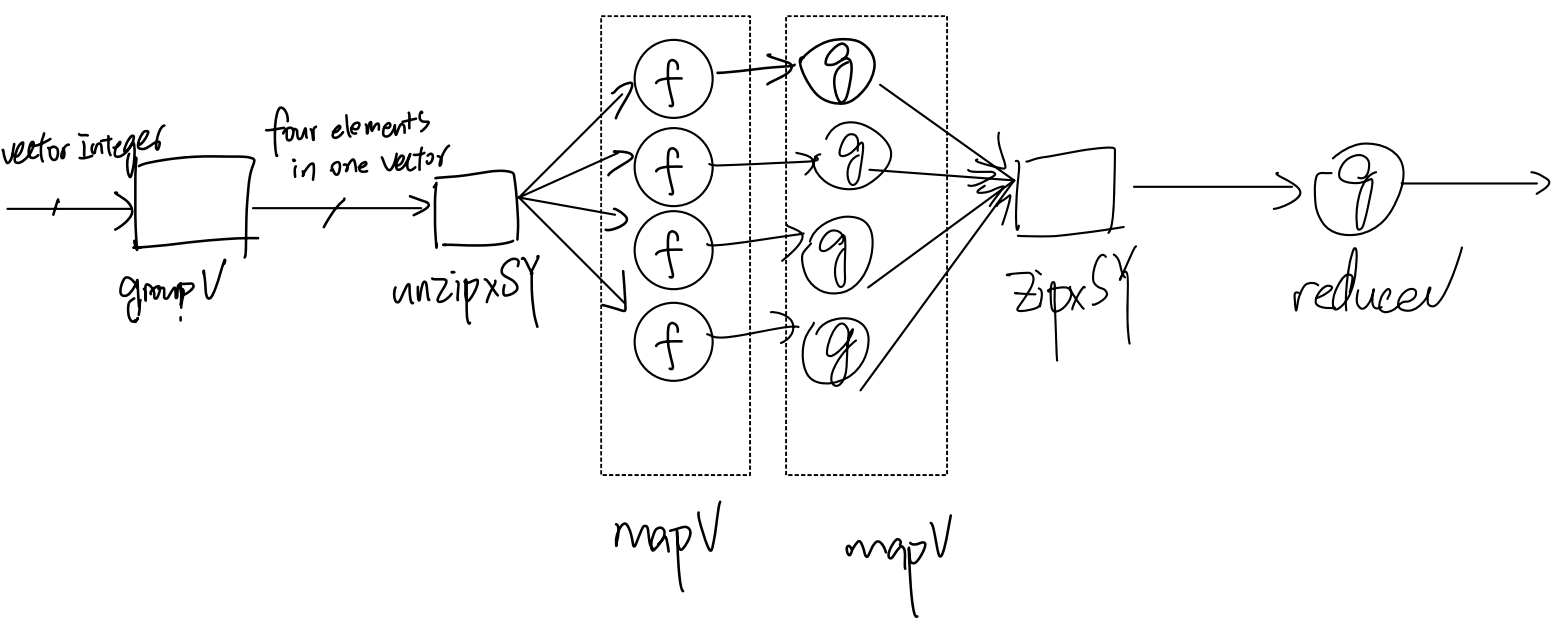


(a)



(b) p-2'



(c) not always.

① there could be data dependencies. The group V divide signals into group of 4, then some related data may not get assigned in the same group

② too many modules can lead to long communication time. If the signal is short then it doesn't worth it.

③ 4 processors if not evenly allocated then the unbalance workload can lead to waste of time.