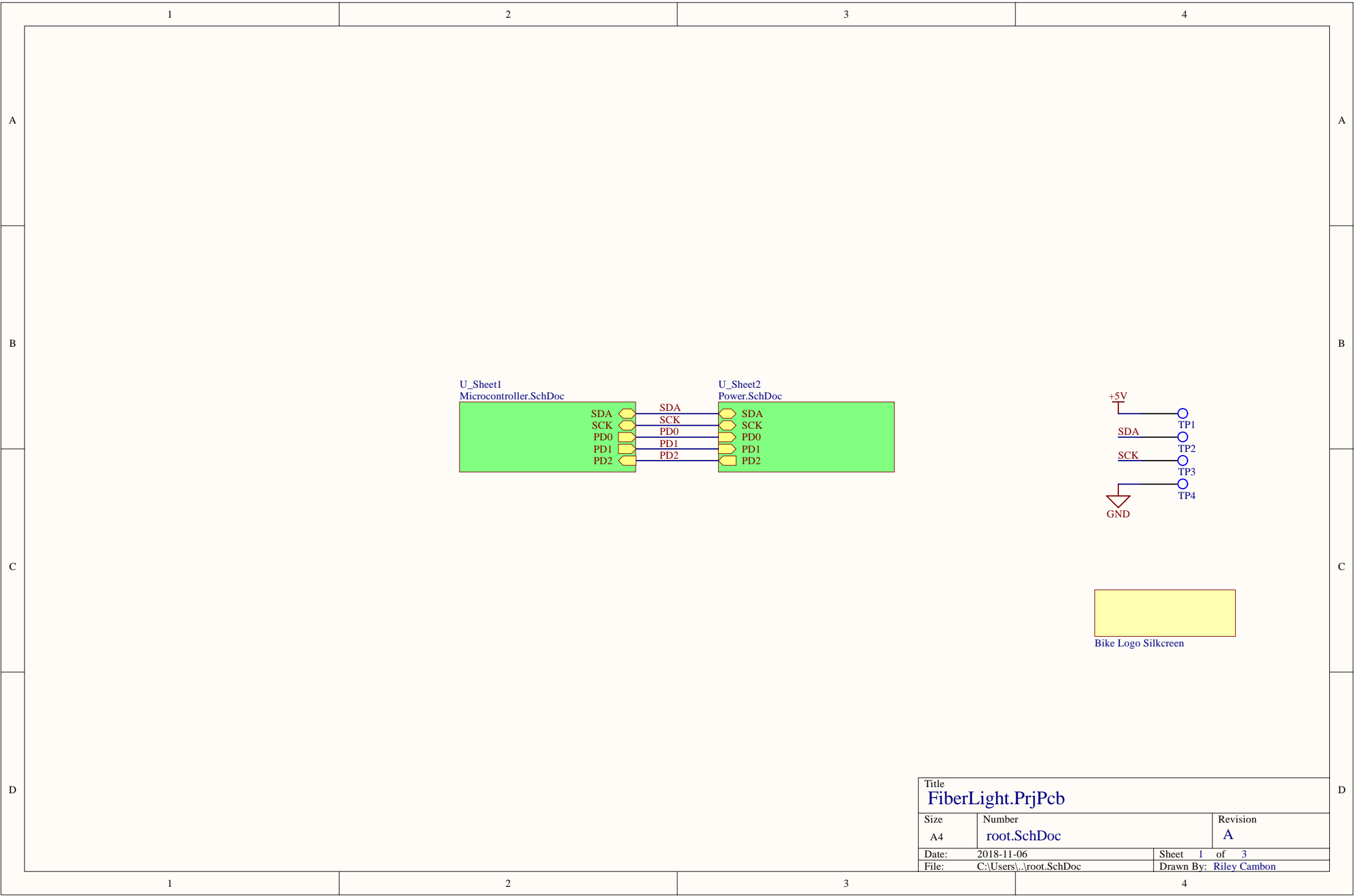
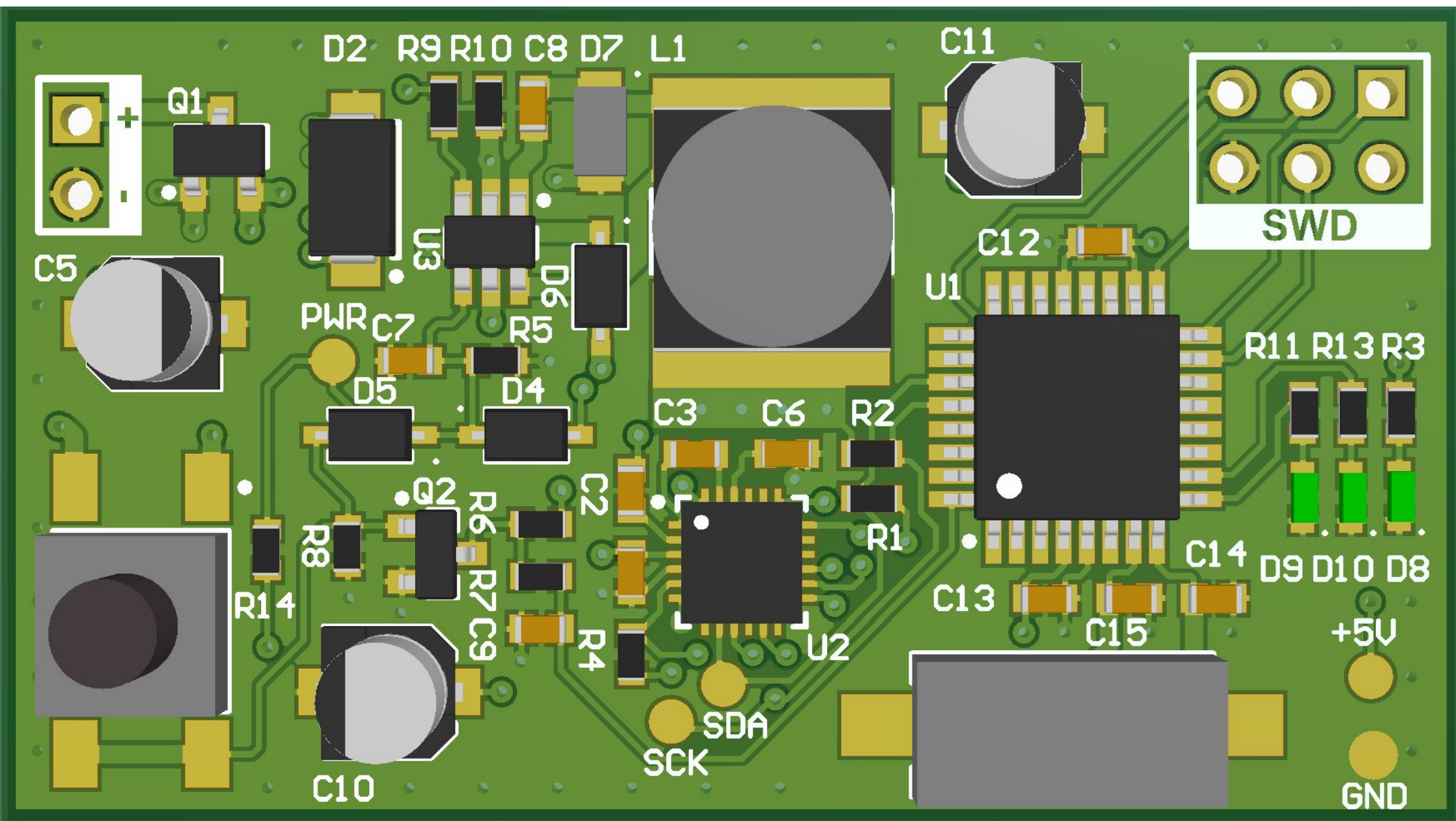


[illegible]

The diagram illustrates a power management circuit for a microcontroller. The VBAT input is connected to a switch SW1. The switch is connected to a 5V regulator circuit consisting of a 0R resistor (R14) and a 100k resistor (R5) in series, with a 0.1uF capacitor (C7) connected to ground. The output of the regulator is PWR_EN. A MOSFET Q2 (NTR5103N) is controlled by PD0, which is connected to the gate of Q2 through a 100k resistor (R6). The drain of Q2 is connected to a 220R resistor (R7) and a 0.1uF capacitor (C9) to ground. The output of the MOSFET is PD2, which is connected to a microcontroller pin.

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