

刘某某

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🎓 教育背景

知春路航空职业学院

2020/09 – 2023/06

硕士研究生 电子信息工程学院 信息与通信工程

- 专业排名: Top 30%
- 主修课程: DSP 体系结构, 时频分析, 检测估计与调制
- 研究方向: 制作未来道具

昌平航空职业学院

2016/09 – 2020/06

本科生 电子信息工程学院 电子信息工程专业

- 专业排名: Top 5%
- 主修课程: C 语言, 计算机软件基础, 通信原理, 信号与系统, 数字信号处理, 数字电路

👨‍💻 实习/项目经历

给老黄设计芯片

2022/05 – 2023/01

- 开发了 xx 工具, 大大提高了团队开发效率。
- 搭建了很牛逼的平台, 验证 xx 功能, 总之很吊。

面向 xx 目标炼丹

2021/10 – 2022/11

- 研究目的: 我为啥要研究这个。
- 研究方法: 采集了 xx 数据, 用了 xx 方法, 实现了 xx。
- 研究结果: 实现了 xx 效果, 比原来提高了 114 倍。

双路语音同传的无线收发系统

2019/07

- 研究目的: 打比赛, 混点综合量化保研。
- 实现方法: 大腿很牛。
- 实验结果: 信号很纯真, 来到北邮综测, 初试和综测双最高分, 但是没进国赛, 作为一名理工男我觉得这太酷了, 很符合我对电赛的幻想。

⚙️ 其他说明

- 编程能力:
 - 熟练使用 Python / C / Matlab
 - Linux 用的挺溜
 - 会写 Verilog
- 语言能力:
 - 过了四六级

📅 学生工作

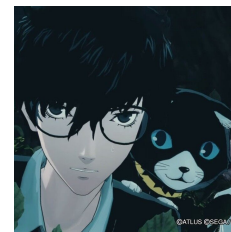
- 古典部副部长, 于文化祭出版社刊《冰菓》, 帮助 2-F 班推理电影结局, 在文化祭中参加猜谜、料理等比赛, 在校外参与新年参拜、生雏祭等活动。

🏆 获奖情况

校级三好学生	2021/10
北京市优秀毕业生	2020/06
全国大学生电子设计竞赛一等奖	2019/09

RYUSHANE

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🎓 EDUCATION BACKGROUND

BeiHang University (BUAA)

2020/09 – 2023/06

Master of Information and Communication Engineering

- Research Topics: Optimization and acceleration of Convolutional neural network on FPGA
- *1st Prize* of freshman merit scholarship
- Merit Student of BUAA

BeiHang University (BUAA)

2016/09 – 2020/06

Bachelor of Electronics and Information Engineering

- *GPA*: 3.78/4.00, *Ranking*: Top 5%
- Beijing Outstanding Graduate(Top %5), *1st Prize* on NUEDC, *1st Prize* on CUMCM

👤 EXPERIENCE

ASIC Design and Verification in NVIDIA

2022/05 – 2023/01

- Purpose: Get a internship experience and hope to convert.
- Result: Develop a tools which is gorgeous for ASIC team and improve our development efficiency.

xx-based Network

2021/10 – 2022/11

- Purpose: I need to get a master degree.
- Method: Data collection, using xx method.
- Result: Achieving a 114514x speedup compared to Raspberry Pi 3b.

Wireless transceiver system for two-way voice simultaneous transmission

2020/07 – 2021/04

- Purpose: Get some comprehensive quantification for postgraduate recommendation.
- Method: Ride the dalao's coattails.
- Result: The signal is very good, came to BUPT for comprehensive test. Both preliminary examination and comprehensive test scores are highest. But we did not advance to the national competition. As a polytechnic man I think it's really cool and fits my fantasy about NUEDC.

⚙️ SKILLS

- Proficient in using Verilog for FPGA development, familiar with the Vivado development environment.
- Proficient in Python / C programming, familiar with Tcl scripting and Linux development environment.
- CET-6

🏠 CAMPUS ACTIVITIES

- Vice President of the Classical Department, publishes the magazine "Ice Tang" at the Cultural Festival, helps the 2-F class to deduce the ending of the movie, participates in the riddle-guessing and cooking competitions at the Cultural Festival, and participates in the New Year's Day worship and the Hatchling Festival outside of school.