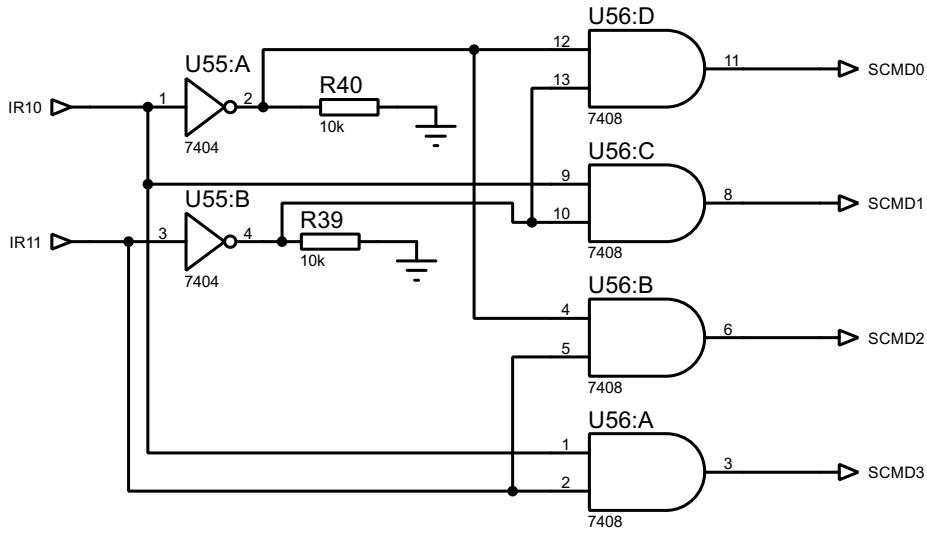
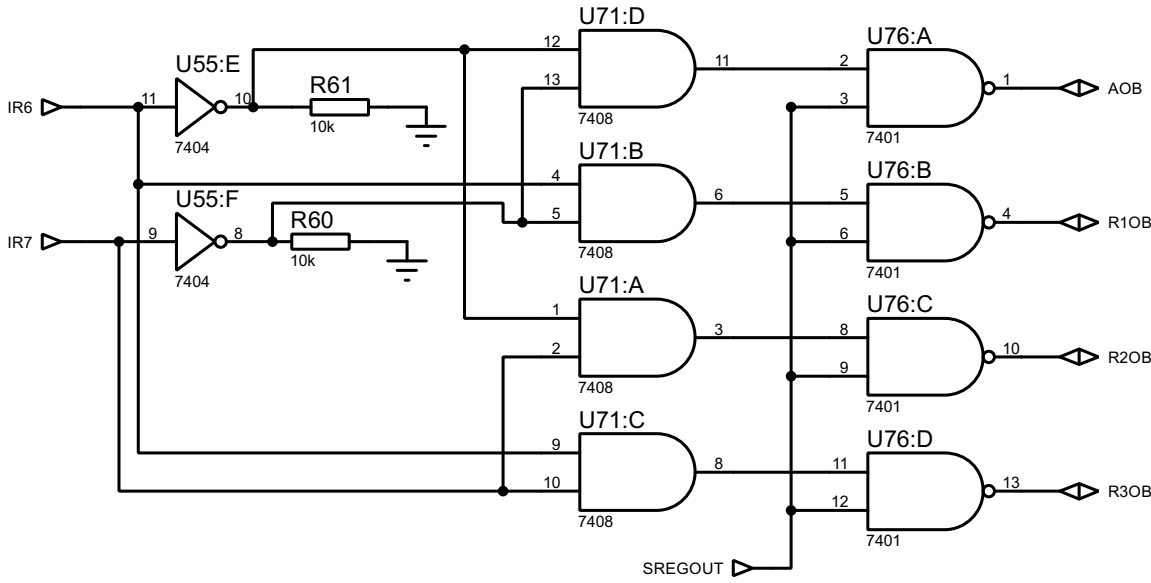


Decoder

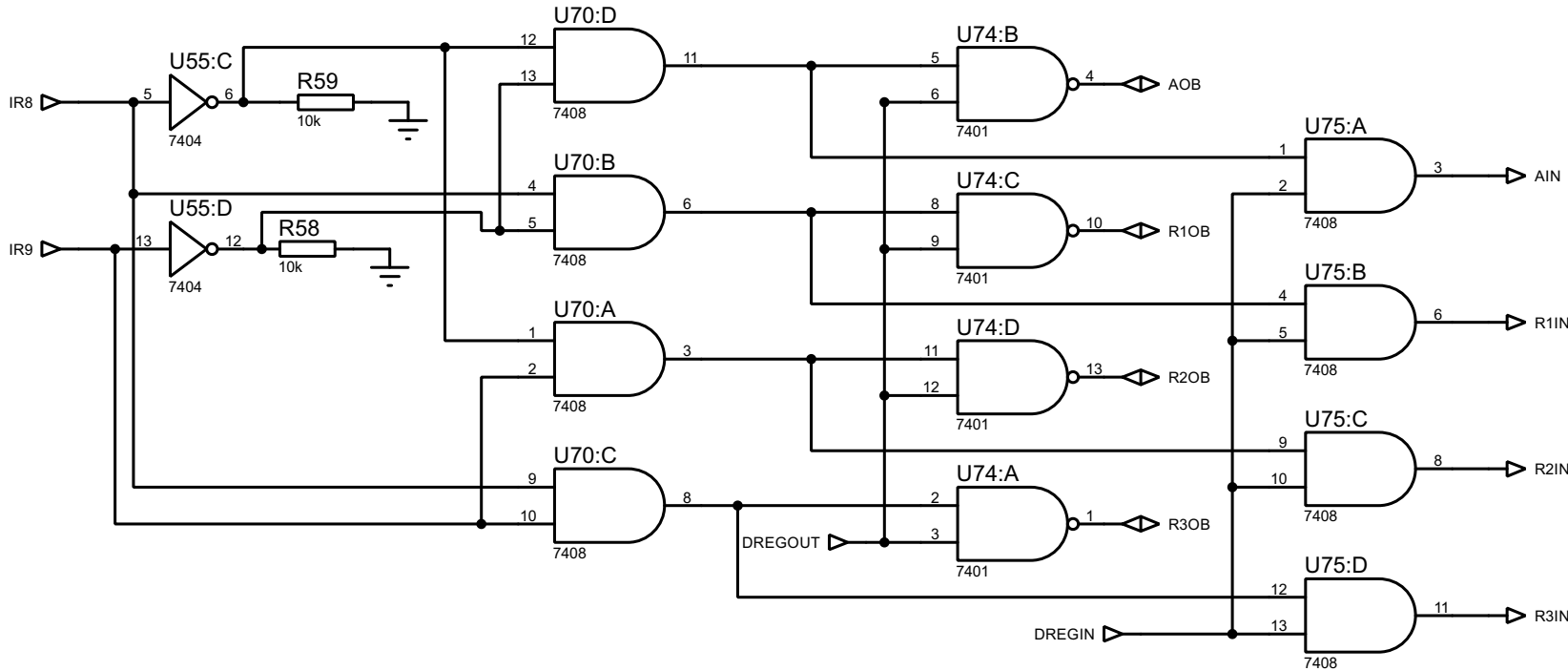
Subcommand decoder



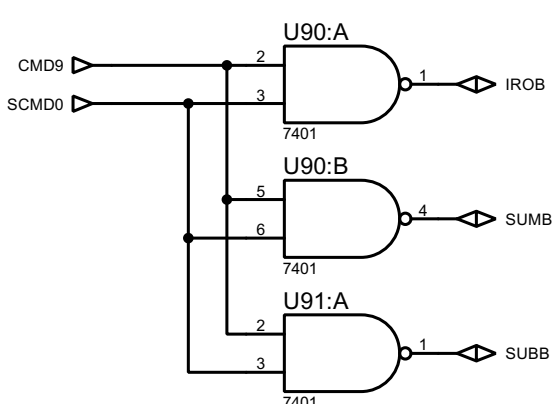
Source register selector



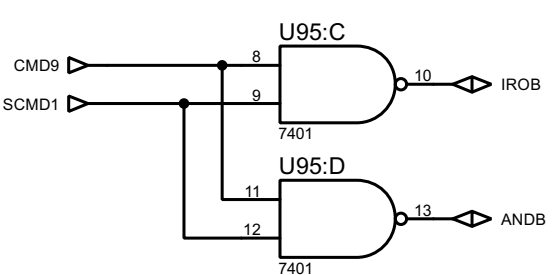
Dest / alt source register selector



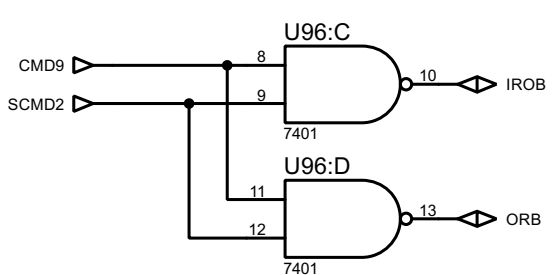
SUBI



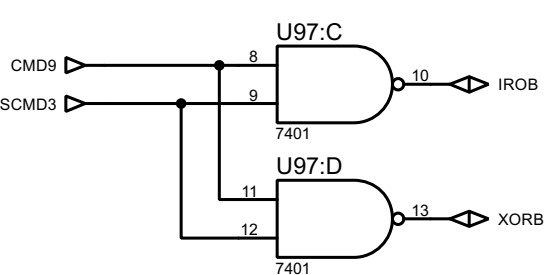
ANDI



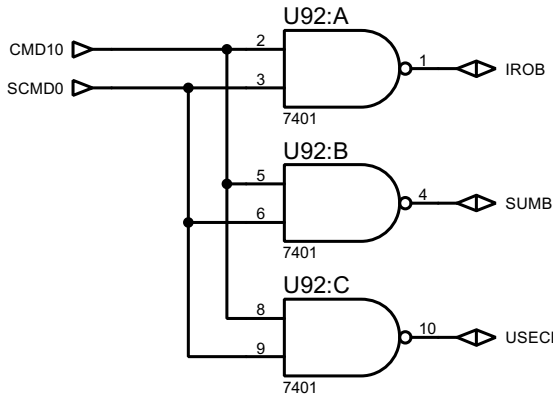
ORI



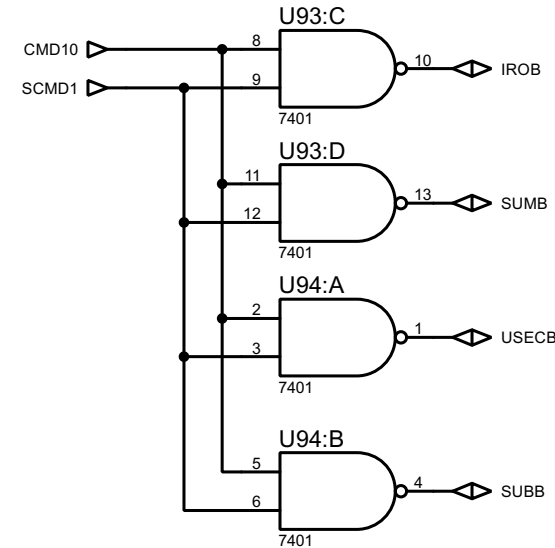
XORI



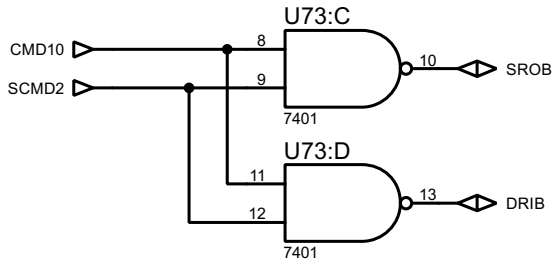
ADDCI



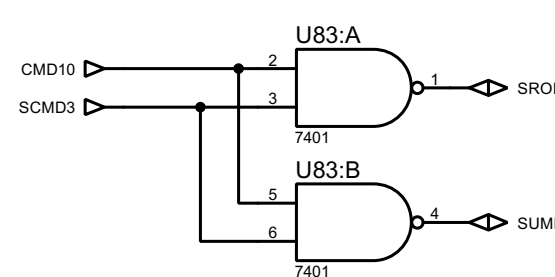
SUBBI



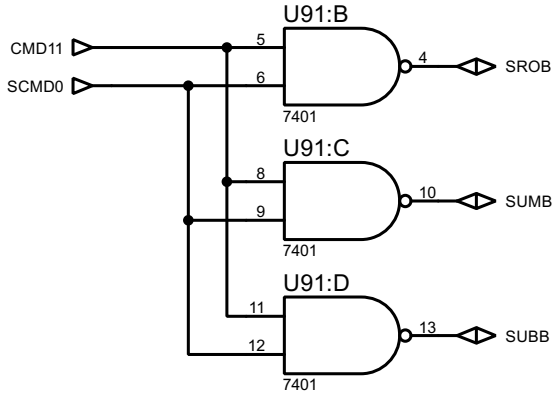
MOV



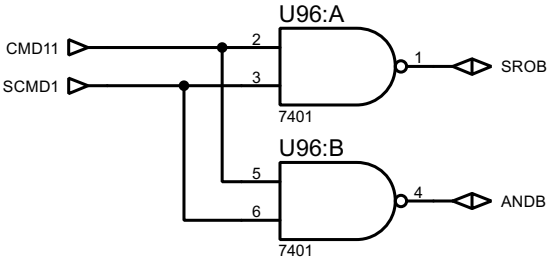
ADD



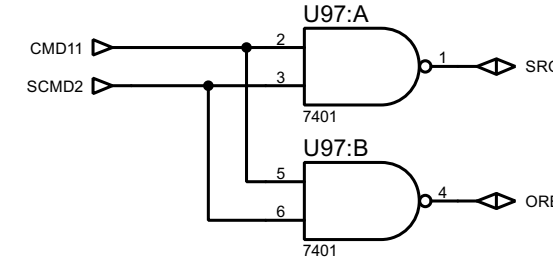
SUB



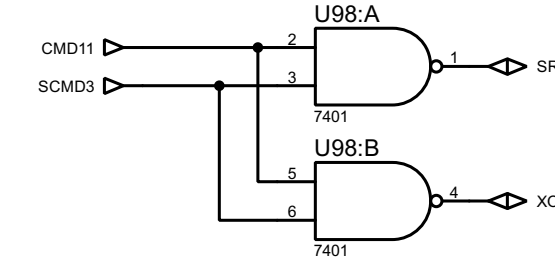
AND



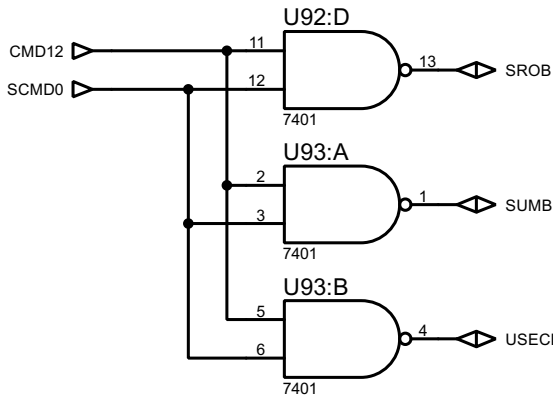
OR



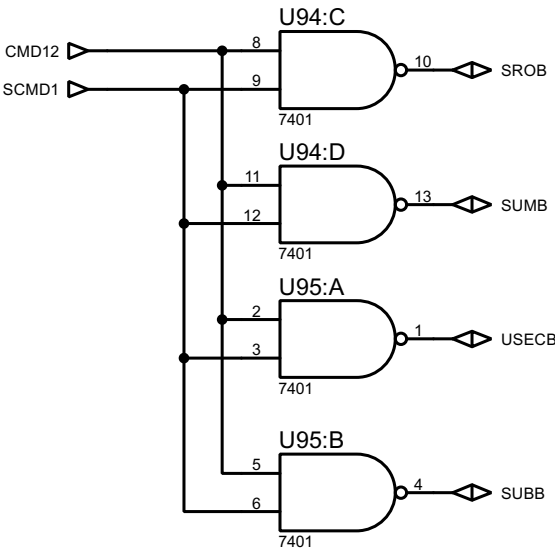
XOR



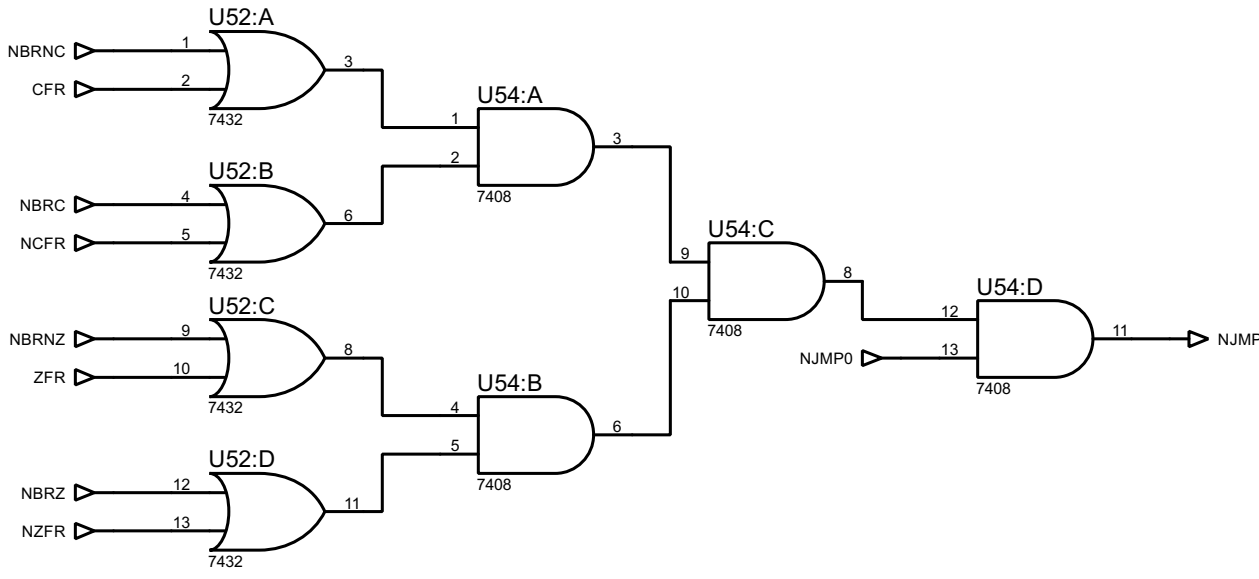
ADDC



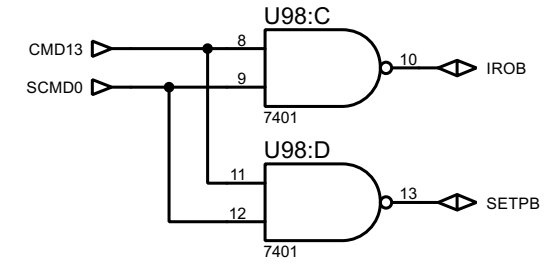
SUBB



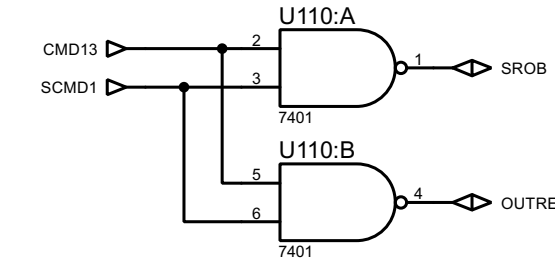
Jump instructions



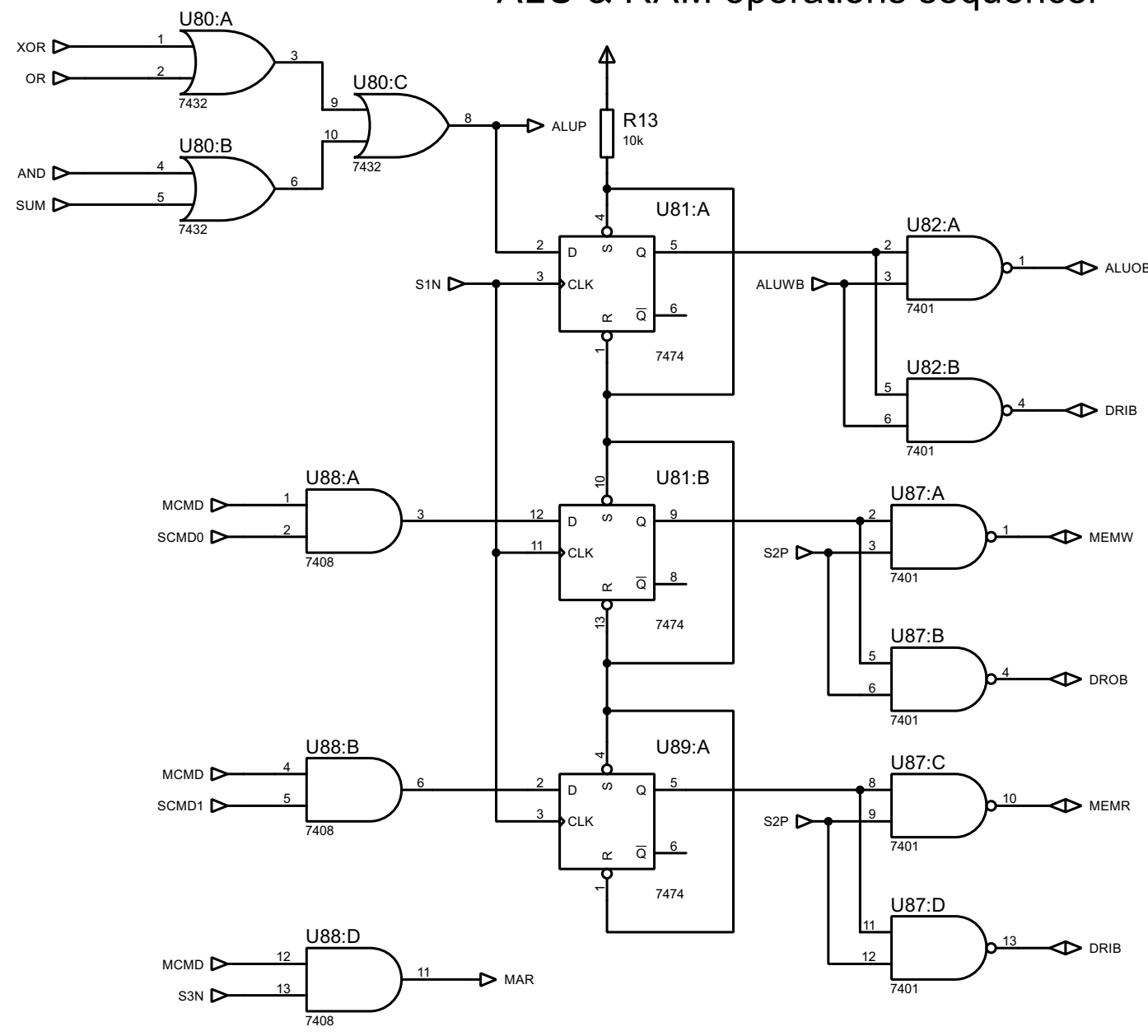
SETP



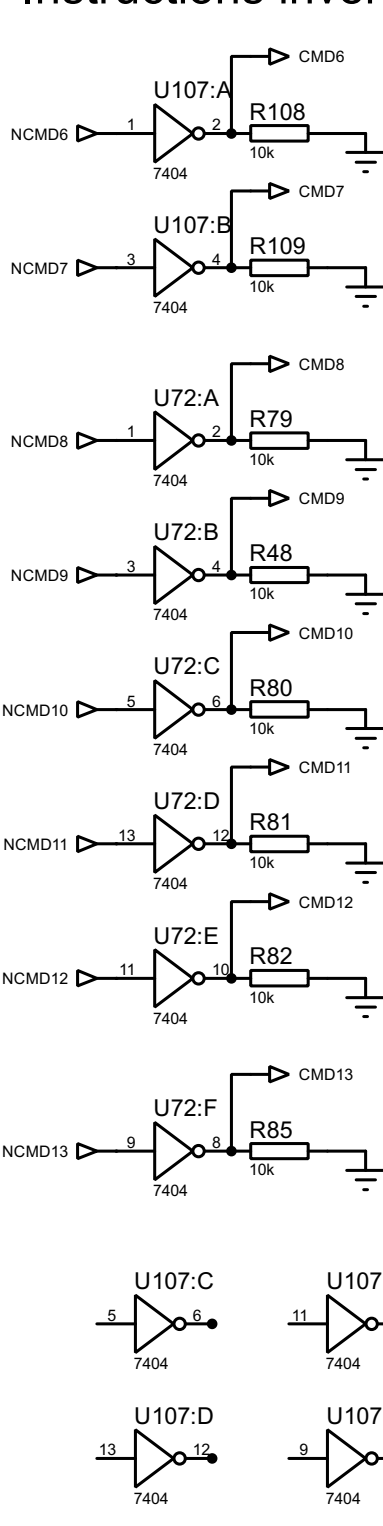
OUT



ALU & RAM operations sequencer



Instructions inverters



RAM access instructions

