



Transcript

```
# Loading work.counter_tb(tb)
# Loading work.counter_arch
add wave -position end sim:/counter_tb/clk
add wave -position end sim:/counter_tb/rst
add wave -position end sim:/counter_tb/up_n
add wave -position end sim:/counter_tb/load_n
add wave -position end sim:/counter_tb/input
add wave -position end sim:/counter_tb/output
add wave -position end sim:/counter_tb/done
VSIM 25> run -all
# ** Note: SIMULATION FINISHED!
# Time: 200 ns Iteration: 0 Instance: /counter_tb
VSIM 26>
```