Steven Miller

Gainesville, FL | steven.miller.040301@gmail.com | 352-256-6936 | linkedin.com/in/steven-miller-ufl
My Portfolio: s-miller-ufl.github.io/portfolio

EDUCATION

Bachelor of Science in Electrical and Computer Engineering
University of Florida | Gainesville, FL

Associate of Arts in Engineering
Santa Fe College | Gainesville, FL

EXPERIENCE

FPGA Based NES Game Console **01/2024 – 12/2024**
University of Florida | Gainesville, FL

- Designed Verilog based video game console for use on an intel-based FPGA.
- Directed a team of 5 members to design and implement logic-based circuits.
- Integrated our FPGA design with a microcontroller to facilitate peripheral communication.

Lead Robotics Testing Engineer **08/2023 – 12/2023**
University of Florida | Gainesville, FL

- Oversaw a variety of testing procedures for a cargo carrying robot to obtain data on its load capabilities.
- Coordinated use of mechanized equipment to test physical limits of robot containing various loads.
- Advised clients on physical limitations of robot for reporting to scientific journals.

Embedded Microcontroller Engineer **05/2023 – 08/2023**
University of Florida | Gainesville, FL

- Wrote several algorithms in assembly and C for use in microcontroller peripheral interface.
- Oversaw implementation of SPI and UART protocol to integrate 3+ peripherals with microcontroller.
- Designed 4 PCBs for use in embedded applications with ARM based microcontrollers.

Digital logic Teaching Assistant **01/2023 – 12/2024**
University of Florida | Gainesville, FL

- Assisted 400+ students and the professor to facilitate practical learning.
- Diagnosed 100+ student-made circuit designs to ensure practicality and correctness.
- Advised students on proper logic design to ensure successful implementation.

VHDL Based Logic Design Engineer **08/2022 – 12/2024**
University of Florida | Gainesville, FL

- Designed a variety of logic circuits using VHDL according to client specifications for use in final product.
- Verified logic circuit designs using Modelsim to ensure behavior matched client specifications.
- Oversaw implementation of logic circuit designs into final project which resulted in early delivery.

SKILLS

PCB Design, Driver Design, Serial Communication, Intel FPGAs, FPGA-Based Logic Design, Intel Quartus Prime, Altium, KICAD, Modelsim, MATLAB, C++/C, Verilog, VHDL