Steven Miller

US Citizen | steven.miller.040301@gmail.com | 352-256-6936 | linkedin.com/in/steven-miller-ufl

EDUCATION

University of Florida | *Bachelor of Science* Gainesville, FL | **06/2019 – 12/2024**

- **Electrical and Computer Engineering**
- **3.3 GPA**

Santa Fe College | *Associate of Arts* Gainesville, FL | **06/2019 – 06/2021**

- **Engineering**
- **3.3 GPA**

SKILLS

Embedded Hardware	*Driver design, I2C, UART, SPI, Embedded C, Gyroscopes, DAC, ADC*
Programming Skills	*C++/C, Verilog, VHDL, x86 assembly, ARM assembly*
Logic Design	*CPU Design, intel FPGA based designs, SOC principles, FPGA design*
Electrical Engineering	*PCB design, ADC design, DAC design, Noise mitigation*
Software Tools	*Intel Quartus Prime, Visual studio, Altium, KICAD, Modelsim, MATLAB*

EXPERIENCE

University of Florida | *Verilog Embedded Systems Engineer* Gainesville, FL | **01/2024 – 12/2024**

- Designed Verilog based video game console for use on an intel-based FPGA
- Directed team of 5 members to design and implement logic-based circuits
- Integrated our FPGA design with a microcontroller to facilitate peripheral communication

University of Florida | *Lead Robotics Testing Engineer* Gainesville, FL | **08/2023 – 12/2023**

- Oversaw over 10+ testing procedures for a cargo carrying robot to obtain data on its load capabilities
- Coordinated use of mechanized equipment to test physical limits of robot containing various loads
- Advised client on physical limitations of robot for reporting to scientific journals

University of Florida | *Embedded Microcontroller Engineer* Gainesville, FL | **05/2023 – 08/2023**

- Wrote 5+ algorithms in assembly and C for use in microcontroller peripheral interface
- Oversaw implementation of SPI and UART protocol to integrate 3+ peripherals with microcontroller
- Designed 4 PCBs for use in embedded applications with ARM based microcontrollers

University of Florida | *Digital logic Teaching assistant* Gainesville, FL | **01/2023 – 12/2024**

- Assisted over 400 students and the professor to facilitate practical learning
- Diagnosed over 50 student made circuit designs to ensure that they were correct and practical
- Advised students on proper logic design to ensure successful implementation

University of Florida |*VHDL Based Logic Design Engineer* Gainesville, FL | **08/2022 – 12/2024**

- Designed over 10+ logic circuits using VHDL according to client need for use in final product
- Verified logic circuit designs using Modelsim to ensure behavior matched client needs
- Oversaw implementation of logic circuit designs into final project which resulted in early delivery

SCHEDULE A ELIGIBLE AND WOULD PREFER TO BE HIRED UNDER SCHEDULE A