Synthesis Report for 'HW_2DConv_Mmap_3'

General Information

Date: Fri Dec 27 10:57:53 2019

Version: 2014.2 (Build 932637 on Wed Jun 11 12:38:34 PM 2014)

Project: CONV Solution: solution1

Product family: kintex7 kintex7_fpv6 **Target device:** xc7k160tfbg484-2

Performance Estimates

• Timing (ns)

o Summary

			Uncertainty
default	10.00	8.75	1.25

• Latency (clock cycles)

o Summary

Late	ency	Inte	Interval				
min	max	min	max	Type			
131153	131153	131154	131154	none			

o Detail

Instance

N/A

■ Loop

Loop Name	Late	ency	Iteration Latency	Initiation 1	Interval	Trip Count	Dinalinad	
Loop Name	min	max	Tteration Latency	achieved	target	Trip Count	прешеч	
- memcpy.filter_buffer.pixel_in	15	15	8	1	1	9	yes	
- memcpy.filter_buffer2.pixel_in2	15	15	8	1	1	9	yes	
- ROW_LOOP_COL_LOOP	131115	131115	13	1	1	131104	yes	

Utilization Estimates

• Summary

Name	BRAM_18K	DSP48E	FF	LUT
Expression	-	1	0	2046
FIFO	-	-	-	-
Instance	-	-	2810	3564
Memory	4	-	0	0
Multiplexer	-	-	-	213
Register	-	-	1052	91
Total	4	1	3862	5914
Available	650	600	202800	101400
Utilization (%)	~0	~0	1	5

• Detail

o Instance

Instance	Module	BRAM_18K	DSP48E	FF	LUT
HW_2DConv_Mmap_3_urem_4ns_4ns_4_7_U0	HW_2DConv_Mmap_3_urem_4ns_4ns_4_7	0	0	35	32
HW_2DConv_Mmap_3_urem_4ns_4ns_4_7_U1	HW_2DConv_Mmap_3_urem_4ns_4ns_4_7	0	0	35	32
HW_2DConv_Mmap_3_user_axi4lite_config_s_axi_U	HW_2DConv_Mmap_3_user_axi4lite_config_s_axi	0	0	548	700
HW_2DConv_Mmap_3_user_axi_in2_m_axi_U	HW_2DConv_Mmap_3_user_axi_in2_m_axi	0	0	548	700

HW_2DConv_Mmap_3_user_axi_in_m_axi_U	HW_2DConv_Mmap_3_user_axi_in_m_axi	0	0 548	700
HW_2DConv_Mmap_3_user_axi_out2_m_axi_U	HW_2DConv_Mmap_3_user_axi_out2_m_axi	0	0 548	700
HW_2DConv_Mmap_3_user_axi_out_m_axi_U	HW_2DConv_Mmap_3_user_axi_out_m_axi	0	0 548	700
Total	7	0	0 2810	3564

• Memory

Memory	Module	BRAM_18K	FF	LUT	Words	Bits	Banks	W*Bits*Banks
line_buffer_M_0_U	HW_2DConv_Mmap_3_line_buffer_M_0	1	0	0	482	8	1	3856
line_buffer_M_1_U	HW_2DConv_Mmap_3_line_buffer_M_0	1	0	0	482	8	1	3856
line_buffer2_M_0_U	HW_2DConv_Mmap_3_line_buffer_M_0	1	0	0	482	8	1	3856
line_buffer2_M_1_U	HW_2DConv_Mmap_3_line_buffer_M_0	1	0	0	482	8	1	3856
Total	4	4	0	0	1928	32	4	15424

o FIFO

N/A

o Expression

Variable Name	Operation	DSP48E	FF	LUT	Bitwidth P0	Bitwidth P
tmp_1_fu_2179_p2	*	1	0	0	9	g
addconv_fu_2276_p2	+	0	0	19	19	19
c_1_fu_2170_p2	+	0	0	9	9	1
indvar_flatten_next_fu_2098_p2	+	0	0	18	18	1
indvar_next1_fu_1047_p2	+	0	0	4	4	1
indvar_next_fu_689_p2	+	0	0	4	4	1
next_mul1_fu_1059_p2	+	0	0	8	8	4
next mul fu 701 p2	+	0	0	8	8	
pixel in25 sum1 fu 2213 p2	+	0	0	33	33	33
pixel in25 sum fu 667 p2	+	0	0	33	33	18
pixel in3 sum1 fu 2198 p2	+	0	0	33	33	33
pixel in3 sum fu 651 p2	+	0	0	33	33	18
pixel out29 sum fu 2301 p2	+	0	0	33	33	33
pixel out7 sum fu 2286 p2	+	0	0	33	33	33
r s fu 2118 p2	+	0	0	9	9	
res 3 0 1 fu 2636 p2	+	0	0	8	8	8
res 3 0 2 fu 2690 p2	+	0	0	8	8	
res 3 1 1 fu 2798 p2	+	0	0	8	8	
res_3_1_2_fu_2852_p2	+	0	0	8	8	
res 3 1 fu 2744 p2	+	0	0	8	8	
res 3 2 1 fu 2960 p2	+	0	0	8	8	8
res 3 2 2 fu 3014 p2	+	0	0	8	8	8
res 3 2 fu 2906 p2	+	0	0	8	8	8
res 3 fu 2582 p2	+	0	0	8	8	8
store data2 2 2 2 fu 3158 p2	+	0	0	4	8	8
store data 2 2 2 fu 3089 p2	+	0	0	4	8	:
tmp15 fu 3047 p2	+	0	0	8	8	8
tmp16 fu 3053 p2	+	0	0	8	8	8
tmp17 fu 3059 p2	+	0	0	4	8	8
tmp18 fu 3065 p2	+	0	0	4	8	8
tmp19 fu 3071 p2	+	0	0	4	8	8
tmp20 fu 3077 p2	+	0	0	4	8	8
tmp21 fu 3083 p2	+	0	0	4	8	8
tmp22 fu 3116 p2	+	0	0	8	8	8
tmp23 fu 3122 p2	+	0	-	8	8	8
tmp24 fu 3128 p2	+	0	0	4	8	8
tmp25 fu 3134 p2	+	0	0	4	8	8
tmp26 fu 3140 p2	+	0	0	4		
tmp27_fu_3146_p2	+	0	-	4		
tmp28 fu 3152 p2	+	0	-	4	i -	
tmp29_fu_2266_p2	+	0		11	11	1
res 0 1 fu 2642 p2	-	0		8		
res_0_2_fu_2696_p2		0		8		8
res 1 1 fu 2804 p2	-	0	-	8		8
res_1_2_fu_2858_p2	-	0		8		8

res_235_1_fu_2966_p2	res 1 fu 2750 p2		0	0	8	1	8
res_235_2 fn_3020_p2		-	-				
res_fu_2588_p2 - 0 0 0 8 1 1 8 8 1		-					
res_s_fu_2912_p2 tmp_4_fu_2250_p2		-	-				
tmp 4 fu 2250 p2							
c mid2 fu 2110 p3							
filter buffer 2	-						
filter buffer 2 Q 0 I fu 1263 p3 Select							
filter buffer 1 0 1 fu 961 p3			-				
filter buffer 2 0 1 fu 905 p3							
newSel10_fu_977_p3							
newSel11 fu 985 p3							
newSel12 fu 993 p3							
newSel13 fu 1001 p3				—— 			
newSel14 fu 1009 p3							
newSel15_fu_1017_p3 Select 0_0_8 1_8 8 newSel16_fu_1025_p3 Select 0_0_8 1_8 1_8 newSel17_fu_1033_p3 Select 0_0_0_8 1_8 8 newSel19_fu_1239_p3 Select 0_0_0_8 1_1_8 8 newSel19_fu_1239_p3 Select 0_0_0_8 1_1_8 8 newSel21_fu_1239_p3 Select 0_0_0_8 1_1_8 8 newSel21_fu_1247_p3 Select 0_0_0_8 1_1_8 8 newSel21_fu_1255_p3 Select 0_0_0_8 1_1_8 8 newSel22_fu_127p_p3 Select 0_0_0_8 1_1_8 8 newSel22_fu_127p_p3 Select 0_0_0_8 1_1_8 8 newSel22_fu_1295_p3 Select 0_0_0_8 1_1_8 8 newSel25_fu_1295_p3 Select 0_0_0_8 1_1_8 8 newSel26_fu_1330_p3 Select 0_0_0_8 1_1_8 8 newSel26_fu_1337_p3 Select 0_0_0_8 1_1_8 8							
newSel16_fu_1025_p3 Select 0 0 8 1 1 8 newSel17_fu_1033_p3 Select 0 0 8 1 1 8 newSel18_fu_1231_p3 Select 0 0 8 1 1 8 newSel19_fu_1239_p3 Select 0 0 8 1 1 8 newSel1_fu_881_p3 Select 0 0 8 1 1 8 newSel20_fu_1247_p3 Select 0 0 8 1 1 8 newSel21_fu_1255_p3 Select 0 0 8 1 1 8 newSel22_fu_1271_p3 Select 0 0 8 1 1 8 newSel23_fu_1279_p3 Select 0 0 8 1 1 8 newSel25_fu_1279_p3 Select 0 0 8 1 1 8 newSel25_fu_1295_p3 Select 0 0 8 1 1 8 newSel25_fu_1295_p3 Select 0 0 8 1 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 1 8 newSel27_fu_1327_p3 Select 0 0 8 1 1 8 newSel28_fu_1334_p3 Select 0 0 8 1 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 1 8 newSel30_fu_1367_p3 Select 0							
newSel17_ftu 1033 p3 Select 0 0 8 1 1 8 8 newSel18_ftu 1231 p3 Select 0 0 8 1 1 8 8 newSel19_ftu 1239 p3 Select 0 0 8 1 1 8 8 newSel1fu 881 p3 Select 0 0 8 1 1 8 8 newSel20_ftu 1247 p3 Select 0 0 8 1 1 8 8 newSel20_ftu 1247 p3 Select 0 0 8 1 1 8 8 newSel21_ftu 1271 p3 Select 0 0 8 1 1 8 8 newSel22_ftu 1271 p3 Select 0 0 8 1 1 8 8 newSel24_ftu 1287 p3 Select 0 0 8 1 1 8 8 newSel24_ftu 1287 p3 Select 0 0 8 1 1 8 8 newSel25_ftu 1295 p3 Select 0 0 8 1 1 8 8 newSel26_ftu 1303 p3 Select 0 0 8 1 1 8 8 newSel26_ftu 1335 p3 Select 0 0 8 1 1 8 8 newSel28_ftu 1335 p3 Select 0 0 8 1 1 8 8 newSel29_ftu 1343 p3 Select 0 0 8 1 1 8 8 newSel29_ftu 1343 p3 Select 0 0 8 1 1 8 8 newSel30_ftu 1351 p3 Select 0 0 8 1 1 8 8 newSel31_ftu 1359 p3 Select 0 0 8 1 1 8 8 newSel32_ft		Select					8
newSel18_fiu_1231_p3 Select 0_0 8 1 8 newSel19_fiu_123p_p3 Select 0_0 8 1 8 newSel1_fiu_123p_p3 Select 0_0 8 1 8 newSel2_fiu_127p_3 Select 0_0 8 1 8 newSel2_fiu_127p_3 Select 0_0 8 1 8 newSel2_fiu_127p_3 Select 0_0 8 1 8 newSel2_fiu_127p_p3 Select 0_0 8 1 8 newSel2_fiu_1287_p3 Select 0_0 8 1 8 newSel2_fiu_1303_p3 Select 0_0 8 1 8 newSel2_fiu_133_p3 Select 0_0 8 1 8 newSel2_fiu_134_p3 Select 0_0 8 1 8 newSel3_fiu_134_p3 Select 0_0 8 1 8 newSel3_fiu_134_p3 Select 0_0 8 1 8 <		Select	0	0		1	8
newSel19 fin 1239 p3 Select 0 0 8 1 8 1 8 newSel21 fin 1247 p3 Select 0 0 8 1 8 8 newSel22 fin 1247 p3 Select 0 0 8 1 8 8 newSel21 fin 1255 p3 Select 0 0 8 1 8 8 newSel22 fin 1271 p3 Select 0 0 8 1 8 8 newSel23 fin 1279 p3 Select 0 0 8 1 8 8 newSel25 fin 1295 p3 Select 0 0 8 1 8 8 newSel25 fin 1295 p3 Select 0 0 8 1 8 8 newSel26 fin 1303 p3 Select 0 0 8 1 8 8 newSel27 fin 1327 p3 Select 0 0 8 1 8 8 newSel28 fin 1335 p3 Select 0 0 8 1 8 8 newSel29 fin 1343 p3 Select 0 0 8 1 8 8 newSel29 fin 1351 p3 Select 0 0 8 1 8 8 newSel31 fin 1359 p3 Select 0 0 8 1 8 8 newSe		Select	0	0		1	8
newSel1 fu 881 p3 Select 0 0 8 1 8 8 newSel20 fu 1247 p3 Select 0 0 8 1 8 newSel20 fu 1247 p3 Select 0 0 8 1 8 newSel22 fu 1255 p3 Select 0 0 8 1 8 newSel22 fu 1271 p3 Select 0 0 8 1 8 newSel22 fu 1279 p3 Select 0 0 8 1 8 newSel23 fu 1279 p3 Select 0 0 8 1 8 newSel24 fu 1287 p3 Select 0 0 8 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 8 newSel25 fu 1337 p3 Select 0 0 8 1 8 newSel28 fu 1335 p3 Select 0 0 8 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 8 newSel30 fu 1351 p3 Select 0 0 8 1 8 newSel31 fu 1359 p3 Select 0 0 8 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 8 newSel33 fu 1383 p3 Select 0 0 8 1 8 newSel34 fu 1383 p3 Select 0 0 8 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 8 newSel35 fu 2610 p3 Select 0 0 8 1 8 newSel35 fu 2610 p3 Select 0 0 8 1 8 newSel35 fu 2610 p3 Select 0 0 8 1 8 newSel35 fu 2610 p3 Select 0 0 8 1 8 newSel35 fu 2615 p3 Select 0 0 8 1 8 newSel44 fu 2602 p3 Select 0 0 8 1 8 newSel44 fu 2625 p3 Select 0 0 8 1 8 newSel45 fu 2655 p3 Select 0 0 8 1 8 newSel45 fu 2655 p3 Select 0 0 8 1 8 newSel45 fu 2709 p3 Select 0 0 8 1 8 newSel45 fu 2709 p3 Select 0 0 8	newSel18_fu_1231_p3	Select	0	0		1	8
newSel20 fig_1247 p3	newSel19_fu_1239_p3	Select	0	0	8	1	8
newSel21_fu_1255_p3 Select 0 0 8 1 8 newSel22_fu_1271 p3 Select 0 0 8 1 8 newSel23_fu_1279_p3 Select 0 0 8 1 8 newSel24_fu_1287_p3 Select 0 0 8 1 8 newSel24_fu_1287_p3 Select 0 0 8 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 8 newSel27_fu_1327_p3 Select 0 0 8 1 8 newSel28_fu_1335_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel31_fu_135p_3 Select 0 0 8 1 8 newSel32_fu_1367_p3 Select 0 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select	newSel1_fu_881_p3	Select	0	0	8	1	8
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newSel23 fu 1279 p3 Select 0 0 8 1 1 8 newSel24 fu 1287 p3 Select 0 0 8 1 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 1 8 newSel26 fu 1303 p3 Select 0 0 8 1 1 8 newSel27 fu 1327 p3 Select 0 0 8 1 1 8 newSel28 fu 1335 p3 Select 0 0 8 1 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 1 8 newSel30 fu 1351 p3 Select 0 0 8 1 1 8 newSel31 fu 135p p3 Select 0 0 8 1 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 1 8 newSel33 fu 1375 p3 Select 0 0 8 1 8 newSel34 fu 1383 p3 Select 0 0 8 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 8 newSel36 fu 2594 p3 Select 0 0 8 1 8 newSel37 fu 2601 p3 Select 0 0 8 1 8 newSel48 fu 2608	newSel21_fu_1255_p3	Select	0	0	8	1	8
newSel24 fu 1287 p3 Select 0 0 8 1 1 8 newSel25 fu 1295 p3 Select 0 0 8 1 1 8 newSel26 fu 1303 p3 Select 0 0 8 1 1 8 newSel27 fu 1327 p3 Select 0 0 8 1 1 8 newSel28 fu 1335 p3 Select 0 0 8 1 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 1 8 newSel21 fu 889 p3 Select 0 0 8 1 1 8 newSel30 fu 1351 p3 Select 0 0 8 1 1 8 newSel31 fu 1359 p3 Select 0 0 8 1 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 1 8 newSel34 fu 1383 p3 Select 0 0 8 1 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 1 8 newSel36 fu 2594 p3 Select 0 0 8 1 8 newSel37 fu 2601 p3 Select 0 0 8 1 8 newSel48 fu	newSel22_fu_1271_p3	Select	0	0	8	1	8
newSel25_fu_1295_p3 Select 0 8 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 8 newSel28_fu_1335_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel31_fu_1359_p3 Select 0 0 8 1 8 newSel31_fu_1359_p3 Select 0 0 8 1 8 newSel31_fu_1359_p3 Select 0 0 8 1 8 newSel33_fu_1380_p3 Select 0 0 8 1 8 newSel3_fu_1391_p3 Select 0 0 8	newSel23_fu_1279_p3	Select	0	0	8	1	8
newSel25_fu_1295_p3 Select 0 0 8 1 8 newSel26_fu_1303_p3 Select 0 0 8 1 8 newSel27_fu_1327_p3 Select 0 0 8 1 8 newSel28_fu_1335_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel31_fu_1359_p3 Select 0 0 8 1 8 newSel31_fu_1375_p3 Select 0 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel35_fu_2504_p3 Select 0 0 <td< td=""><td>newSel24 fu 1287 p3</td><td>Select</td><td>0</td><td>0</td><td>8</td><td>1</td><td>8</td></td<>	newSel24 fu 1287 p3	Select	0	0	8	1	8
newSel27_fu_1327_p3 Select 0 0 8 1 8 newSel28_fu_1335_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel2_fu_1343_p3 Select 0 0 8 1 8 newSel3_fu_135_p3 Select 0 0 8 1 8 newSel31_fu_135_p3 Select 0 0 8 1 8 newSel31_fu_1375_p3 Select 0 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_138a_p3 Select 0 0 8 1 8 newSel35_fu_1391_p3 Select 0 0 8 1 8 newSel36_fu_2601_p3 Select 0 0 8 1 8 newSel37_fu_2601_p3 Select 0 0 8 </td <td>newSel25 fu 1295 p3</td> <td>Select</td> <td>0</td> <td>0</td> <td>8</td> <td>1</td> <td>8</td>	newSel25 fu 1295 p3	Select	0	0	8	1	8
newSel28 fu 1335 p3 Select 0 0 8 1 1 8 newSel29 fu 1343 p3 Select 0 0 8 1 1 8 newSel2 fu 889 p3 Select 0 0 8 1 1 8 newSel30 fu 1351 p3 Select 0 0 8 1 1 8 newSel31 fu 1359 p3 Select 0 0 8 1 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 1 8 newSel33 fu 1375 p3 Select 0 0 8 1 1 8 newSel34 fu 1383 p3 Select 0 0 8 1 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 1 8 newSel36 fu 2594 p3 Select 0 0 8 1 8 newSel37 fu 2601 p3 Select 0 0 8 1 8 newSel38 fu 2608 p3 Select 0 0 8 1 8 newSel39 fu 2615 p3 Select 0 0 8 1 8 newSel40 fu 2622 p3 Select 0 0 8 1 8 newSel41 fu 2622 p3 Select 0 0 8 1 8 newSel42 fu 2648 p3 Select 0 0 8 1 8 newSel44 fu 2662 p3 <td>newSel26 fu 1303 p3</td> <td>Select</td> <td>0</td> <td>0</td> <td>8</td> <td>1</td> <td>8</td>	newSel26 fu 1303 p3	Select	0	0	8	1	8
newSel28_fu_1335_p3 Select 0 0 8 1 8 newSel29_fu_1343_p3 Select 0 0 8 1 8 newSel2_fu_889_p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel31_fu_1367_p3 Select 0 0 8 1 8 newSel32_fu_1367_p3 Select 0 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel35_fu_1391_p3 Select 0 0 8 1 8 newSel36_fu_2594_p3 Select 0 0 8 1 8 newSel37_fu_2601_p3 Select 0 0 8 1 8 newSel38_fu_2608_p3 Select 0 0 8 1 8 newSel39_fu_2615_p3 Select 0 0 8 1 8 newSel40_fu_262_p3 Select 0 0 8 1 8 newSel40_fu_262_p3 Select	newSel27 fu 1327 p3	Select	0	0	8	1	8
newSel29 fu_1343 p3 Select 0 0 8 1 8 newSel2 fu_889 p3 Select 0 0 8 1 8 newSel30_fu_1351_p3 Select 0 0 8 1 8 newSel31_fu_1359_p3 Select 0 0 8 1 8 newSel31_fu_1375_p3 Select 0 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel35_fu_1391_p3 Select 0 0 8 1 8 newSel35_fu_1391_p3 Select 0 0 8 1 8 newSel35_fu_2601_p3 Select 0 0 8 1 8 newSel37_fu_2601_p3 Select 0 0 8 1 8 newSel38_fu_2608_p3 Select 0 0 8 1 8 newSel39_fu_2615_p3 Select 0 0 8 1 8 newSel40_fu_2612_p3 Select 0 0 8 1 8 newSel40_fu_2622_p3 Select 0 0 8 1 8 newSel40_fu_2622_p3 Select 0 0 8 1 8 newSel41_fu_2629_p3 Sele		Select	0	0	8	1	8
newSel2 fu 889 p3 Select 0 0 8 1 8 newSel30 fu 1351 p3 Select 0 0 8 1 8 newSel31 fu 1359 p3 Select 0 0 8 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 8 newSel32 fu 1367 p3 Select 0 0 8 1 8 newSel33 fu 1375 p3 Select 0 0 8 1 8 newSel34 fu 1383 p3 Select 0 0 8 1 8 newSel35 fu 1391 p3 Select 0 0 8 1 8 newSel36 fu 2594 p3 Select 0 0 8 1 8 newSel37 fu 2601 p3 Select 0 0 8 1 8 newSel38 fu 2608 p3 Select 0 0 8 1 8 newSel39 fu 2615 p3 Select 0 0 8 1 8 newSel39 fu 2615 p3 Select 0 0 8 1 8 newSel40 fu 2622 p3 Select 0 0 8 1 8 newSel41 fu 2629 p3 Select 0 0 8 1 8 newSel42 fu 2648 p3 Select 0 0 8 1 8 newSel43 fu 2655 p3 Sele		Select	0	0	8	1	8
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newSel31_fu_1359_p3 Select 0 8 1 8 newSel32_fu_1367_p3 Select 0 8 1 8 newSel33_fu_1375_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel34_fu_1383_p3 Select 0 0 8 1 8 newSel35_fu_1391_p3 Select 0 0 8 1 8 newSel36_fu_2594_p3 Select 0 0 8 1 8 newSel39_fu_2615_p3 Select 0 0 8 1 8 newSel31_fu_897_p3 Select 0 0 8 1 8 newSel43_fu_2622_p3 Select 0 0 8 1 8 newSel44_fu_2622_p3 Select 0 0 8 1			0	0		1	8
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newSel56_fu_2770_p3				-	_		8
							8
newSel57_fu_2777_p3	newSel57_fu_2777_p3	Select	0	0	8	1	8

Select 0 0 8 1 8 8 1 8 8 1 8 8	newSel58 fu 2784 p3	Select	0	0	8	1	8
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newSel66 fn 2845 p3			0	0		1	
newSel66 fu 2864 p3		Select	0	0		1	
newSelf6 fu 2871 p3	newSel65_fu_2845_p3	Select	0	0	8	1	8
newSel68 fu 2878 p3	newSel66_fu_2864_p3	Select	0	0	8	1	8
newSel69 fu 2885 p3	newSel67_fu_2871_p3	Select	0	0	8	1	8
newSelfo fu 929 p3	newSel68_fu_2878_p3	Select	0	0	8	1	8
newSel70 fu 2892 p3	newSel69_fu_2885_p3	Select	0	0	8	1	8
newSel71_fu_2899_p3	newSel6 fu 929 p3	Select	0	0	8	1	8
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window_buffer_M_0_0_1_fu_2485_p3 Select 0 0 8 1 1 8 window_buffer_M_0_1_3_fu_2478_p3 Select 0 0 8 1 8 window_buffer_M_0_2_fu_2472_p3 Select 0 0 8 1 8 window_buffer_M_1_0_1_fu_2465_p3 Select 0 0 8 1 8 window_buffer_M_1_1_3_fu_2458_p3 Select 0 0 8 1 8 window_buffer_M_1_2_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1	window_buffer2_M_2_1_3_fu_2377_p3	Select	0	0	8	1	8
window_buffer_M_0_1_3_fu_2478_p3 Select 0 0 8 1 1 8 window_buffer_M_0_2_fu_2472_p3 Select 0 0 8 1 1 8 window_buffer_M_1_0_1_fu_2465_p3 Select 0 0 8 1 8 window_buffer_M_1_2_fu_2458_p3 Select 0 0 8 1 8 window_buffer_M_1_2_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1	window_buffer2_M_2_2_fu_2370_p3	Select	0	0	8	1	8
window_buffer_M_0_2_fu_2472_p3 Select 0 0 8 1 8 window_buffer_M_1_0_1_fu_2465_p3 Select 0 0 8 1 8 window_buffer_M_1_0_1_fu_2465_p3 Select 0 0 8 1 8 window_buffer_M_1_2_fu_2458_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1	window_buffer_M_0_0_1_fu_2485_p3	Select	0	0	8	1	8
window_buffer_M_1_0_1 fu_2465_p3 Select 0 0 8 1 8 window_buffer_M_1_1_3 fu_2458_p3 Select 0 0 8 1 8 window_buffer_M_1_2_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1 1	window buffer M 0 1 3 fu 2478 p3	Select	0	0	8	1	8
window_buffer_M_1_0_1 fu_2465_p3 Select 0 0 8 1 1 8 window_buffer_M_1_1_3 fu_2458_p3 Select 0 0 8 1 1 8 window_buffer_M_1_2_fu_2452_p3 Select 0 0 8 1 1 8 window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2431_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1	window buffer M 0 2 fu 2472 p3	Select	0	0	8	1	8
window_buffer_M_1 1_3 fu_2458_p3 Select 0 0 8 1 8 window_buffer_M_1 2_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2 0_1 fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2 1_3 fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2 2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1		Select	0	0	8	1	8
window_buffer_M_1 2_fu_2452_p3 Select 0 0 8 1 8 window_buffer_M_2 0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2 1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2 1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2 2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1				0		1	
window_buffer_M_2_0_1_fu_2445_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1 1				_			
window_buffer_M_2_1_3_fu_2438_p3 Select 0 0 8 1 8 window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1							
window_buffer_M_2_2_fu_2431_p3 Select 0 0 8 1 8 ap_sig_bdd_1315 and 0 0 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1							
ap_sig_bdd_1315 and 0 0 1 1 1 ap_sig_bdd_1328 and 0 0 1 1 1 ap_sig_bdd_1341 and 0 0 1 1 1					_		
ap_sig_bdd_1328 and 0 0 1 1 ap_sig_bdd_1341 and 0 0 1 1 1				-			8
ap_sig_bdd_1341 and 0 0 1 1 1	1- 0			-			1
	1 = 0 =			-	_		
$ ap_sig_bdd_3090 $ and $ 0 0 1 1 1 1 $				_			
	ap_sig_bdd_3090	and	0	0	1	1	1

ap sig bdd 3091	and	0	0	1	1	1
ap sig bdd 3094	and	0	0	1	1	1
ap sig bdd 3096	and	0	0	1	1	1
ap sig bdd 3098	and	0	0	1	1	1
ap sig bdd 710	and	0	0	1	1	1
ap sig bdd 755	-	0	0		1	1
1= 0= =	and		—— 	1		
or_cond_20_fu_2164_p2	and	0	0	1	1	1
sel_tmp11_fu_792_p2	and	0	0	1	1	1
sel_tmp12_fu_798_p2	and	0	0	1	1	1
sel_tmp14_fu_819_p2	and	0	0	1	1	1
sel_tmp15_fu_825_p2	and	0	0	1	1	1
sel_tmp1_fu_759_p2	and	0	0	1	1	1
sel_tmp20_fu_1106_p2	and	0	0	1	1	1
sel_tmp22_fu_1117_p2	and	0	0	1	1	1
sel_tmp24_fu_1128_p2	and	0	0	1	1	1
sel_tmp25_fu_1134_p2	and	0	0	1	1	1
sel_tmp28_fu_1150_p2	and	0	0	1	1	1
sel_tmp29_fu_1156_p2	and	0	0	1	1	1
sel_tmp31_fu_1177_p2	and	0	0	1	1	1
sel_tmp32_fu_1183_p2	and	0	0	1	1	1
sel_tmp35_fu_1468_p2	and	0	0	1	1	1
sel_tmp37_fu_1504_p2	and	0	0	1	1	1
sel_tmp39_fu_1540_p2	and	0	0	1	1	1
sel_tmp41_fu_1576_p2	and	0	0	1	1	1
sel_tmp43_fu_1612_p2	and	0	0	1	1	1
sel_tmp45_fu_1648_p2	and	0	0	1	1	1
sel_tmp47_fu_1684_p2	and	0	0	1	1	1
sel_tmp49_fu_1720_p2	and	0	0	1	1	1
sel_tmp4_fu_770_p2	and	0	0	1	1	1
sel_tmp51_fu_1756_p2	and	0	0	1	1	1
sel tmp53 fu 1792 p2	and	0	0	1	1	1
sel tmp55 fu 1828 p2	and	0	0	1	1	1
sel tmp57 fu 1864 p2	and	0	0	1	1	1
sel tmp59 fu 1900 p2	and	0	0	1	1	1
sel_tmp61_fu_1936_p2	and	0	0	1	1	1
sel tmp63 fu 1972 p2	and	0	0	1	1	1
sel tmp65 fu 2008 p2	and	0	0	1	1	1
sel tmp67 fu 2044 p2	and	0	0	1	1	1
sel tmp69 fu 2080 p2	and	0	0	1	1	1
sel tmp6 fu 748 p2	and	0	0	1	1	1
sel tmp7 fu 776 p2	and	0	0	1	1	1
tmp6 fu 1100 p2	and	0	0	1	1	1
tmp fu 742 p2	and	0	0	1	1	1
exitcond1 fu 2104 p2	icmp	0	0	8	9	6
exitcond8 fu 683 p2	icmp	0	0	2	4	4
exitcond flatten fu 2092 p2	icmp	0	0	20	18	18
exitcond fu 1041 p2	icmp	0	0	2	4	4
icmp1 fu 2158 p2	icmp	0	0	7	8	1
icmp fu 2142 p2	icmp	0	0	7	8	1
isIter0 fu 695 p2	icmp	0	0	2	4	1
isIter fu 1053 p2	icmp	0	0	2	4	1
		0	0	2	2	1
sel_tmp10_fu_787_p2	icmp	0	0	2	2	1
sel_tmp13_fu_814_p2	icmp		 -			1
sel_tmp17_fu_1085_p2	icmp	0	0	2	2	
sel_tmp18_fu_1090_p2	icmp	0	0	2	2	1
sel_tmp19_fu_1095_p2	icmp	0	0	2	2	1
sel_tmp21_fu_1112_p2	icmp	0	0	2	2	1
sel_tmp23_fu_1123_p2	icmp	0	0	2	2	1
sel_tmp26_fu_1140_p2	icmp	0	0	2	2	1
sel_tmp27_fu_1145_p2	icmp	0	0	2	2	1
sel_tmp2_fu_765_p2	icmp	0	0	2	2	1
sel_tmp30_fu_1172_p2	icmp	0	0	2	2	1
sel_tmp3_fu_732_p2	icmp	0	0	2	2	1

sel tmp5 fu 737 p2	icmp	0	0	2	2	1
sel tmp8 fu 782 p2	icmp	0	0	2	2	1
sel tmp9 fu 754 p2	icmp	0	0	2	2	1
sel tmp fu 727 p2	icmp	0	0	2	2	1
tmp 10 fu 1480 p2	icmp	0	0	7	8	1
tmp 14 fu 1166 p2	icmp	0	0	2	2	1
tmp 15 fu 1486 p2	icmp	0	0	7	8	1
tmp 16 fu 1492 p2	icmp	0	0	7	8	2
tmp 21 0 1 fu 1516 p2		0	$\frac{0}{0}$	7	8	1
tmp_21_0_2_fu_1588_p2	icmp	0	0	7	8	1
tmp 21 1 1 fu 1732 p2	· -	0	$\frac{0}{0}$	7	8	1
tmp 21 1 2 fu 1804 p2	icmp	0	0	7	8	1
1	icmp		0	7	8	1
tmp_21_1_fu_1660_p2	icmp	0	_	7		1
tmp_21_2_1_fu_1948_p2	icmp	0	0	7	8	
tmp_21_2_2_fu_2020_p2	icmp	0	0		8	1
tmp_21_2_fu_1876_p2	icmp	0	0	7	8	1
tmp_22_0_1_fu_1522_p2	icmp	0	0	7	8	1
tmp_22_0_2_fu_1594_p2	icmp	0	0	7	8	1
tmp_22_1_1_fu_1738_p2	icmp	0	0	7	8	1
tmp_22_1_2_fu_1810_p2	icmp	0	0	7	8	1
tmp_22_1_fu_1666_p2	icmp	0	0	7	8	1
tmp_22_2_1_fu_1954_p2	icmp	0	0	7	8	1
tmp_22_2_fu_2026_p2	icmp	0	0	7	8	1
tmp_22_2_fu_1882_p2	icmp	0	0	7	8	1
tmp_23_0_1_fu_1528_p2	icmp	0	0	7	8	2
tmp_23_0_2_fu_1600_p2	icmp	0	0	7	8	2
tmp_23_1_1_fu_1744_p2	icmp	0	0	7	8	2
tmp_23_1_2_fu_1816_p2	icmp	0	0	7	8	2
tmp_23_1_fu_1672_p2	icmp	0	0	7	8	2
tmp_23_2_1_fu_1960_p2	icmp	0	0	7	8	2
tmp_23_2_2_fu_2032_p2	icmp	0	0	7	8	2
tmp_23_2_fu_1888_p2	icmp	0	0	7	8	2
tmp_24_0_1_fu_1552_p2	icmp	0	0	7	8	1
tmp_24_0_2_fu_1624_p2	icmp	0	0	7	8	1
tmp_24_1_1_fu_1768_p2	icmp	0	0	7	8	1
tmp_24_1_2_fu_1840_p2	icmp	0	0	7	8	1
tmp_24_1_fu_1696_p2	icmp	0	0	7	8	1
tmp_24_2_1_fu_1984_p2	icmp	0	0	7	8	1
tmp_24_2_2_fu_2056_p2	icmp	0	0	7	8	1
tmp_24_2_fu_1912_p2	icmp	0	0	7	8	1
tmp 25 0 1 fu 1558 p2	icmp	0	0	7	8	1
tmp 25 0 2 fu 1630 p2	icmp	0	0	7	8	1
tmp 25 1 1 fu 1774 p2	icmp	0	0	7	8	1
tmp 25 1 2 fu 1846 p2	icmp	0	0	7	8	1
tmp 25 1 fu 1702 p2	icmp	0	0	7	8	1
tmp 25 2 1 fu 1990 p2	icmp	0	0	7	8	1
tmp_25_2_2_fu_2062_p2	icmp	0	0	7	8	1
tmp_25_2_fu_1918_p2	icmp	0	0	7	8	1
tmp 26 0 1 fu 1564 p2	icmp	0	0	7	8	2
tmp_26_0_2_fu_1636_p2	icmp	0	0	7	8	2
tmp_26_1_1_fu_1780_p2	icmp	0	0	7	8	2
tmp 26 1 2 fu 1852 p2	icmp	0	0	7	8	2
tmp 26 1 fu 1708 p2	icmp	0	0	7	8	2
tmp 26 2 1 fu 1996 p2	icmp	0	0	7	8	2
tmp_26_2_2_fu_2068_p2	icmp	0	0	7	8	2
tmp_26_2_fu_1924_p2	icmp	0	0	7	8	2
tmp 5 fu 808 p2	icmp	0	$\frac{0}{0}$	2	2	1
tmp 6 fu 1450 p2		0	0	7	8	1
tmp_9_fu_1456_p2	icmp	0	$\frac{0}{0}$	7	8	2
tmp s fu 1444 p2		0	0	7	8	1
	icmp	0	0	1	1	1
ap_sig_bdd_811	or	0	0	1	1	
ap_sig_bdd_857 or cond10 fu 1582 p2	or	0	0	1	1	1
01_01_01u_1302_p2	or	U	U	1	1	1

or_cond11_fu_1618_p2	or	0	0	1	1	1
or cond12 fu 1654 p2	or	0	0	1	1	1
or cond13 fu 1690 p2	or	0	0	1	1	1
or cond14 fu 1726 p2	or	0	0	1	1	1
or cond15 fu 1762 p2	or	0	0	1	1	1
or cond16 fu 1798 p2	or	0	0	1	1	1
or cond17 fu 1834 p2	or	0	0	1	1	1
or cond18 fu 1870 p2	or	0	0	1	1	1
or cond19 fu 1906 p2	or	0	0	1	1	1
or cond1 fu 837 p2	or	0	0	1	1	1
or cond20 fu 1942 p2	or	0	0	1	1	1
or cond21 fu 1978 p2	or	0	0	1	1	1
or cond22 fu 2014 p2	or	0	0	1	1	1
or cond23 fu 2050 p2	or	0	0	1	1	1
or cond24 fu 2086 p2	or	0	0	1	1	1
or cond25 fu 1189 p2	or	0	0	1	1	1
or cond26 fu 1195 p2	or	0	0	1	1	1
or cond27 fu 1201 p2	or	0	0	1	1	1
or cond28 fu 1207 p2	or	0	0	1	1	1
or cond29 fu 1213 p2		0	$\frac{0}{0}$	1	1	1
or cond2 fu 843 p2	or	0	$\frac{0}{0}$	1	1	1
	or		_			1
or_cond30_fu_1219_p2	or	0	0	1	1	1
or_cond31_fu_1225_p2	or	0	0	1	1	1
or_cond3_fu_849_p2	or	0	0	1	1	1
or_cond4_fu_855_p2	or	0	0	1	1	1
or_cond5_fu_861_p2	or	0	0	1	1	1
or_cond6_fu_867_p2	or	0	0	1	1	1
or_cond7_fu_1474_p2	or	0	0	1	1	1
or_cond8_fu_1510_p2	or	0	0	1	1	1
or_cond9_fu_1546_p2	or	0	0	1	1	1
or_cond_fu_831_p2	or	0	0	1	1	1
tmp_13_fu_1162_p2	or	0	0	2	2	2
tmp_2_fu_804_p2	or	0	0	2	2	2
sel_tmp34_fu_1462_p2	xor	0	0	2	1	2
sel_tmp36_fu_1498_p2	xor	0	0	2	1	2
sel_tmp38_fu_1534_p2	xor	0	0	2	1	2
sel_tmp40_fu_1570_p2	xor	0	0	2	1	2
sel_tmp42_fu_1606_p2	xor	0	0	2	1	2
sel_tmp44_fu_1642_p2	xor	0	0	2	1	2
sel_tmp46_fu_1678_p2	xor	0	0	-	1	2
sel_tmp48_fu_1714_p2	xor	0	0	2	1	2 2 2
sel_tmp50_fu_1750_p2	xor	0	0	2	1	2
sel_tmp52_fu_1786_p2	xor	0	0	2	1	2
sel_tmp54_fu_1822_p2	xor	0	0	2	1	2
sel_tmp56_fu_1858_p2	xor	0	0	2	1	2
sel_tmp58_fu_1894_p2	xor	0	0	2	1	2 2 2 2
sel_tmp60_fu_1930_p2	xor	0	0	2	1	2
sel_tmp62_fu_1966_p2	xor	0	0	2	1	2
sel_tmp64_fu_2002_p2	xor	0	0	2	1	2 2
sel_tmp66_fu_2038_p2	xor	0	0	2	1	2
sel_tmp68_fu_2074_p2	xor	0	0	2	1	2
Total	348	1	0	2046	1271	1682
iplexer						

• Multiplexer

Name	LUT	Input Size	Bits	Total Bits
ap_NS_fsm	8	10	4	40
c_reg_586	9	2	9	18
indvar1_phi_fu_545_p4	4	2	4	8
indvar1_reg_541	4	2	4	8
indvar_flatten_reg_564	18	2	18	36
indvar_phi_fu_522_p4	4	2	4	8
indvar_reg_518	4	2	4	8
phi_mul1_reg_553	8	2	8	16
phi_mul_reg_530	8	2	8	16

r_phi_fu_579_p4	9	2	9	18
r_reg_575	9	2	9	18
user_axi_in2_ARADDR	32	3	32	96
user_axi_in2_ARLEN	32	3	32	96
user_axi_in_ARADDR	32	3	32	96
user_axi_in_ARLEN	32	3	32	96
Total	213	42	209	578

• Register

Name	FF	LUT	Bits	Const Bits
ap_CS_fsm	4	0	4	0
ap_reg_ioackin_user_axi_in2_ARREADY	1	0	1	0
ap_reg_ioackin_user_axi_in_ARREADY	1	0	1	0
ap_reg_ioackin_user_axi_out2_AWREADY	1	0	1	0
ap_reg_ioackin_user_axi_out2_WREADY	1	0	1	0
ap_reg_ioackin_user_axi_out_AWREADY	1	0	1	0
ap_reg_ioackin_user_axi_out_WREADY	1	0	1	0
ap_reg_ppiten_pp0_it0	1	0	1	0
ap_reg_ppiten_pp0_it1	1	0	1	0
ap_reg_ppiten_pp0_it2	1	0	1	0
ap_reg_ppiten_pp0_it3	1	0	1	0
ap_reg_ppiten_pp0_it4	1	0	1	0
ap_reg_ppiten_pp0_it5	1	0	1	0
ap_reg_ppiten_pp0_it6	1	0	1	0
ap_reg_ppiten_pp0_it7	1	0	1	0
ap_reg_ppiten_pp1_it0	1	0	1	0
ap_reg_ppiten_pp1_it1	1	0	1	0
ap_reg_ppiten_pp1_it2	1	0	1	0
ap_reg_ppiten_pp1_it3	1	0	1	0
ap_reg_ppiten_pp1_it4	1	0	1	0
ap_reg_ppiten_pp1_it5	1	0	1	0
ap_reg_ppiten_pp1_it6	1	0	1	0
ap_reg_ppiten_pp1_it7	1	0	1	0
ap_reg_ppiten_pp2_it0	1	0	1	0
ap_reg_ppiten_pp2_it1	1	0	1	0
ap_reg_ppiten_pp2_it10	1	0	1	0
ap_reg_ppiten_pp2_it11	1	0	1	0
ap_reg_ppiten_pp2_it12	1	0	1	0
ap_reg_ppiten_pp2_it2	1	0	1	0
ap_reg_ppiten_pp2_it3	1	0	1	0
ap_reg_ppiten_pp2_it4	1	0	1	0
ap_reg_ppiten_pp2_it5	1	0	1	0
ap_reg_ppiten_pp2_it6	1	0	1	0
ap_reg_ppiten_pp2_it7 ap_reg_ppiten_pp2_it8	1	0	1	0
ap reg ppiten_pp2_ito ap reg ppiten pp2 it9	1	0	1	0
ap_reg_ppstg_line_buffer2_M_0_addr_reg_3830_pp2_it7	9	0	9	0
ap reg ppstg line buffer M 0 addr reg 3818 pp2 it7	9	0	9	0
c_mid2_reg_3754	9	0	9	0
c_reg_586	9	0	9	0
exitcond8 reg 3196	1	0	1	0
exitcond flatten reg 3745	1	0	1	0
exitcond reg 3331	1	0	1	0
filter base2 reg 3190	32	0	32	0
filter base reg 3184	32	0	32	0
filter buffer2 0 0 s fu 176	8	0	8	0
filter buffer2 0 1 s fu 180	8	0	8	0
filter buffer2 0 2 s fu 184	8	0	8	0
filter_buffer2_1_0_s_fu_188	8	0	8	0
filter_buffer2_1_1_s_fu_192	8	0	8	0
filter_buffer2_1_2_s_fu_196	8	0	8	0
filter buffer2 2 0 s fu 200	8	0	8	0
filter buffer2 2 1 s fu 204	8	0	8	0
filter buffer2 2 2 s fu 208	8	0	8	0

filter buffer 0 0 s reg 506	8	0	8	0
filter buffer 0 1 s reg 494	8	0	8	0
filter buffer 0 2 s reg 482	8	0	8	0
filter buffer 1 0 s reg 470	8	0	8	0
filter buffer 1 1 s reg 458	8	0	8	0
filter buffer 1 2 s reg 446	8	0	8	0
filter_buffer_2_0_s_reg_434	8	0	8	0
filter_buffer_2_1_s_reg_422	8	0	8	0
filter_buffer_2_2_s_reg_410	8	0	8	0
icmp_reg_3769	1	0	1	0
indvar1 reg 541	4	0	4	0
indvar flatten reg 564	18	0	18	0
indvar_natch_reg_304	4	0	4	0
indvar_next_reg_3333	4	0	4	0
indvar_reg_518	4	0	4	0
isIter0 reg 3205	1	0	1	0
isIter reg 3340	1	0	1	0
line buffer2 M 0 addr reg 3830	9	0	9	0
line buffer2 M 1 addr reg 3824	9	0	9	0
line buffer M 0 addr reg 3818	9	0	9	0
line buffer M 1 addr reg 3812	9	0	9	0
or_cond10_reg_3530	1	0	1	0
or_cond11_reg_3545	1	0	1	0
	1	0	1	0
or_cond12_reg_3560	1			
or_cond13_reg_3575	1	0	1	0
or_cond14_reg_3590	1			
or_cond15_reg_3605 or_cond16_reg_3620	1	0	1	0
	1			0
or_cond17_reg_3635	1	0	1	0
or_cond18_reg_3650	1	0	1	0
or_cond19_reg_3665 or_cond20_reg_3680	1	0	1	0
or cond21 reg 3695	1	0	1	0
or cond22 reg 3710	1	0	1	0
or cond23 reg 3725	1	0	1	0
or cond24 reg 3740	1	0	1	0
or_cond7_reg_3485	1	0	1	0
or_cond8_reg_3500	1	0	1	0
or_cond9_reg_3515	1	0	1	0
or_cond_20_reg_3791	1	0	1	0
p_t2_reg_3349	2	0	2	0
p t reg 3214	2	0	2	0
phi_mul1_reg_553	8	0	8	0
phi mul reg 530	8	0	8	0
pixel in 2 0 data reg	32	0	32	0
pixel in 2 0 vld reg	0	0	1	1
pixel in 0 data reg	32	0	32	0
pixel_in_0_vld_reg	0	0	1	1
pixel out2 0 data reg	32	0	32	0
pixel out2 0 vld reg	0	0	1	1
pixel_out_0_data_reg	32	0	32	0
pixel_out_0_vld_reg	0	0	1	1
r mid2 reg 3761	9	0	9	0
r reg 575	9	0	9	0
reg_625	8	0	8	0
reg_630	8	0	8	0
return value 1 reg 3851	8	0	8	0
return value 2 reg 3856	8	0	8	0
return value 3 reg 3861	8	0	8	0
return value reg 3846	8	0	8	0
sel tmp35 reg 3480	1	0	1	0
sel_tmp37_reg_3495	1	0	1	0
sel tmp39 reg 3510	1	0	1	0
	-	0		U

sel_tmp41_reg_3525	1	0	1	0
sel_tmp43_reg_3540	1	0	1	0
sel tmp45 reg 3555	1	0	1	0
sel tmp47 reg 3570	1	0	1	0
sel tmp49 reg 3585	1	0	1	0
sel tmp51 reg 3600	1	0	1	0
sel tmp53 reg 3615	1	0	1	0
sel_tmp55_reg_3630	1	0	1	0
sel_tmp57_reg_3645	1	0	1	0
sel_tmp59_reg_3660	1	0	1	0
sel_tmp61_reg_3675	1	0	1	0
sel_tmp63_reg_3690	1	0	1	0
sel_tmp65_reg_3705	1	0	1	0
sel_tmp67_reg_3720	1	0	1	0
sel_tmp69_reg_3735	1	0	1	0
store_data2_2_2_reg_3871	8	0	8	0
store_data_2_2_2_reg_3866	8	0	8	0
tmp_10_cast_reg_3174	33	0	33	0
tmp_16_reg_3490	1	0	1	0
tmp_17_reg_3358	2	0	2	0
tmp_21_cast_reg_3179	33	0	33	0
tmp_23_0_1_reg_3505	1	0	1	0
tmp_23_0_2_reg_3535	1	0	1	0
tmp_23_1_1_reg_3595	1	0	1	0
tmp_23_1_2_reg_3625	1	0	1	0
tmp_23_1_reg_3565	1	0	1	0
tmp_23_2_1_reg_3685	1	0	1	0
tmp_23_2_2_reg_3715	1	0	1	0
tmp_23_2_reg_3655	1	0	1	0
tmp_26_0_1_reg_3520	1	0	1	0
tmp_26_0_2_reg_3550	1	0	1	0
tmp_26_1_1_reg_3610	1	0	1	0
tmp_26_1_2_reg_3640	1	0	1	0
tmp_26_1_reg_3580	1	0	1	0
tmp_26_2_1_reg_3700	1	0	1	0
tmp_26_2_2_reg_3730	1	0	1	0
tmp_26_2_reg_3670	1	0	1	0
tmp_3_reg_3223	2	0	2	0
tmp_6_cast_reg_3164	33	0	33	0
tmp_9_cast_reg_3169	33	0	33	0
tmp_9_reg_3475	1	0	1	0
user_axi_in2_addr_reg_3806	32	0	32	0
user_axi_in_addr_reg_3800	32	0	32	0
user_axi_out2_addr_reg_3841	32	0	32	0
user_axi_out_addr_reg_3836	32	0	32 8	0
window_buffer2_M_0_0_2_fu_248	8	0	8	0
window_buffer2_M_0_0_fu_252	8	0	8	
window_buffer2_M_0_1_fu_256	8	0	8	0
window_buffer2_M_1_0_2_fu_260 window buffer2 M 1 0 fu 264	8	0	8	0
window_buffer2_M_1_0_fu_264 window buffer2_M_1_1 fu_268	8	0	8	0
window_buffer2_M_1_1_fu_208 window_buffer2_M_2_0_2_fu_272	8	0	8	0
window_buffer2_M_2_0_2_fu_2/2 window buffer2_M_2_0 fu_2/6	8	0	8	0
window_buffer2_M_2_0_fu_2/6 window buffer2_M_2_1 fu_280	8	0	8	0
window_buffer_M_2_1_fu_280 window buffer_M_0_0_2_fu_212	8	0	8	0
window buffer M 0 0 fu 216	8	0	8	0
window_buffer_M_0_0_fu_210 window buffer_M_0_1 fu_220	8	0	8	0
window_buffer M 1 0 2 fu 224	8	0	8	0
window buffer M 1 0 fu 228	8	0	8	0
window_buffer_M_1_0_fu_228 window buffer M_1_1 fu_232	8	0	8	0
window_buffer_M_1_1_fu_232 window buffer M 2 0 2 fu 236	8	0	8	0
window_buffer_M_2_0_2_fu_236 window buffer_M_2_0 fu_240	8	0	8	0
window_buffer_M_2_0_fu_240 window buffer_M_2_1_fu_244	8	0	8	0
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c_mid2_reg_3754	0	9	9	0
exitcond8_reg_3196	0	1	1	0
exitcond_flatten_reg_3745	0	1	1	0
exitcond_reg_3331	0	1	1	0
icmp_reg_3769	0	1	1	0
or_cond_20_reg_3791	0	1	1	0
p_t2_reg_3349	0	2	2	0
p_t_reg_3214	0	2	2	0
r_mid2_reg_3761	0	9	9	0
user_axi_out2_addr_reg_3841	0	32	32	0
user_axi_out_addr_reg_3836	0	32	32	0
Total	1052	91	1147	4

Interface

• Summary

RTL Ports	Dir	Bits	Protocol	Source Object	C Type
s axi user axi4lite config AWVALID	in	1	s axi	user axi4lite config	scalar
s axi user axi4lite config AWREADY	out	1	s axi	user axi4lite config	scalar
s axi user axi4lite config AWADDR	in	6	s axi	user axi4lite config	scalar
s axi user axi4lite config WVALID	in	1	s axi	user axi4lite config	scalar
s axi user axi4lite config WREADY	out	1	s axi	user axi4lite config	scalar
s axi user axi4lite config WDATA	in	32	s axi	user axi4lite config	scalar
s axi user axi4lite config WSTRB	in	4	s axi	user axi4lite config	scalar
s axi user axi4lite config ARVALID	in	1	s axi	user axi4lite config	scalar
s axi user axi4lite config ARREADY	out	1	s axi	user axi4lite config	scalar
s axi user axi4lite config ARADDR	in	6	s axi	user axi4lite config	scalar
s axi user axi4lite config RVALID	out	1	s axi	user axi4lite config	scalar
s axi user axi4lite config RREADY	in	1	s axi	user axi4lite config	scalar
s axi user axi4lite config RDATA	out	32	s axi	user axi4lite config	scalar
s axi user axi4lite config RRESP	out	2	s axi	user axi4lite config	scalar
s axi user axi4lite config BVALID	out	1	s axi	user axi4lite config	scalar
s axi user axi4lite config BREADY	in	1	s axi	user axi4lite config	scalar
s axi user axi4lite config BRESP	out	2	s axi	user axi4lite config	scalar
ap_clk	in	1		HW 2DConv Mmap 3	return value
ap rst n	in	_		HW 2DConv Mmap 3	
interrupt	out	_	1	HW 2DConv Mmap 3	
m axi user axi in AWVALID	out	<u> </u>	m axi	user axi in	pointer
m axi user axi in AWREADY	in	1	m axi	user axi in	pointer
m axi user axi in AWADDR	out	32	m axi	user axi in	pointer
m axi user axi in AWID	out	1	m axi	user axi in	pointer
m axi user axi in AWLEN	out	8	m axi	user axi in	pointer
m axi user axi in AWSIZE	out	—	m axi	user axi in	pointer
m axi user axi in AWBURST	out	—	m axi	user axi in	pointer
m axi user axi in AWLOCK	out	2	m axi	user axi in	pointer
m axi user axi in AWCACHE	out	4	m axi	user axi in	pointer
m axi user axi in AWPROT	out	3	m axi	user axi in	pointer
m axi user axi in AWOOS	out	—	m axi	user axi in	pointer
m axi user axi in AWREGION	out	4	m axi	user axi in	pointer
m axi user axi in AWUSER	out		m axi	user axi in	pointer
m axi user axi in WVALID	out		m axi	user axi in	pointer
m axi user axi in WREADY	in	1	m axi	user axi in	pointer
m axi user axi in WDATA	out	32	m axi	user axi in	pointer
m axi user axi in WSTRB	out	4	m axi	user axi in	pointer
m axi user axi in WLAST	out	-	m axi		pointer
m_axi_user_axi_in_WID	out		m axi	 .	pointer
m axi user axi in WUSER	out	<u> </u>	m axi		pointer
m axi user axi in ARVALID	out	-	m_axi		pointer
m axi user axi in ARREADY	in	_	m axi		pointer
m_axi_user_axi_in_ARADDR	out	-	m axi		pointer
m axi user axi in ARID	out	-	m axi		pointer
m axi user axi in ARLEN	out	-	m axi		pointer
m axi user axi in ARSIZE	out		m axi		pointer
III_GAT_GOT_GAT_III_ARGIZE	out		111_4X1	usci_axi_iii	pomici

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m_axi_user_axi_in_ARBURST	out	2	m_axi	user_axi_in	pointer
m_axi_user_axi_in_ARLOCK	out	2	m_axı	user_axi_in	pointer
m_axi_user_axi_in_ARCACHE	out	4	m_axı	user_axi_in	pointer
m_axi_user_axi_in_ARPROT	out	3	m_axı	user_ax1_in	pointer
m_axi_user_axi_in_ARQOS	out	4	m_axı	user_ax1_1n	pointer
m_axi_user_axi_in_ARREGION	out	4	m_axi	user_axi_in	pointer
m_axi_user_axi_in_ARUSER	out	1	m_axi	user_axi_in	pointer
m_axi_user_axi_in_RVALID	in	1	m_axi	user_axi_in	pointer
m_axi_user_axi_in_RREADY	out	1	m_axi	user_axi_in	pointer
m_axi_user_axi_in_RDATA	in	32	m_axi	user_axi_in	pointer
m_axi_user_axi_in_RLAST	in	1	m_axi	user_axi_in	pointer
m axi user axi in RID	in	1	m axi	user axi in	pointer
m axi user axi in RUSER	in	1	m axi	user axi in	pointer
m axi user axi in RRESP	in	2	m axi	user axi in	pointer
m axi user axi in BVALID	in	1	m axi	user axi in	pointer
m axi user axi in BREADY	out	1	m axi	user axi in	pointer
m axi user axi in BRESP	in	2	m axi	user axi in	pointer
m axi user axi in BID	in	1	m axi	user axi in	pointer
DITGED	in	1			
	+		m_ax1	user_axi_in	pointer
m_axi_user_axi_in2_AWVALID	out	1	m_axı	user_axi_in2	pointer
m_axi_user_axi_in2_AWREADY	in	1	m_axı	user_axi_in2	pointer
m_axi_user_axi_in2_AWADDR	out	32	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWID	out	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWLEN	out	8	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWSIZE	out	3	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWBURST	out	2	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWLOCK	out	2	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_AWCACHE	out	4	m_axi	user_axi_in2	pointer
m axi user axi in2 AWPROT	out	3	m axi	user axi in2	pointer
m axi user axi in2 AWQOS	out	4	m axi	user axi in2	pointer
m axi user axi in2 AWREGION	out	4	m axi	user axi in2	pointer
m axi user axi in2 AWUSER	out	1	m axi	user axi in2	pointer
m axi user axi in2 WVALID	out	1	m axi	user axi in2	pointer
m axi user axi in2 WREADY	in	1	m axi	user axi in2	pointer
m axi user axi in2 WDATA	+	32			<u> </u>
	out	4	m_axi		pointer
	out		m_ax1	user_axi_in2	pointer
m_axi_user_axi_in2_WLAST	out	1	m_axı	user_axi_in2	pointer
m_axi_user_axi_in2_WID	out	1	m_axı	user_axi_in2	pointer
m_axi_user_axi_in2_WUSER	out	1	m_axı	user_axi_in2	pointer
m_axi_user_axi_in2_ARVALID	out		m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_ARREADY	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_ARADDR	out	32	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_ARID	out	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_ARLEN	out	8	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_ARSIZE	out	3	m_axi	user_axi_in2	pointer
m axi user axi in2 ARBURST	out	2	m axi	user axi in2	pointer
m axi user axi in2 ARLOCK	out	_	m axi	user axi in2	pointer
m axi user axi in2 ARCACHE	out	4	m axi	user axi in2	pointer
m axi user axi in2 ARPROT	out	3	m_axi	user axi in2	pointer
m axi user axi in2 ARQOS	out		m_axi	user axi in2	pointer
m axi user axi in2 ARREGION	out	4	m axi	user axi in2	pointer
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m_axi_user_axi_in2_ARUSER	out		m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RVALID	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RREADY	out	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RDATA	in	32	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RLAST	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RID	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RUSER	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_RRESP	in	2	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_BVALID	in	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_BREADY	out	1	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_BRESP	in	2	m_axi	user_axi_in2	pointer
m_axi_user_axi_in2_BID	in	1	m_axi	user_axi_in2	pointer
,	-				•

m axi user axi out AWVALID	m axi user axi in2 BUSER	in	1	m axi	user axi in2	pointer
		+				
		+ -				
		+				
			-			
m axi user axi out AWSIZE m axi user axi out AWBURST out 2 m axi user axi out pointer m axi user axi out AWCACHE m axi user axi out pointer m axi user axi out AWCACHE m axi user axi out way out m axi user axi out AWCACHE m axi user axi out pointer m axi user axi out AWROOS out 4 m axi user axi out pointer m axi user axi out AWROOS out 4 m axi user axi out pointer m axi user axi out AWREGION out 1 m axi user axi out pointer m axi user axi out AWREGION out 1 m axi user axi out pointer m axi user axi out WACALID out 1 m axi user axi out pointer m axi user axi out WACALID out 1 m axi user axi out pointer m axi user axi out WACALID out 2 m axi user axi out pointer m axi user axi out WACALID out 3 m axi user axi out pointer m axi user axi out WACALID out 1 m axi user axi out pointer m axi user axi out WACALID out 1 m axi user axi out WACALID out 1 m axi user axi out pointer m axi user axi out WIDE out 1 m axi user axi out pointer m axi user axi out WIDE out 1 m axi user axi out pointer m axi user axi out WIDE out 1 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi user axi out ARALID out 3 m axi user axi out pointer m axi user axi out ARALID out 3 m axi user axi out pointer m axi user axi out ARALID out 3 m axi user axi out pointer m axi user axi out ARALID out 3 m axi user axi out pointer m axi user axi out ARALID out 3 m axi user axi out pointer m axi user axi out ARALID out 4 m axi user axi out pointer m axi user axi out ARALID out 5 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi user axi out ARALID out 2 m axi user axi out pointer m axi user axi out ARALID out 1 m axi user axi out pointer m axi						-
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m_axi_user_axi_out2_ARLEN	out	8	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARSIZE	out	3	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARBURST	out	2	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARLOCK	out	2	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARCACHE	out	4	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARPROT	out	3	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARQOS	out	4	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARREGION	out	4	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_ARUSER	out	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RVALID	in	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RREADY	out	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RDATA	in	32	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RLAST	in	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RID	in	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RUSER	in	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_RRESP	in	2	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_BVALID	in	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_BREADY	out	1	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_BRESP	in	2	m_axi	user_axi_out2	pointer
m_axi_user_axi_out2_BID	in	1	m_axi	user_axi_out2	pointer
m axi user axi out2 BUSER	in	1	m axi	user axi out2	pointer