CHIPNET

(a) Communication scenario of multiple R2B devices.
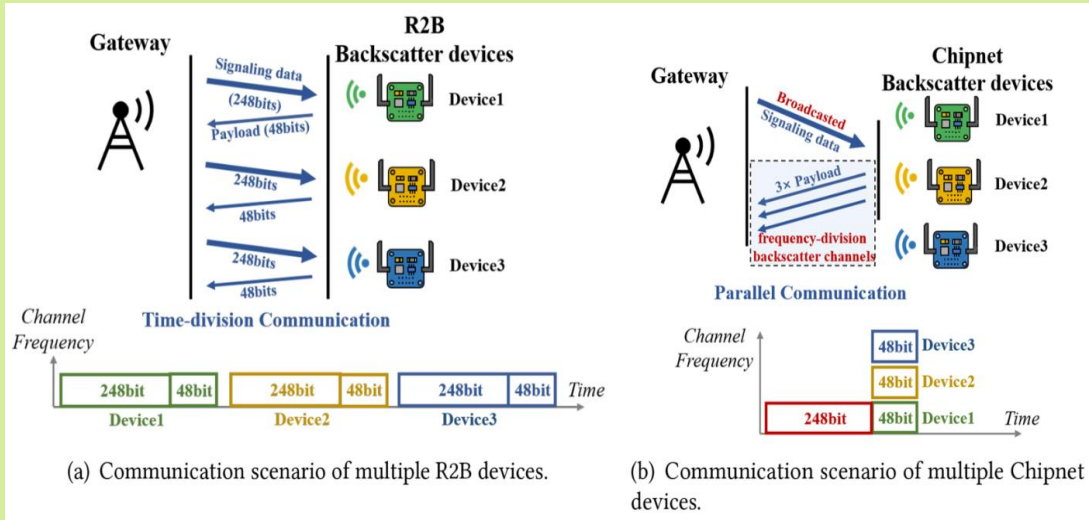
(b) Communication scenario of multiple Chipnet devices.

Challenges: Signal Blocking, Concurrency, Distance

- Processor-free Architecture (R2B)
- Frequency Division (Parallel)
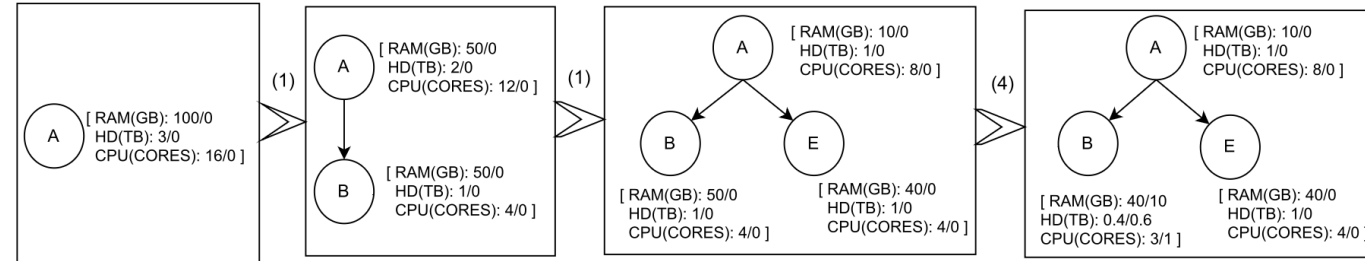- Hardware Simplification

HIERARCHICAL NAMESPACE

FIGURE 13. Example of a sequence of direct graph transformations.

Challenges: Practical Implementation, Rigid Resource Constraints, Adoption

- Resource Isolation with Graph Transform
- Multiple Asynchronous Session Type (MPST) Protocol
- Security Profile Management (Namespaces)