

Product Description

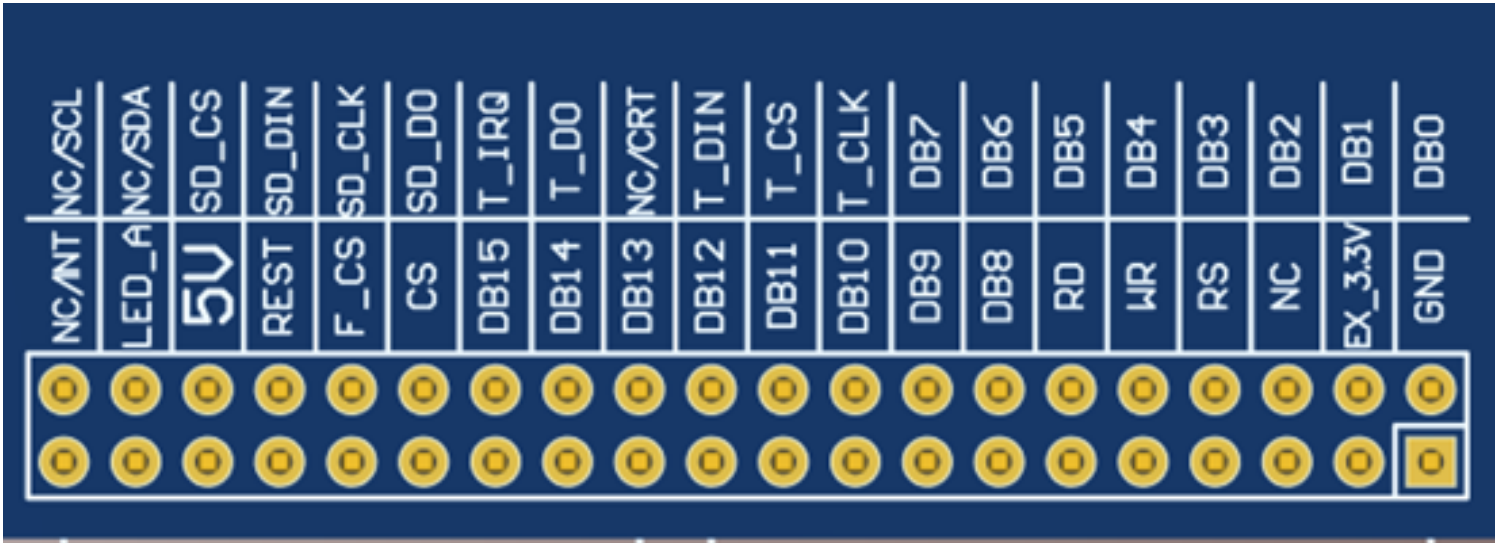
- 7.0-inch high-definition color screen, supporting 16bit RGB 65K color display, with rich display colors
- Support 800x480 resolution, and the display effect is very clear
- Using 16-bit parallel bus transmission with fast transfer speed
- Compatible with capacitive touch screen and resistive touch screen
- Support SPI flash
- With SD card slot, support SD card expansion
- Support three backlight control modes (always on, GPIO control, PWM control)
- Provides a rich sample program for STM32 and C51 platforms
- Military-grade process standards, long-term stable work
- Provide underlying driver technical support

Product Parameters

Name	Parameter
Display Color	16BIT RGB 65K color
SKU	MRB7963
Screen Size	7.0(inch)
Screen Type	TFT

Driver IC	SSD1963
Resolution	800x480(Pixel)
Module Interface	16Bit parallel interface
Active Area	154.08x85.92(mm)
Touch Screen Type	Capacitive touch screen or Resistive touch screen
Touch IC	GT911 (capacitive touch), XPT2046 (resistive touch)
Module PCB Size	181.00x108.00(mm)
Operating Temperature	-10°C~60°C
Storage Temperature	-20°C~70°C
Operating Voltage	5V
IO Voltage	3.3V
Power Consumption	TBD
Rough Weight(Package containing)	TBD

Interface Definition



Note:

- A.The pins beginning with T are the relevant pins of the resistive touch screen;
- B.The pins beginning with SD are SD card related pins;
- C.Pins beginning with F are SPI flash related pins;
- D.NC/ starts with relevant pins of capacitive touch screen;
- E.The rest are LCD related pins;
- F.SPI flash is not welded. If you need to use it, please weld it yourself;

Number	Pin	Pin Name
--------	-----	----------

	Name	
1	GND	power ground pin
2	EX_3.3V	Power input pin (connected to 3.3V), not used
3	NC	Undefined, reserved
4	RS	LCD register / data selection control pin (high level: register, low level: data)
5	WR	LCD write control pin
6	RD	LCD read control pin
7~14	DB8 ~ DB15	LCD data bus high 8-bit pin
15	CS	LCD reset control pin(low level enable)
16	F_CS	SPI FLASH chip select control pin(low level enable)
17	REST	LCD reset control pin(low level reset)
18	5V	Power input pin (connected to 5V)
19	LED_A	LCD backlight control pin(it needs to be used according to the selection)
20	NC/INT	There is no definition when connecting the resistance touch screen,Used as interrupt pin when connected to capacitive touch screen
21~28	DB0~DB7	LCD data bus low 8-bit pin
29	T_CLK	Resistance touch screen SPI bus clock pin
30	T_CS	Resistive touch screen chip selection pin (low level enable)
31	T_DIN	Resistance touch screen SPI bus write data pin
32	NC/CRT	There is no definition when connecting the resistance touch screen,Used as reset pin when connected to capacitive touch screen (low level reset)
33	T_DO	Resistance touch screen SPI bus read data pin
34	T_IRQ	Resistance touch screen interrupt detection pin (active at low level)
35	SD_DO	SD card SDIO bus read data pin
36	SD_CLK	SD card SDIO bus clock pin
37	SD_DIN	SD card SDIO bus write data pin
38	SD_CS	SD card pin selection (low level enable)
39	NC/SDA	There is no definition when connecting the resistance touch screen,As IIC bus data pin when connected to capacitive touch screen
40	NC/SCL	There is no definition when connecting the resistance touch screen,Used as IIC bus clock pin when connected to capacitive touch screen

Program Download

- **7.0inch 16bit Parallel-Port Module package** (http://www.lcdwiki.com/res/Program/Parallel_Port/7.0inch/16BIT_SSD1963_MRB7963_V1.0/7.0inch_16BIT_Module_SSD1963_800x480_MRB7)

[963_V1.0.zip](#))

Product Documentation

- **7.0inch 16bit Parallel-Port Module User Manual** ([http://www.lcdwiki.com/res/MRB7963/User_Manual\(Required_view\).pdf](http://www.lcdwiki.com/res/MRB7963/User_Manual(Required_view).pdf))
- **7.0 inch LCD specification** (http://www.lcdwiki.com/res/MRB7963/070TFT-EWV1_BOE_50PIN_800X480.pdf)
- **7.0inch 16bit Parallel-Port Module size picture** (http://www.lcdwiki.com/res/MRB7963/7inch_SSD1963_display_Size.pdf)
- **7.0inch 16bit Parallel-Port Module Schematic** (http://www.lcdwiki.com/res/MRB7963/7inch_SSD1963_display_Schematic.pdf)
- **SSD1963 data sheet** (http://www.lcdwiki.com/res/MRB7963/SSD1963_DS_V1.1.pdf)
- **Resistance touch screen control IC data manual xpt2046** (<http://www.lcdwiki.com/res/MRB7963/XPT2046.pdf>)

Reference Materials

- **C51 Keil and stc-isp software use illustration** (http://www.lcdwiki.com/res/PublicFile/C51_Keil_I%26stc-isp_Use_Illustration_EN.pdf)
- **STM32 keil software use illustration** (http://www.lcdwiki.com/res/PublicFile/STM32_Keil_Use_Illustration_EN.pdf)
- **PCtoLCD2002 software use illustration** (http://www.lcdwiki.com/res/PublicFile/PCtoLCD2002_Use_Illustration_EN.pdf)
- **Image2Lcd software use illustration** (http://www.lcdwiki.com/res/PublicFile/Image2Lcd_Use_Illustration_EN.pdf)
- **Chinese and English display modulo settings** (http://www.lcdwiki.com/Chinese_and_English_display_modulo_settings)

Common Software

- **PCtoLCD2002** (<http://www.lcdwiki.com/res/software/PCtoLCD2002.zip>)
- **Image2Lcd** (<http://www.lcdwiki.com/res/software/Image2Lcd.zip>)

[BACK TO TOP](#)

Retrieved from "http://www.lcdwiki.com/index.php?title=7.0inch_16BIT_Module_SSD1963&oldid=4110"

Last modified

- [3 years ago](#)