Blue-Pill Oxpecker: Enabling Transaction Based Writing for VMI Technical Details

I. LIBVMI APIS

The main read/write APIs of LibVMI are *vmi_read_X* and *vmi_write_X* functions. These APIs get virtual/physical addresses alongside with the context that they must be evaluated in, before finding the appropriate host memory address. LibVMI can execute operation on kernel memory by using the zero as the process id. Table I lists the typical usage and description of important LibVMI read/write APIs.

TABLE I: LibVMI read/write APIs. The *X* character represents different bit size numbers in the function name. For example, $vmi_read_X_va$ represents $vmi_read_4_va$, $vmi_read_8_va$, etc.

API Name	Description	Example
vmi_read_X	Read X bits from given (physical or virtual) address based on context and place output in buffer.	<pre>access_context_t ctx = { .translate_mechanism = VMI_TM_PROCESS_PID, .addr = vaddr, .pid = pid }; vmi_read_32(vmi, &ctx, ret_val);</pre>
vmi_read_addr	Read from (64 or 32 bits) address based on system and put the result in buffer.	<pre>vmi_read_addr(vmi, &ctx, ret_val);</pre>
vmi_read_str	Read from address as a string.	<pre>vmi_read_str(vmi, &ctx);</pre>
vmi_read_X_va	Read from virtual ad- dress and return the re- sult in buffer.	<pre>vmi_read_32_va(vmi, vaddr, pid, ret_val);</pre>
vmi_read_X_pa	Read from physical ad- dress and return the re- sult in buffer.	<pre>vmi_read_32_pa(vmi, paddr, ret_val);</pre>
vmi_write_X	Same as <i>vmi_read_X</i> but for writing.	<pre>vmi_write_32_pa(vmi, paddr value);</pre>

II. SYSCALL INTERCEPTION USING NITRO FRAMEWORK

Nitro employs different approaches for intercepting *iret*, *sysexit*, and *sysret* assembly instructions. To intercept interrupt-based syscalls (i.e. "int 0x80"), it sets the *Interrupt Descriptor Table Register (IDTR)* which indicates size of Interrupt Descriptor Table (IDT) to 255. Therefore, all higher interrupt numbers will raise a *general protection fault* which can be caught in VMM. For sysret instruction, the SCE flag in the Extended Feature Enable Register (EFER) is cleared in order to raise an *invalid opcode exception* upon execution of sysret instructions. When an invalid opcode exception is caught by hypervisor, it detects execution of *sysret* (and similarly *syscall*) instruction by looking at the corresponding

instruction that has raised the exception. Finally, *sysexit* is intercepted similar to *sysret*, but by changing *CS* register to *NULL* value which raises a *general protection fault* exception during the execution of *sysexit/sysenter* instructions. All these scenarios lead to *VM_EXIT* events at hypervisor.

Nitro allows a userspace process to configure hypervisor through a series of *ioctl* calls. Following four *ioctl* calls provide the main API for intercepting syscalls.

- KVM_NITRO_ATTACH_VCPUS: used to attach to a virtual CPU,
- 2) KVM_NITRO_SYSCALL_TRAP: used for setting syscall traps for a virtual CPU in GVM,
- 3) KVM_NITRO_GET_EVENT: used for registering a userspace listener for GVM events,
- 4) KVM_NITRO_GET_CONTINUE: used for resuming the execution of GVM.

III. KILL EXAMPLE

Fig. 1 shows an example code using the Oxpecker to kill a guest process. More details are available in the Oxpecker Github repository [1].

```
1 import libvirt, asyncio
2 from nitro.nitro import Nitro
3 from ox_api import Oxpecker
5 def main(args):
    vm_name = args['<vm_name>']
    con = libvirt.open('qemu:///system')
    domain = con.lookupByName(vm_name)
    nitro = Nitro(domain, True)
11
    ox = Oxpecker(nitro)
    pid = 619
    def callback():
14
      kill = Kill(ox)
      kill.do_exit(pid)
    loop = asyncio.get_event_loop()
18
    loop.run_until_complete(
19
      asyncio.wait for (
        ox.beginTransaction(callback, loop), timeout=3
21
    loop.close()
    ox.cancelTransaction()
```

Fig. 1: An example of using Oxpecker APIs for killing a guest process.

IV. LOCK MODULE

The source code of the sample lock module which was used for evaluation of write consistency is depicted in Fig. 2. This module is used to demonstrate the LibVMI inconsistency.

```
int init_module(void)
2 {
    unsigned long j0, j1;
    int delay = 1*HZ;
    uint32_t dining_spoon = 30;
    uint32_t crash = 0;
    printk(KERN_INFO "The address is %p\n", &dining_spoon);
    while(!fatal_signal_pending(current))
10
      j0 = jiffies;
      j1 = j0 + delay;
11
      spin_lock(&my_lock);
12
13
      dining_spoon = 30;
      while (time_before(jiffies, j1))
14
15
       schedule();
16
      if(dining_spoon != 30)
       crash += 1;
18
      spin_unlock(&my_lock);
19
      msleep_interruptible(1000);
      printk(KERN_INFO "Total crash number %d\n", crash);
20
      printk(KERN_INFO "The case is %d\n", dining_spoon);
21
22
23
    return 0;
24 }
```

Fig. 2: The lock module which is used to create race conditions with concurrent Oxpecker transactions. This kernel module waits for a second with msleep_interruptible and busy waits while holding the my_lock for one second. Oxpecker is expected to limit its writes to the period that my_lock is released.