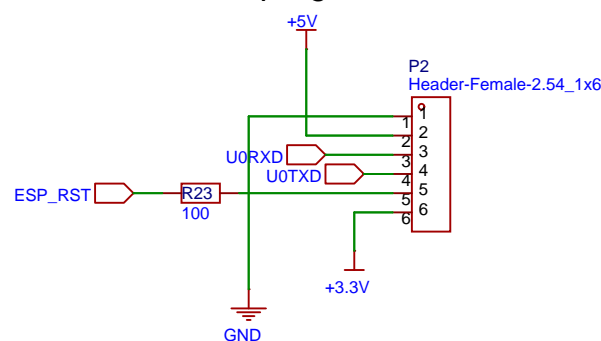
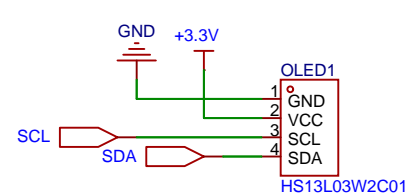


External TTL programmer

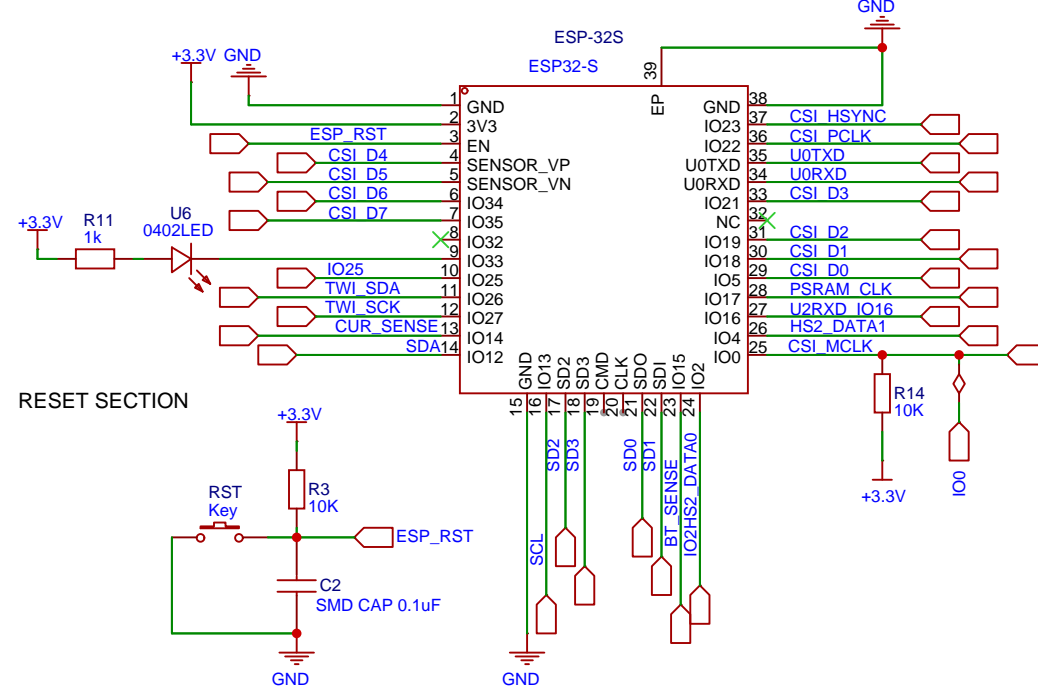
Intergrated TTL programmer



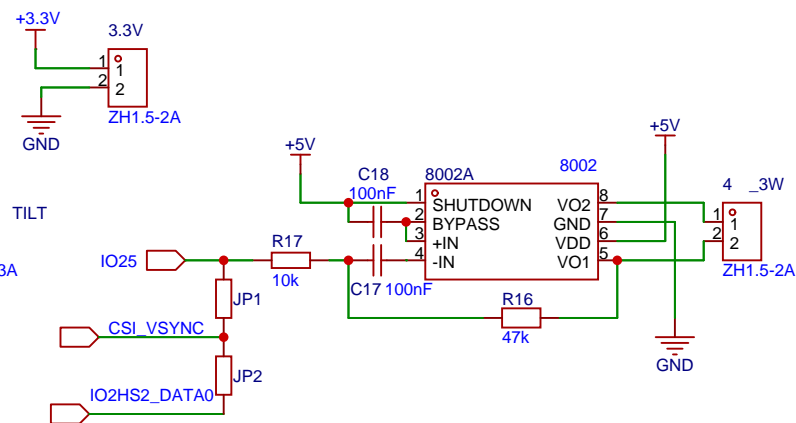
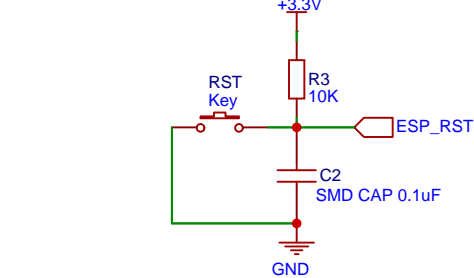
OLED 1.3' 1306



ESP32-S

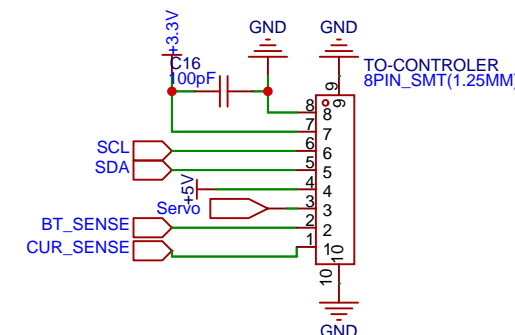
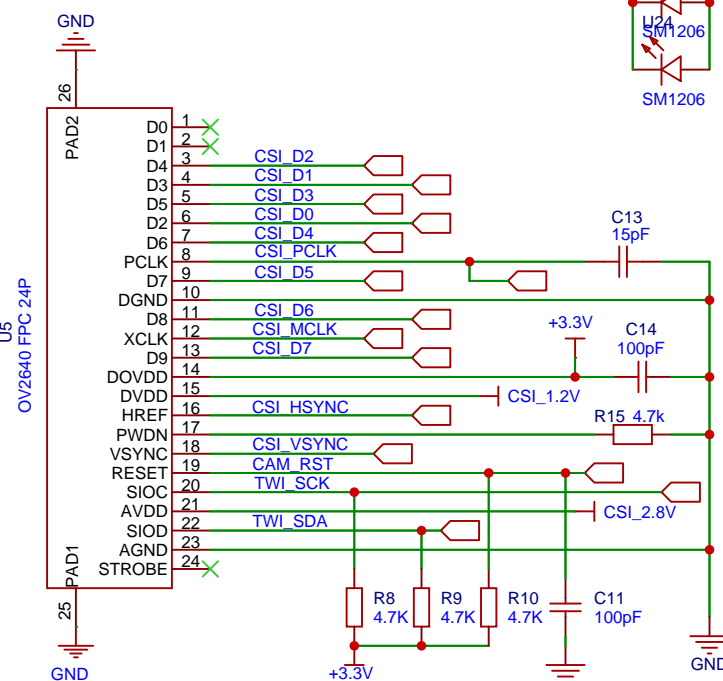


RESET SECTION



If IO2 works as CSI Vsync, connect JP2
IF not , connect JP1 and don't connect R17 and JP2

OV2640 ESP32 CAM




Schematic

Upper_ESP32

Update Date 2024-12-25

Create Date 2024-12-10

Part Number PCB002

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