

					Calendar Week															
Task	Target FB (h)	Target LJ (h)	Actual FB (h)	Actual LJ (h)	38	39	40	41	42	43	44	45	46	47	48	49	50	51		
Project start																				
Problem analysis and concept discussion	8	8	6	6	<div></div>	<div></div>	<div></div>													
Administration and project planning	5	5	5	5	<div></div>	<div></div>	<div></div>													
Rapid prototyping for concept validation	10		8			<div></div>	<div></div>													
Hardware																				
Detail block diagram and core component evaluation		5		3			<div></div>	<div></div>	<div></div>											
Schematic design, bill of material (BOM) and Component ordering	12	15	12	10			<div></div>	<div></div>	<div></div>											
Mechanical design (e.g. Case evaluation)	5		7					<div></div>	<div></div>											
PCB Layout	12	12	12	12					<div></div>	<div></div>										
Review and production preparation	5	5	3	2					<div></div>	<div></div>			<div></div>	<div></div>						
Assembly and testing	5	5	15	15							<div></div>	<div></div>	<div></div>							
Firmware					MS1															
Familiarization with IDE, programmer, RTOS, chip periphery	10		4				<div></div>	<div></div>												
Concept design	5	5	5	5						<div></div>										
Implementation of file system	15		25							<div></div>	<div></div>	<div></div>	<div></div>							
Implementation of USB-Interface	20		20							<div></div>	<div></div>	<div></div>	<div></div>							
Implementation of CAN-Interface		5		10								<div></div>	<div></div>							
Implementation of LAN/WLAN-Interface		30		45						<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>					
Implementation of IMU		5		5										<div></div>	<div></div>					
Configuration file loading and parsing	15		12									<div></div>	<div></div>	<div></div>						
FMS-Package parser and data packing		30		20								<div></div>	<div></div>	<div></div>	<div></div>					
Integration of modules and main application	25	20	25	25									<div></div>	<div></div>	<div></div>	<div></div>				
Backend Software					MS2															
Implementation of network interface		10		10						<div></div>	<div></div>	<div></div>								
Data fetching, processing and storing		15		8									<div></div>	<div></div>						
Implementation of file serving for device	5		2									<div></div>	<div></div>							
Graphical Visualizer	10		10										<div></div>	<div></div>	<div></div>					
Configuration file creator	8		17									<div></div>	<div></div>							
System testing																				
Use Case 1	8	8	2	2												<div></div>	<div></div>			
Use Case 2	5	5	2	2												<div></div>	<div></div>			
Use Case 3	5	5	2	2												<div></div>	<div></div>			
Documentation					MS3															
Requirements Specification / Report	40	40	45	45		<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>	<div></div>		
Abstract / Poster / Elevator speech (video)	7	7	10	10												<div></div>	<div></div>	<div></div>		
Total (h):	240	240	249	242															MS4	MS5

Florian Baumgartner Target

Florian Baumgartner Actual

Luca Jost Target

Luca Jost Actual

Both Target

Both Actual

Milestone

Description

MS1

Hardware design finished and production start

MS2

Firmware key-requirements implemented

MS3

System integration tests validated

MS4

Submission date for abstract: 10.12.2021

MS5

Submission date for thesis and video: 23.12.2021