Task	Target FB (h)	Target LJ (h)	Actual FB (h)	Actual LJ (h)	Calendar Week													
					38	39	40	41	42	43	44	45	46	47	48	49	50	51
Project start																		
Problem analysis and concept discussion	8	8	6	6														
Administration and project planning	5	5	5	5														
Rapid prototyping for concept validation	10																	
Hardware																		
Detail block diagram and core component evaluation		5																
Schematic design, bill of material (BOM) and **Component ordering**	12	15																
Mechanical design (e.g. Case evaluation)	5																	
PCB Layout	12	12																
Review and production preparation	5	5																
Assembly and testing	5	5																
Firmware																	MS1	
Familiarization with IDE, programmer, RTOS, chip periphery	10																	
Concept design	5	5																
Implementation of file system	15																	
Implementation of USB-Interface	20																	
Implementation of CAN-Interface		5																
Implementation of LAN/WLAN-Interface		30																
Implementation of IMU		5																
Configuration file loading and parsing	15																	
FMS-Package parser and data packing		30																
Integration of modules and main application	25	20																
Backend Software																	MS2	
Implementation of network interface		10																
Data fetching, processing and storing		15																
Implementation of file serving for device	5																	
Graphical Visualizer	10																	
Configuration file creator	8																	
System testing																		
Use Case 1	8	8																
Use Case 2	5	5																
Use Case 3	5	5																
Documentation																	MS3	
Requirements Specification / Report	40	40	5	5														
Abstract / Poster / Elevator speech (video)	7	7														MS4		MS5
Total (h):	240	240	16	16												MS4		MS5

Florian Baumgartner Target		Milestone	Description
Florian Baumgartner Actual		MS1	Hardware design finished and production start
Luca Jost Target		MS2	Firmware key-requirements implemented
Luca Jost Actual		MS3	System integration tests validated
Both Target		MS4	Submission date for abstract: 10.12.2021
Both Actual		MS5	Submission date for thesis and video: 23.12.2021