						Calendar Week													
Task	Target F	FB (h)	Target LJ (h)	Actual FB (h)	Actual LJ (h)	38	39	40	41	42	43	44	45	46	47	48	49	50	51
Project start																			
Problem analysis and concept discussion	8		8																$\vdash$
Administration and project planning	5		5																
Rapid prototyping for concept validation	10	)																	
Hardware																			
Detail block diagram and core component evaluation			5																
Schematic design, bill of material (BOM) and Component ordering	12		15																
Mechanical design (e.g. Case evaluation)	5																		
PCB Layout	12		12																
Review and production preparation	5		5																
Assembly and testing	5		5																
Firmware										MS1									
Familiarization with IDE, programmer, RTOS, chip periphery	10	1																	
Concept design	5		5																-
Implementation of file system	15																		
Implementation of USB-Interface	20	)																	
Implementation of CAN-Interface			5																_
Implementation of LAN/WLAN-Interface			30																
Implementation of IMU			5																_
Configuration file loading and parsing	15																		
FMS-Package parser and data packing			30																_
Integration of modules and main application	25		20																
Backend Software																MS2			
Implementation of network interface			10																_
Data fetching, processing and storing			15																
Implementation of file serving for device	5																		_
Graphical Visualizer	10																		
Configuration file builder	8																		
System testing																			
Use Case 1	10		10																
Use Case 2	8		8																
Documentation																	- 1	VIS3	
Report	40		40																
Abstract / Poster / Elevator speech (video) / Finishing touches	7		7																
	Total (h): 240	)	240	0	0												MS4		MS5

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Florian Baumgartner Target	
Florian Baumgartner Actual	
Luca Jost Target	
Luca Jost Actual	
Both Target	
Both Actual	

Milestone	Description
MS1	Hardware design finished and production start
MS2 MS3 MS4 MS5	Firmware key-requirements implemented
MS3	System integration tests validated
MS4	Submission date for abstract: 10.12.2021
MS5	Submission date for thesis and video: 23.12.2021