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Truth Table.

A	B	C	D	F	F
0	0	0	0	1	1
0	0	0	1	1	$\overline{D}$
0	0	1	0	1	
0	0	1	1	0	
0	1	0	0	0	0
0	1	0	1	0	
0	1	1	0	1	
0	1	1	1	0	$\overline{D}$
1	0	0	0	0	
1	0	0	1	0	
1	0	1	0	0	D
1	0	1	1	1	
1	1	0	0	0	
1	1	0	1	0	0
1	1	1	0	0	
1	1	1	1	1	

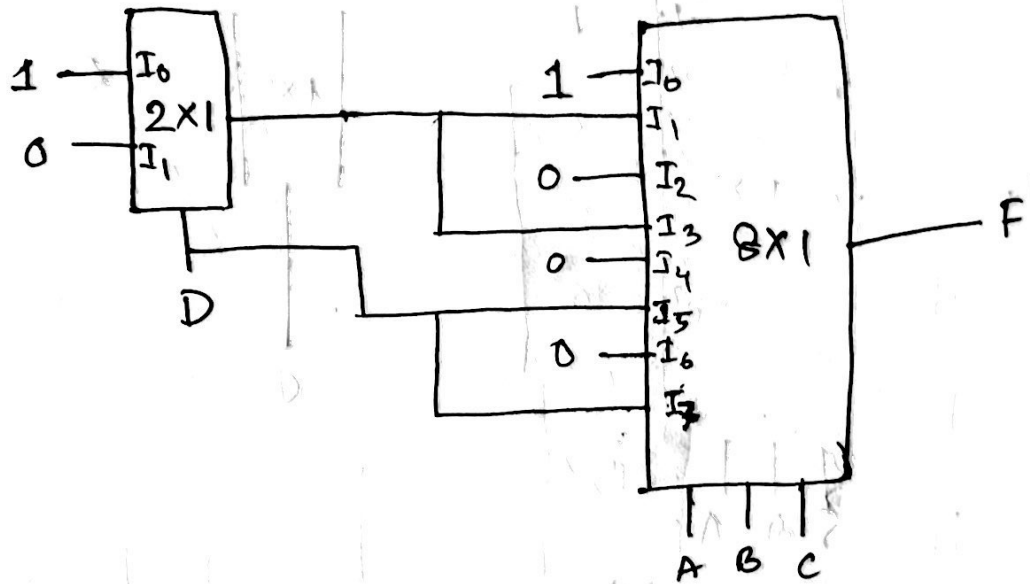


Fig Circuit Diagram