B.Tech - Even Sem : Semester in Exam-I
Academic Year:2021-2022
21EC1202 - COMPUTER ORGANIZATION & ARCHITECTURE
Set No: 3

Time:				Max.Marks: 50			
S.NO	Answer All Questions	Choice	Options	Marks	CO	CO BTL	COI BTL
1.	What is addressing mode and explain Immediate,direct and indire addressing modes with suitable examples	choice Q-2		4.5Marks	CO1	2	1
2.	Elaborate PUSH and POP operation in stack.			4.5Marks	CO1	2	2
3.	Sketch the Mealy and Moore Machines and give a brief comparison note.	choice Q-4		8Marks	CO1	2	1
4.	Illustrate different transfer of control operations in instruction set			8Marks	CO1	2	2
5.	Implement an ALU with four Arithmetic and Logic operations each. Give a brief description on instruction set of a processor.	choice Q-6		12.5Marks	CO1	2	1
6.	Provide the assembly language using zero, one and two-address instructions for the give expression: Z=(A+B-C+D+E)/5			12.5Marks	CO1	2	2
7.	Explain in brief about Register Organization.	choice Q-8		4.5Marks	CO2	2	1
8.	Elaborate different operations performed by CPU.			4.5Marks	CO2	2	2
9.	Explain the Microprogrammed Control CPU design in detail with a neat sketch and list its merits and demerits.	choice Q-10		8Marks	CO2	2	1
10.	Illustrate the internal structure of CPU with internal buses.			8Marks	CO2	2	2
11.	Compare merits and demerits of Mechine level,Assembly lelvel and High level language programs.	choice Q-12		12.5Marks	CO2	2	1
12.	Explain the design of control unit for the selection of micro-operation fields (F1,F2 and F3) with neat diagram.			12.5Marks	CO2	2	2

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